



**THE DATASHEET OF  
SN74BCT29864BDW**



# SN74BCT29864B 9-BIT BUS TRANSCEIVER WITH 3-STATE OUTPUTS

SCBS010A – NOVEMBER 1988 – REVISED NOVEMBER 1993

- BiCMOS Design Substantially Reduces Standby Current
- Functionally Equivalent to 'ALS29864 and AMD Am29864A
- Power-Up High-Impedance State
- ESD Protection Exceeds 2000 V Per MIL-STD-883C, Method 3015
- Package Options Include Plastic Small-Outline (DW) Packages and Standard Plastic 300-mil DIPs (NT)

## description

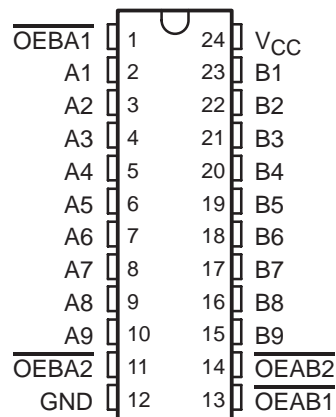
This 9-bit transceiver is designed for asynchronous communication between data buses. The control-function implementation allows for maximum flexibility in timing.

The device allows data transmission from the A bus to the B bus or from the B bus to the A bus, depending upon the logic levels at the output-enable ( $\overline{OEBA}$  and  $\overline{OEAB}$ ) inputs.

The outputs are in the high-impedance state during power-up and power-down conditions. The outputs remain in the high-impedance state while the device is powered-down.

The SN74BCT29864B is characterized for operation from 0°C to 70°C.

DW OR NT PACKAGE  
(TOP VIEW)



FUNCTION TABLE

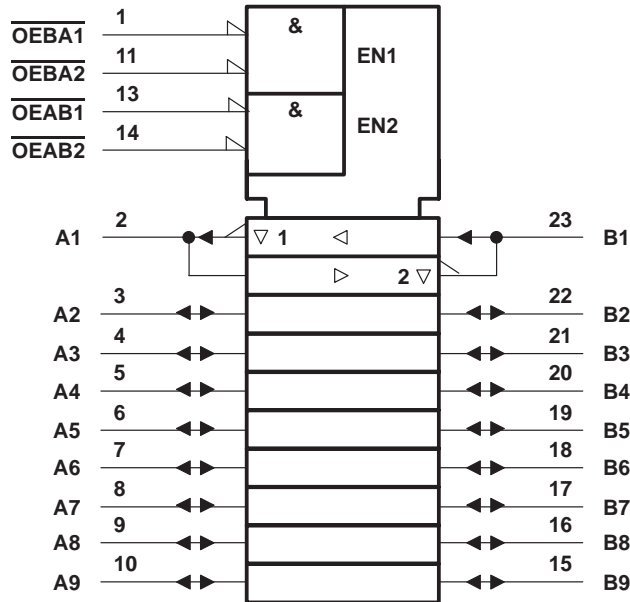
INPUTS				OPERATION
$\overline{OEAB1}$	$\overline{OEAB2}$	$\overline{OEBA1}$	$\overline{OEBA2}$	
L	L	L	L	Latch A and B
L	L	H	X	$\overline{A}$ to B
L	L	X	H	$\overline{B}$ to A
H	X	L	L	$\overline{B}$ to A
X	H	L	L	$\overline{B}$ to A
H	X	H	X	Isolation
H	X	X	H	
X	H	X	H	
X	H	H	X	

# SN74BCT29864B

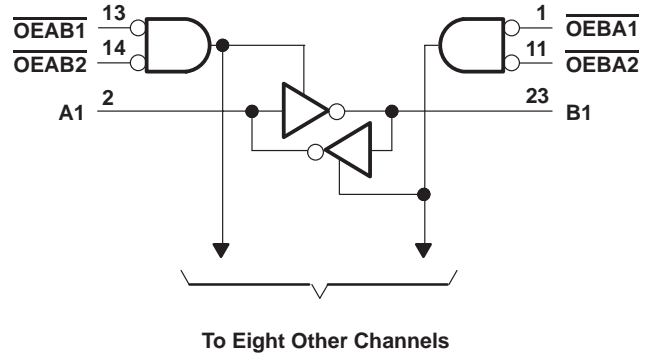
## 9-BIT BUS TRANSCEIVER WITH 3-STATE OUTPUTS

SCBS010A – NOVEMBER 1988 – REVISED NOVEMBER 1993

### logic symbol†



### logic diagram (positive logic)



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, $V_{CC}$	–0.5 V to 7 V
Input voltage range (I/O ports) (see Note 1)	–0.5 V to 5.5 V
Input voltage range (excluding I/O ports) (see Note 1)	–0.5 V to 7 V
Voltage range applied to any output in the high state	–0.5 V to $V_{CC}$
Input clamp current	–30 mA
Current into any output in the low state	96 mA
Operating free-air temperature range	0°C to 70°C
Storage temperature range	–65°C to 150°C

‡ Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The negative voltage rating may be exceeded if the input current rating is observed.

### recommended operating conditions

		MIN	NOM	MAX	UNIT
$V_{CC}$	Supply voltage	4.5	5	5.5	V
$V_{IH}$	High-level input voltage	2			V
$V_{IL}$	Low-level input voltage			0.8	V
$I_{IK}$	Input clamp current			–18	mA
$I_{OH}$	High-level output current			–24	mA
$I_{OL}$	Low-level output current			48	mA
$T_A$	Operating free-air temperature	0		70	°C



# SN74BCT29864B

## 9-BIT BUS TRANSCEIVER WITH 3-STATE OUTPUTS

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP†	MAX	UNIT
V <sub>IK</sub>		V <sub>CC</sub> = 4.5 V, I <sub>I</sub> = -18 mA				-1.2	V
V <sub>OH</sub>		V <sub>CC</sub> = 4.5 V	I <sub>OH</sub> = -15 mA	2.4	3.3		V
			I <sub>OH</sub> = -24 mA	2	3.1		
		V <sub>CC</sub> = 4.75 V,	I <sub>OH</sub> = -3 mA	2.7			
V <sub>OL</sub>		V <sub>CC</sub> = 4.5 V,	I <sub>OL</sub> = 48 mA		0.35	0.5	V
I <sub>I</sub>		V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 5.5 V			0.1	mA
I <sub>IH</sub>	Control inputs	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 2.7 V			20	μA
	A or B port‡					20	
I <sub>IL</sub>	Control inputs	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 0.5 V			-0.2	mA
	A or B port‡					-0.2	
I <sub>IO(off)</sub> §		V <sub>CC</sub> = 0,	V <sub>O</sub> = 2.7 V			0.1	mA
I <sub>OS</sub> ¶		V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 0	-75		-250	mA
I <sub>CC</sub>		V <sub>CC</sub> = 5.5 V	Outputs high		18	30	mA
			Outputs low		30	45	
			Outputs disabled		6.5	12	
C <sub>i</sub>		V <sub>CC</sub> = 5 V,	V <sub>I</sub> = 0.5 V or 2.5 V		6		pF
C <sub>io</sub>		V <sub>CC</sub> = 5 V,	V <sub>I</sub> = 0.5 V or 2.5 V		8		pF

† All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.

‡ For I/O ports, the parameters I<sub>IH</sub> and I<sub>IL</sub> include the off-state output current.

§ I<sub>O(off)</sub> = Power-off bus leakage current

¶ Not more than one output should be shorted at a time and duration of the short circuit should not exceed one second.

### switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V <sub>CC</sub> = 5 V, C <sub>L</sub> = 50 pF, R <sub>1</sub> = 500 Ω, R <sub>2</sub> = 500 Ω, T <sub>A</sub> = 25°C			V <sub>CC</sub> = 4.5 V to 5.5 V, C <sub>L</sub> = 50 pF, R <sub>1</sub> = 500 Ω, R <sub>2</sub> = 500 Ω, T <sub>A</sub> = MIN to MAX#		UNIT
			MIN	TYP	MAX	MIN	MAX	
t <sub>PLH</sub>	A or B	B or A	1	3.5	5.3	1	6.1	ns
t <sub>PHL</sub>			0.5	2.3	4.6	0.5	4.8	
t <sub>PZH</sub>	OEAB or OEBA	A or B	2.3	5	7.2	2.3	8.4	ns
t <sub>PZL</sub>			4.3	7.3	10.6	4.3	12.5	
t <sub>PHZ</sub>	OEAB or OEBA	A or B	2.3	4.6	7.6	2.3	8.4	ns
t <sub>PLZ</sub>			2	4	7	2	8.2	

# For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.





**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
SN74BCT29864BDW	ACTIVE	SOIC	DW	24	25	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74BCT29864BDWE4	ACTIVE	SOIC	DW	24	25	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74BCT29864BDWG4	ACTIVE	SOIC	DW	24	25	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74BCT29864BDWR	ACTIVE	SOIC	DW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74BCT29864BDWRE4	ACTIVE	SOIC	DW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74BCT29864BDWRG4	ACTIVE	SOIC	DW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74BCT29864BNT	ACTIVE	PDIP	NT	24	15	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74BCT29864BNTE4	ACTIVE	PDIP	NT	24	15	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSELETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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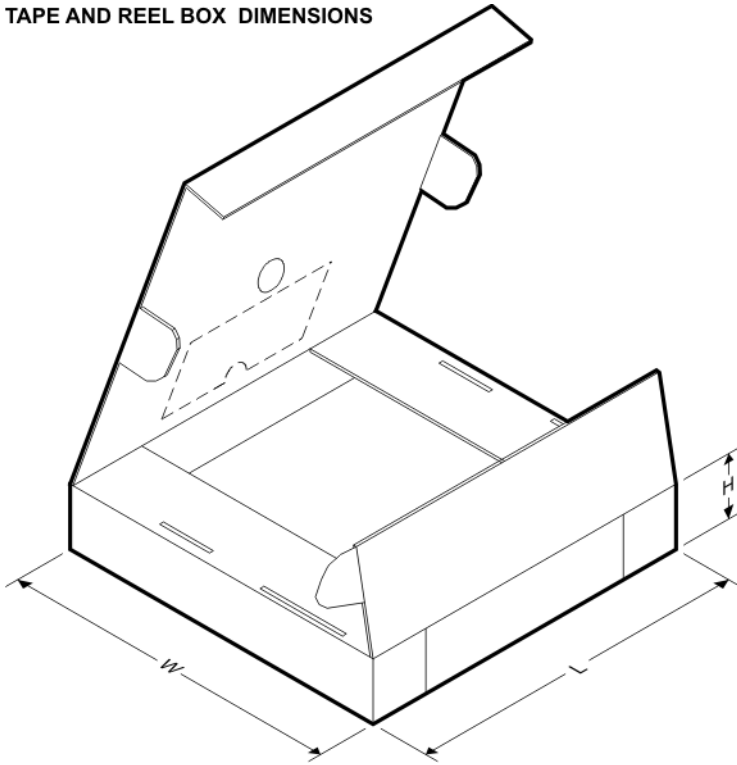
**QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE**



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74BCT29864BDWR	SOIC	DW	24	2000	330.0	24.4	10.75	15.7	2.7	12.0	24.0	Q1

**TAPE AND REEL BOX DIMENSIONS**



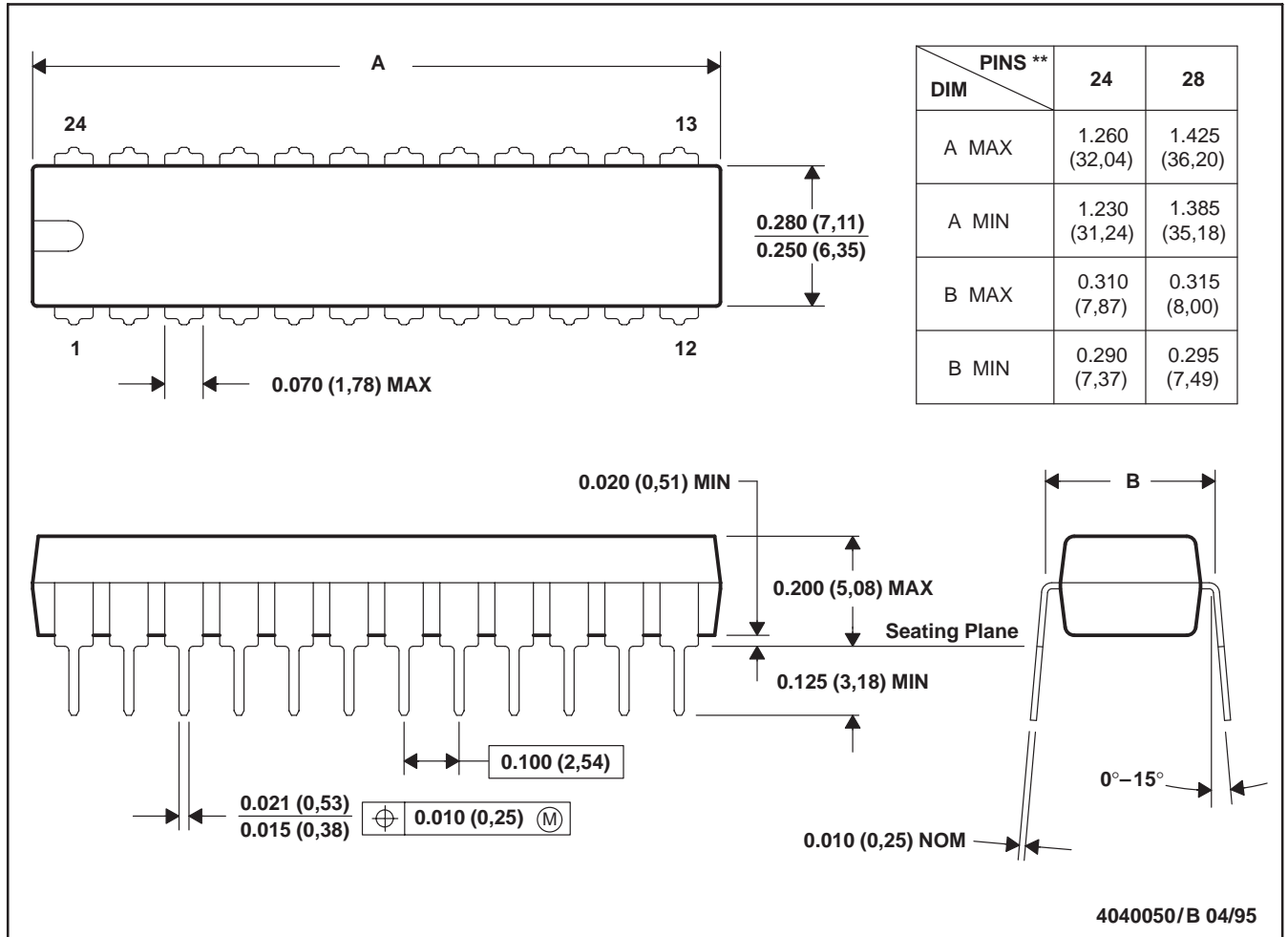
\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74BCT29864BDWR	SOIC	DW	24	2000	346.0	346.0	41.0

NT (R-PDIP-T\*\*)

PLASTIC DUAL-IN-LINE PACKAGE

24 PINS SHOWN



- NOTES: A. All linear dimensions are in inches (millimeters).  
 B. This drawing is subject to change without notice.



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