



**THE DATASHEET OF  
D15V0HA1U2LP-7B**



## Product Summary

<b>V<sub>BR</sub> (Min)</b>	<b>I<sub>PP</sub> (Max)</b>	<b>C<sub>T</sub> (Typ)</b>
16.0V	20A	95pF

## Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD and Surge. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

## Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

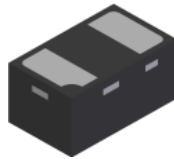
## Features

- Low Profile Package (0.50mm Typical) and Ultra-Small PCB Footprint Area (1.1mm × 0.7mm Max) Suitable for Compact Portable Electronics
- One Channel of ESD and Surge Protection
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: I<sub>PP</sub> max 20A
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: X1-DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (Approximate)

X1-DFN1006-2



Bottom View



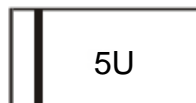
Device Schematic

## Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity
D15V0HA1U2LP-7B	Standard	5U	7	8	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



5U = Product Type Marking Code  
Bar Denotes Pin 1 or Cathode Side

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P <sub>PP</sub>	460	W	8/20μs, per Figure 3
Peak Pulse Current	I <sub>PP</sub>	20	A	8/20μs, per Figure 3
ESD Protection—Contact Discharge	V <sub>ESD_CONTACT</sub>	±30	kV	IEC 61000-4-2 Standard
ESD Protection—Air Discharge	V <sub>ESD_AIR</sub>	±30	kV	IEC 61000-4-2 Standard

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P <sub>D</sub>	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	500	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	V <sub>RWM</sub>	—	—	15.0	V	—
Reverse Current (Note 6)	I <sub>R</sub>	—	0.02	0.1	μA	V <sub>R</sub> = V <sub>RWM</sub>
Reverse Breakdown Voltage	V <sub>BR</sub>	16.0	—	19.5	V	I <sub>R</sub> = 1mA
Reverse Clamping Voltage	V <sub>CL</sub>	—	18	—	V	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20μs
		—	20	—		I <sub>PP</sub> = 10A, t <sub>p</sub> = 8/20μs
		—	23	—		I <sub>PP</sub> = 20A, t <sub>p</sub> = 8/20μs
Capacitance	C <sub>T</sub>	—	95	—	pF	V <sub>R</sub> = 0V, f = 1MHz

- Notes:
- Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
  - Short duration pulse test used to minimize self-heating effect.

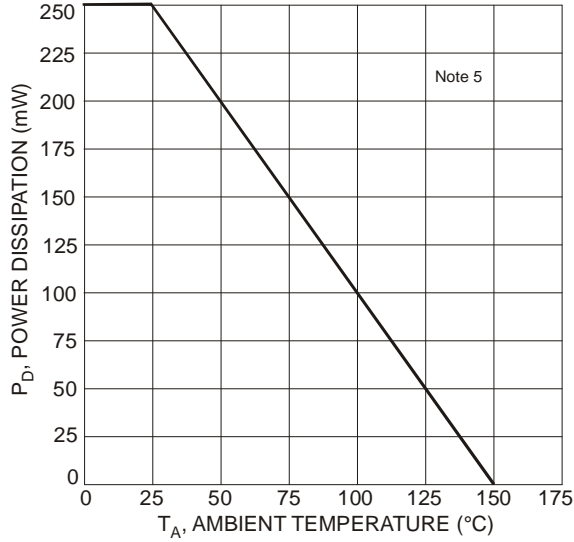


Figure 1 Power Derating Curve

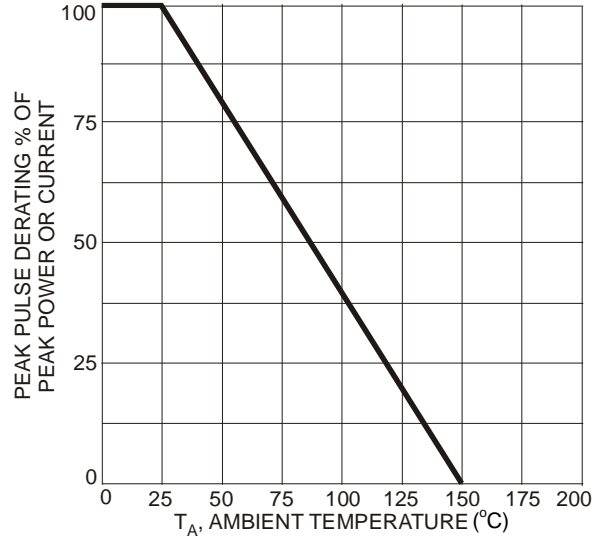


Figure 2 Pulse Derating Curve

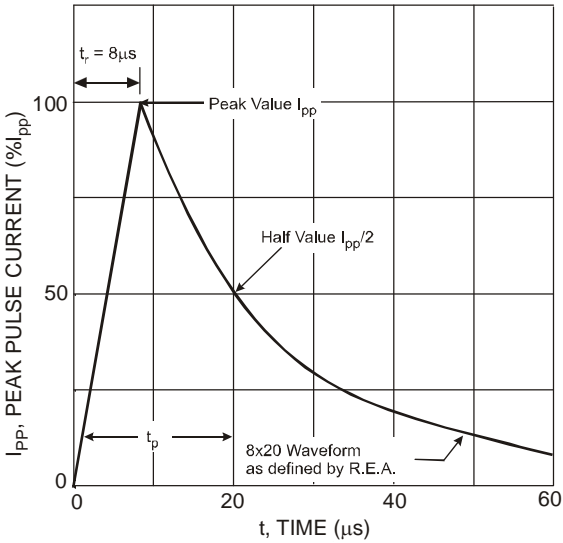


Figure 3 Typical 8 x 20µs Pulse Waveform

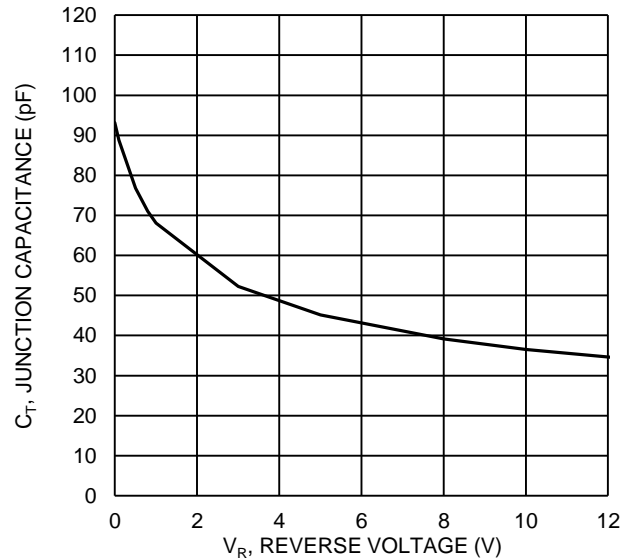


Figure 4 Typical Total Capacitance

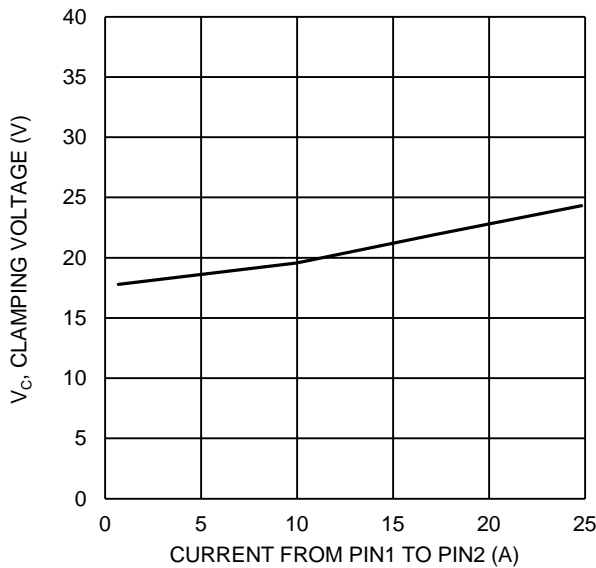


Figure 5 Clamping Voltage Characteristic  
( $t_p = 8/20\mu s$ )

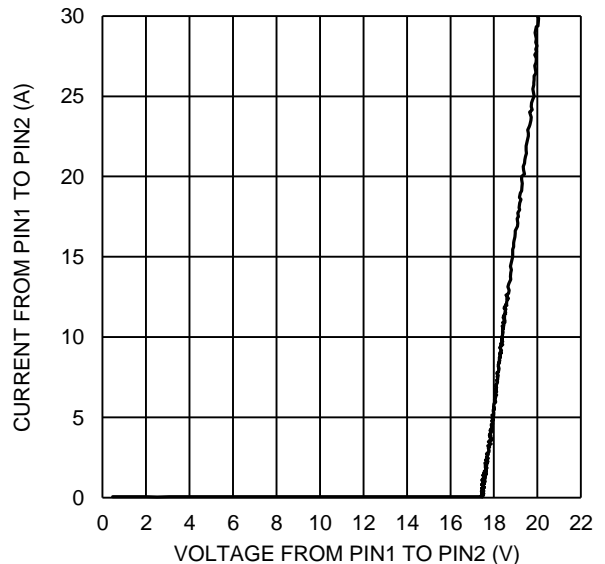


Figure 6 Pin1 to Pin2 TLP Curve ( $t_p = 100ns$ )



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