



THE DATASHEET OF ISL95855CIRTZ

ISL95855C

3+2+1 Voltage Regulator With Expanded Iccmax Register Range Supporting IMVP8 CFL/CNL CPUs

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The [ISL95855C](#) is compliant with IMVP8™, and provides a complete power solution for Intel microprocessors supporting core, graphics, and system agent rails. The controller provides control and protection for three Voltage Regulator (VR) outputs. The VR A output can be configured for 3-, 2-, or 1-phase operation. VR B is configurable for 2- or 1-phase operation, and VR C supports 1-phase operation. The address options for these three outputs allow for maximum flexibility to support the IMVP8 CPU. All three VRs share a common serial control bus to communicate with the CPU and achieve lower cost and smaller board area compared with a two-chip approach. The ISL95855C has expanded Iccmax register tables for VR A and VR C to support Intel CFL-H.

Based on Intersil's Robust Ripple Regulator (R3™) technology, the R3 modulator has many advantages compared to traditional modulators. These include faster transient settling time, variable switching frequency in response to load transients, and improved light-load efficiency due to diode emulation mode with load-dependent low switching frequency.

The ISL95855C has several other key features. The controller provides PWM outputs, which support Intel DrMOS power stages (or similar) and discrete power stages using the Intersil ISL95808 high voltage synchronous rectified buck MOSFET driver. The controller complies with IMVP8 PS4 power requirements and supports compatible power stages and drivers. The controller supports DCR current sensing with a single NTC thermistor for DCR temperature compensation, or more precision through resistor current sensing.

Features

- Supports Intel serial data bus interface
 - Fully supports PS4 power domain entry and exit
- Supports system input power monitor (PSYS)
- Three output controller
 - VR A configurable for 3-, 2-, 1-phase VR design
 - VR B configurable for 2-, 1-phase VR design
 - VR C supports 1-phase VR design
- 0.5% system accuracy over temperature
- Low supply current in PS4 state
- Supports multiple current sensing methods
 - Lossless inductor DCR current sensing
 - Precision resistor current sensing
- Differential remote voltage sensing
- Programmable SVID address
- Programmable V_{BOOT} voltage at start-up
- Resistor programmable address selection, I_{MAX} , slew rate, switching frequency, and droop
- Adaptive body diode conduction time reduction

Applications

- IMVP8 compliant notebooks, desktops, Ultrabooks, and tablets
- Supports higher Iccmax range of CFL-H CPUs

Related Literature

- For a full list of related documents, visit our website
- [ISL95855C](#) product page

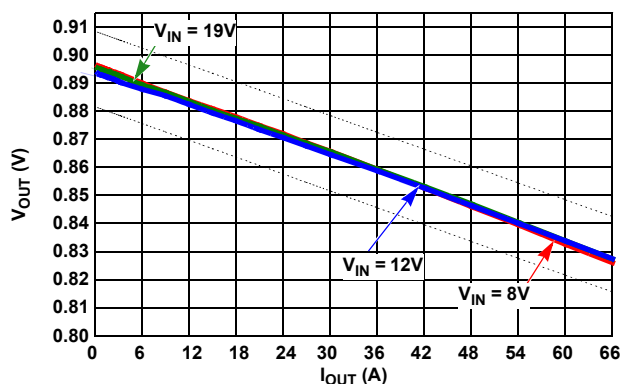


Figure 1. Load Line Regulation

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