



**THE DATASHEET OF  
DMN2112SN-7**



**N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR**
**Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> T <sub>A</sub> = +25°C
20V	0.10Ω @ V <sub>GS</sub> = 4.5V	0.5A
	0.14Ω @ V <sub>GS</sub> = 2.5V	0.5A
	0.25Ω @ V <sub>GS</sub> = 1.5V	0.1A

**Description**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

**Applications**

- Notebook Computer
- Portable Phone
- PCMCIA Cards and Battery Powered Circuits

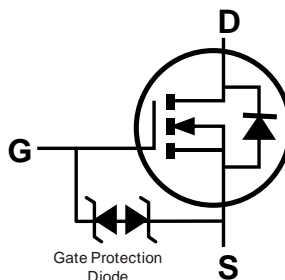


ESD Protected

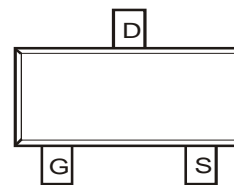
SC59



TOP VIEW



EQUIVALENT CIRCUIT


 TOP VIEW  
Pin Out Configuration

**Features**

- Low On-Resistance
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

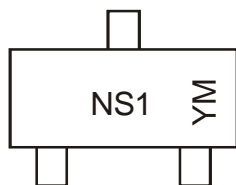
**Mechanical Data**

- Case: SC59
- Case Material - Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.014 grams (Approximate)

**Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN2112SN-7	SC59	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

**Marking Information**


NS1 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: E = 2017)  
 M = Month (ex: 9 = September)

## Date Code Key

Year	2007	...	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	U	...	E	F	G	H	I	J	K	L	M

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	20	V
Gate-Source Voltage	V <sub>GSS</sub>	± 8	V
Drain Current	I <sub>D</sub>	1.2 4.0	A
			Continuous Pulsed

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation	P <sub>d</sub>	500	mW
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	250	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 5)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	10	μA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Body Leakage	I <sub>GSS</sub>	—	—	± 10	μA	V <sub>GS</sub> = ± 8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 5)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	—	1.2	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1.0mA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	—	0.10 0.14 0.25	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 0.5A V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 0.5A V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 0.1A
Forward Transfer Admittance	Y <sub>fs</sub>	—	4.2	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.5A
Diode Forward Voltage	V <sub>SD</sub>	—	0.8	1.1	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
<b>DYNAMIC CHARACTERISTICS (Note 6)</b>						
Input Capacitance	C <sub>iss</sub>	—	220	—	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	120	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	45	—	pF	
<b>SWITCHING CHARACTERISTICS (Note 6)</b>						
Turn-On Delay Time	t <sub>D(ON)</sub>	—	10	—	ns	V <sub>DD</sub> = 5V, I <sub>D</sub> = 0.5A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = 50Ω
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	75	—	ns	
Turn-On Rise Time	t <sub>R</sub>	—	15	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	65	—	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.  
6. Guaranteed by design. Not subject to product testing.

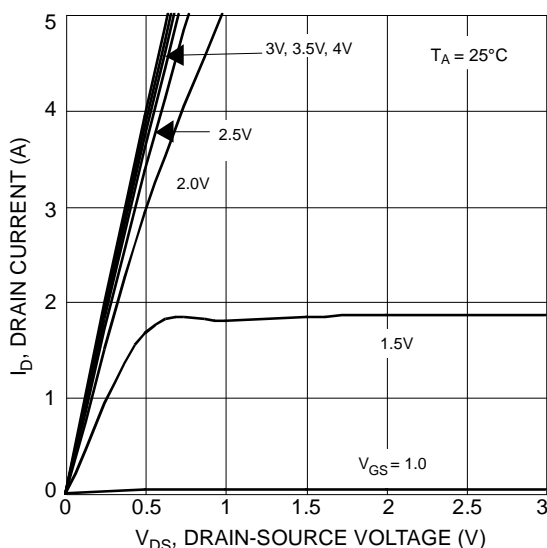


Fig. 1 Typical Output Characteristics

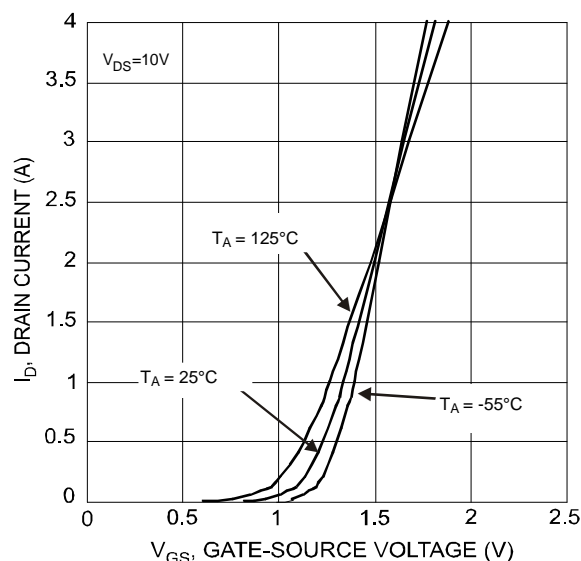


Fig. 2 Typical Transfer Characteristics

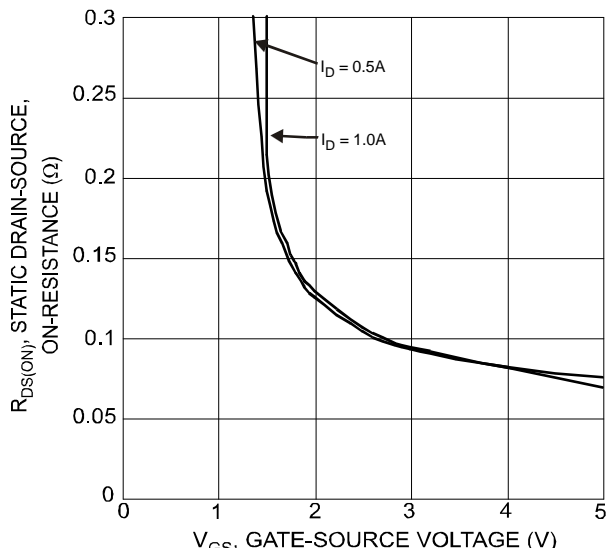


Fig. 3 On-Resistance vs. Gate Voltage

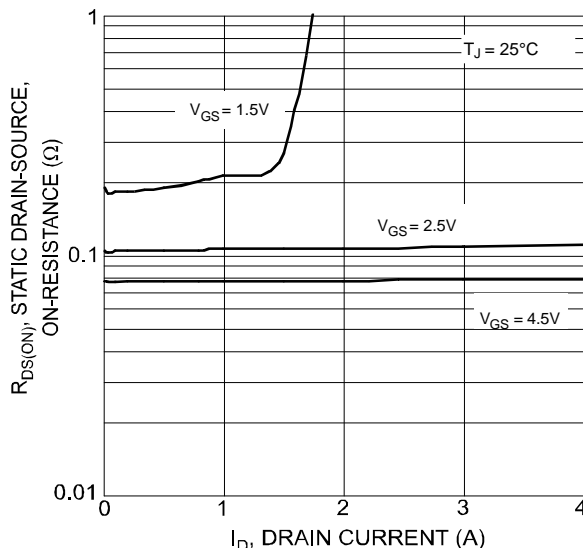


Fig. 4 On-Resistance vs. Drain Current

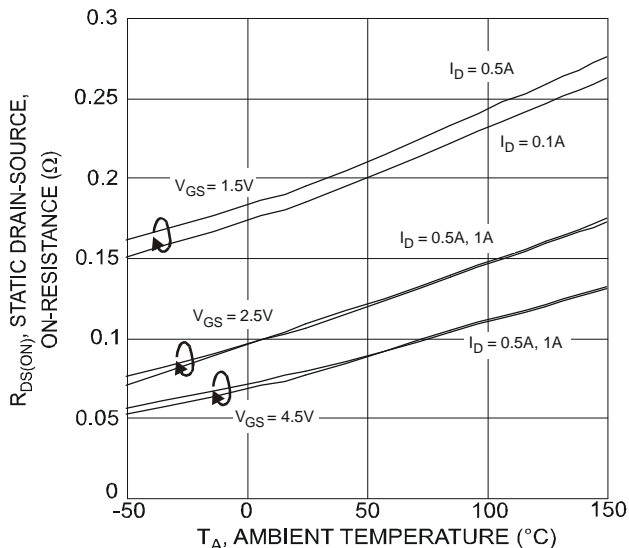


Fig. 5 On-Resistance Variation with Temperature

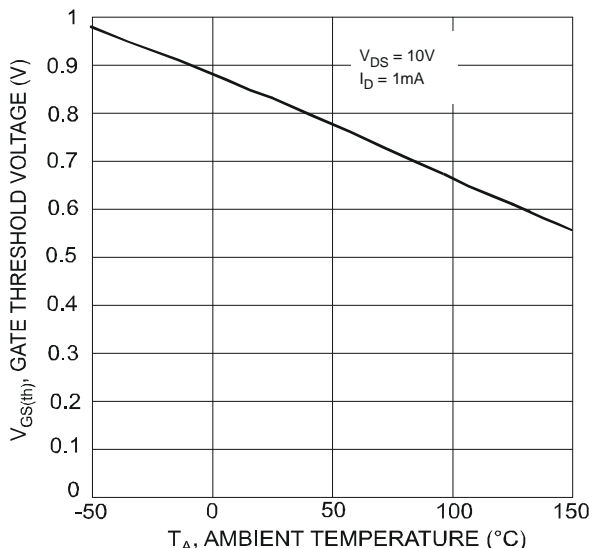


Fig. 6 Gate Threshold Voltage vs. Temperature

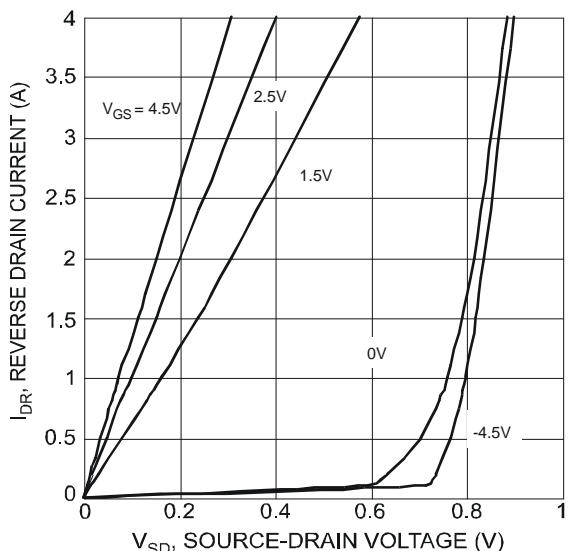


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

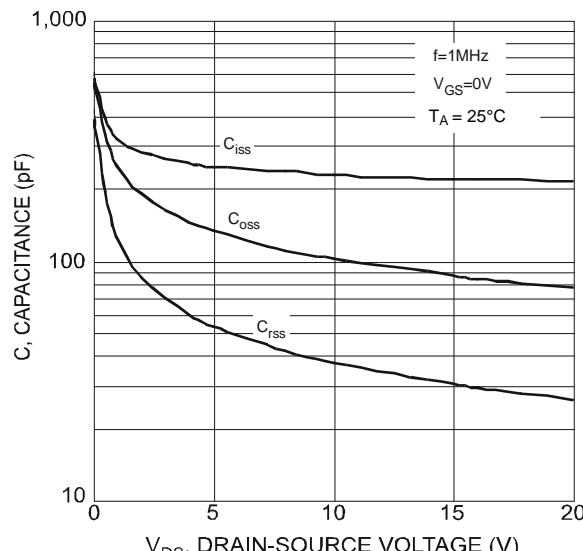
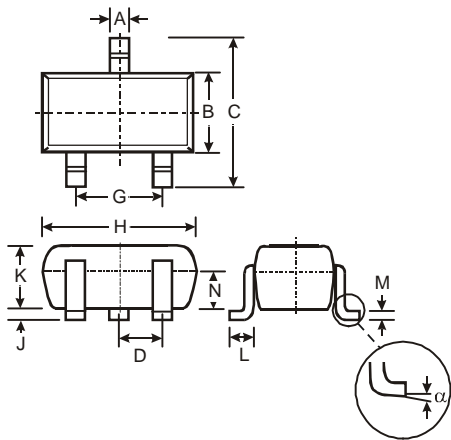


Fig. 8 Typical Junction Capacitance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SC59**

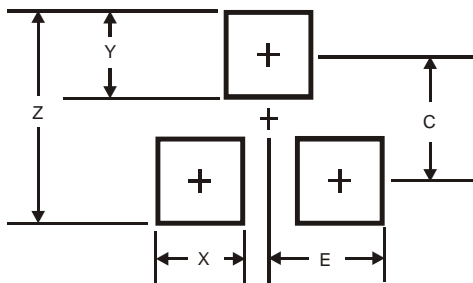


SC59			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
	0°	8°	-
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SC59**



Dimensions	Value (in mm)
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

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