



THE DATASHEET OF PESD3V3T1BLDYL



PESD3V3T1BLD

Bidirectional ESD protection diode

26 April 2024

Product data sheet

1. General description

Bidirectional ElectroStatic Discharge (ESD) protection diode designed to protect one signal line from the damage caused by ESD and other transients.

The device is housed in a SOD882D leadless ultra small Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

2. Features and benefits

- Bidirectional ESD protection of one line
- Ultra small SMD plastic package 1 x 0.6 x 0.37 mm
- Side-wettable flanks
- ESD protection up to 30 kV
- Very high surge robustness; $I_{PP} = 12\text{ A}$ for 8/20 μs ; average measured

3. Applications

- ESD and surge protection for interface lines

4. Quick reference data

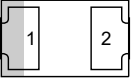
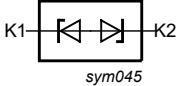
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25\text{ }^{\circ}\text{C}$		-	-	3.3	V
I_{PPM}	rated peak pulse current	$t_p = 8/20\text{ }\mu\text{s}$	[1]	-	-	10	A
V_{CL}	clamping voltage	$I_{PPM} = 10\text{ A}$; $t_p = 8/20\text{ }\mu\text{s}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	[1]	-	9.3	11	V

[1] Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode 1[1]	 Transparent top view DFN1006D-2 (SOD882D)	 <i>sym045</i>
2	K2	cathode 2		

[1] The marking band indicates the cathode

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD3V3T1BLD	DFN1006D-2	leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.4 mm body	SOD882D

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD3V3T1BLD	0110 0100

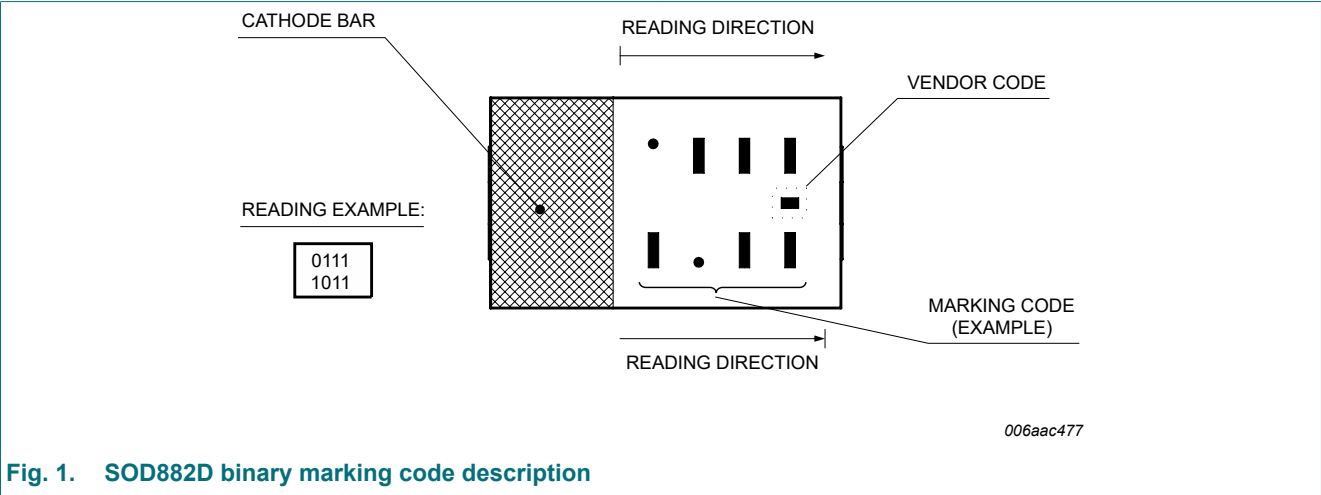


Fig. 1. SOD882D binary marking code description

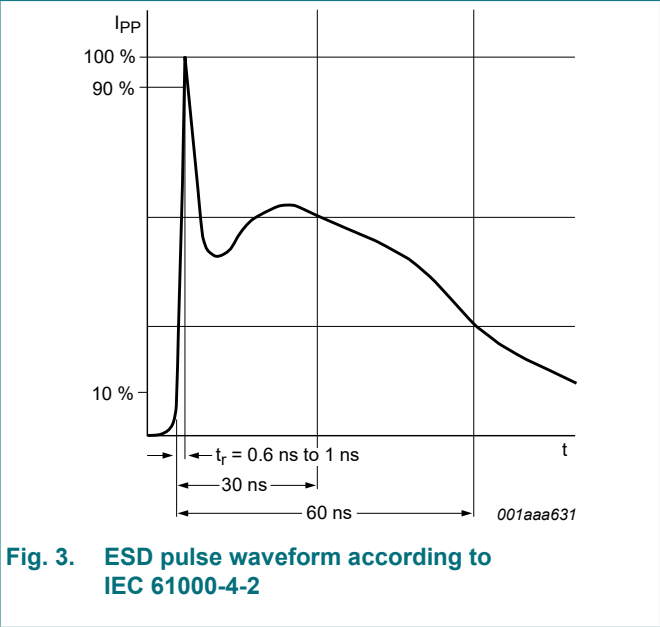
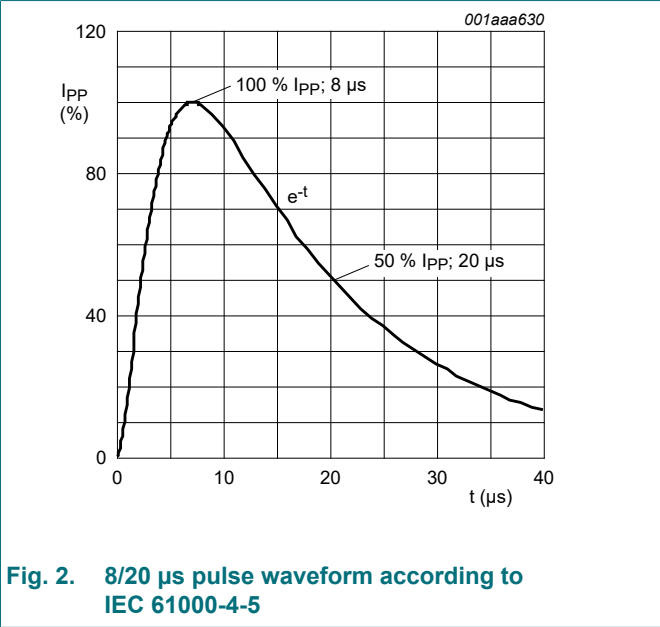
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I_{PPM}	rated peak pulse current	$t_p = 8/20 \mu s$	[1]	-	10	A
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V_{ESD}	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge)	[2]	-	30	kV

- [1] Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC 61000-4-5.
[2] Device stressed with ten non-repetitive ESD pulses.



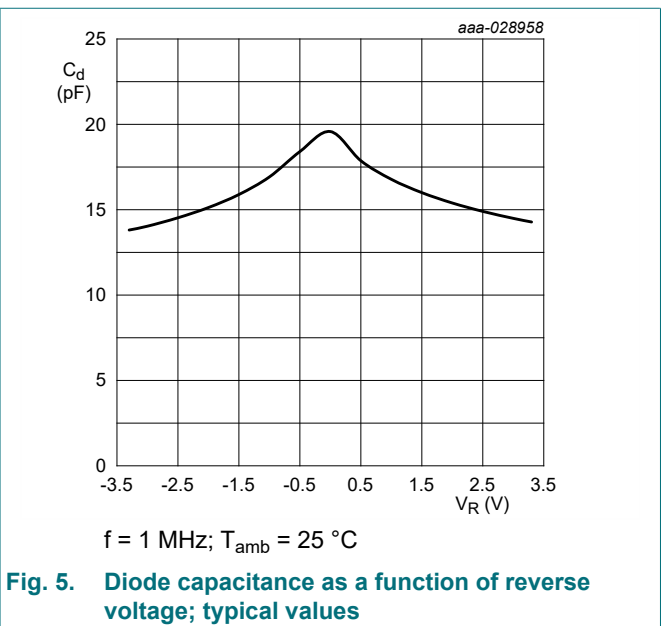
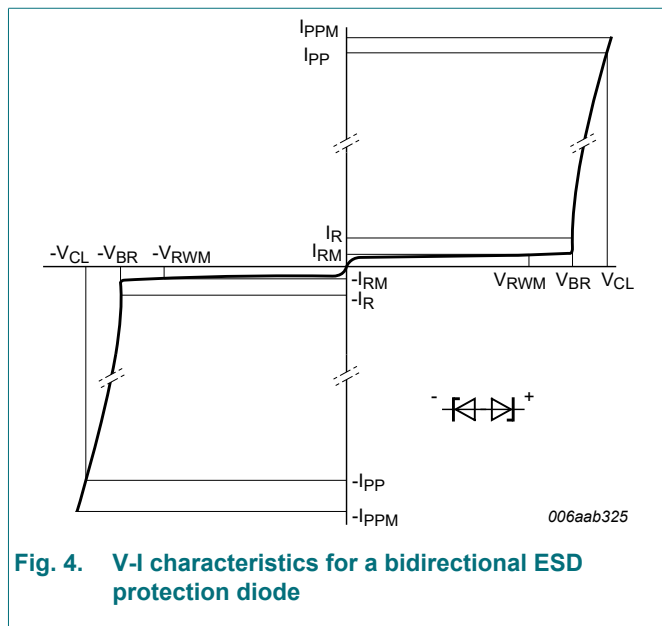
9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3.3	V
V _{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C		4.7	5.5	8.7	V
I _{RM}	reverse leakage current	V _{RWM} = 3.3 V; T _{amb} = 25 °C		-	0.1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	20	25	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1]	-	6.5	-	V
		I _{PPM} = 10 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1]	-	9.3	11	V
		I _{PP} = 16 A; t _p = 100 ns; T _{amb} = 25 °C	[2]	-	9.5	-	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[2]	-	0.12	-	Ω
		I _R = -10 A; T _{amb} = 25 °C	[2]	-	0.21	-	Ω

[1] Non-repetitive current pulse 8/20 μ s exponential decay waveform according to IEC 61000-4-5.

[2] Non-repetitive current pulse, Transmission Line Pulse (TLP) $t_p = 100$ ns; square pulse; ANSI/ESD STM5.5.1-2008.



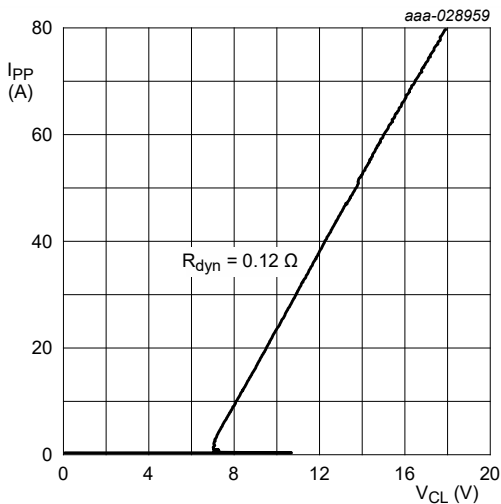


Fig. 6. Dynamic resistance with positive clamping; typical values

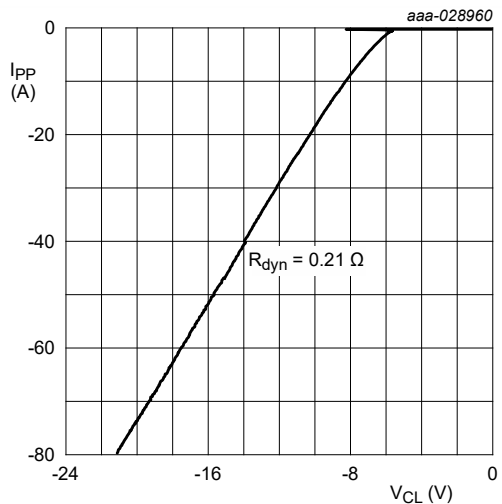


Fig. 7. Dynamic resistance with negative clamping; typical values

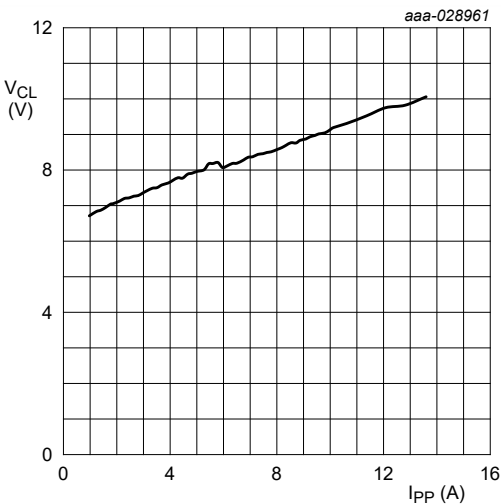


Fig. 8. Positive clamping voltage (8/20 μs pulse); typical values

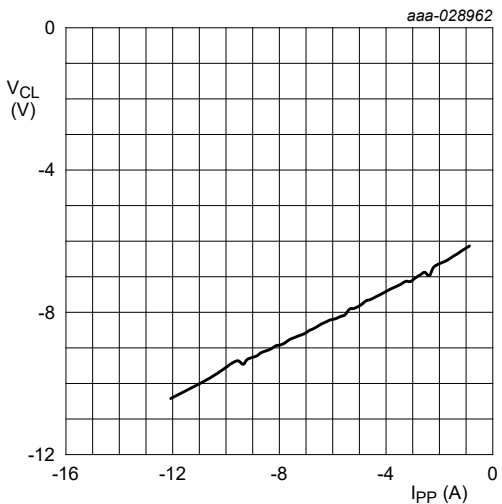


Fig. 9. Negative clamping voltage (8/20 μs pulse); typical values

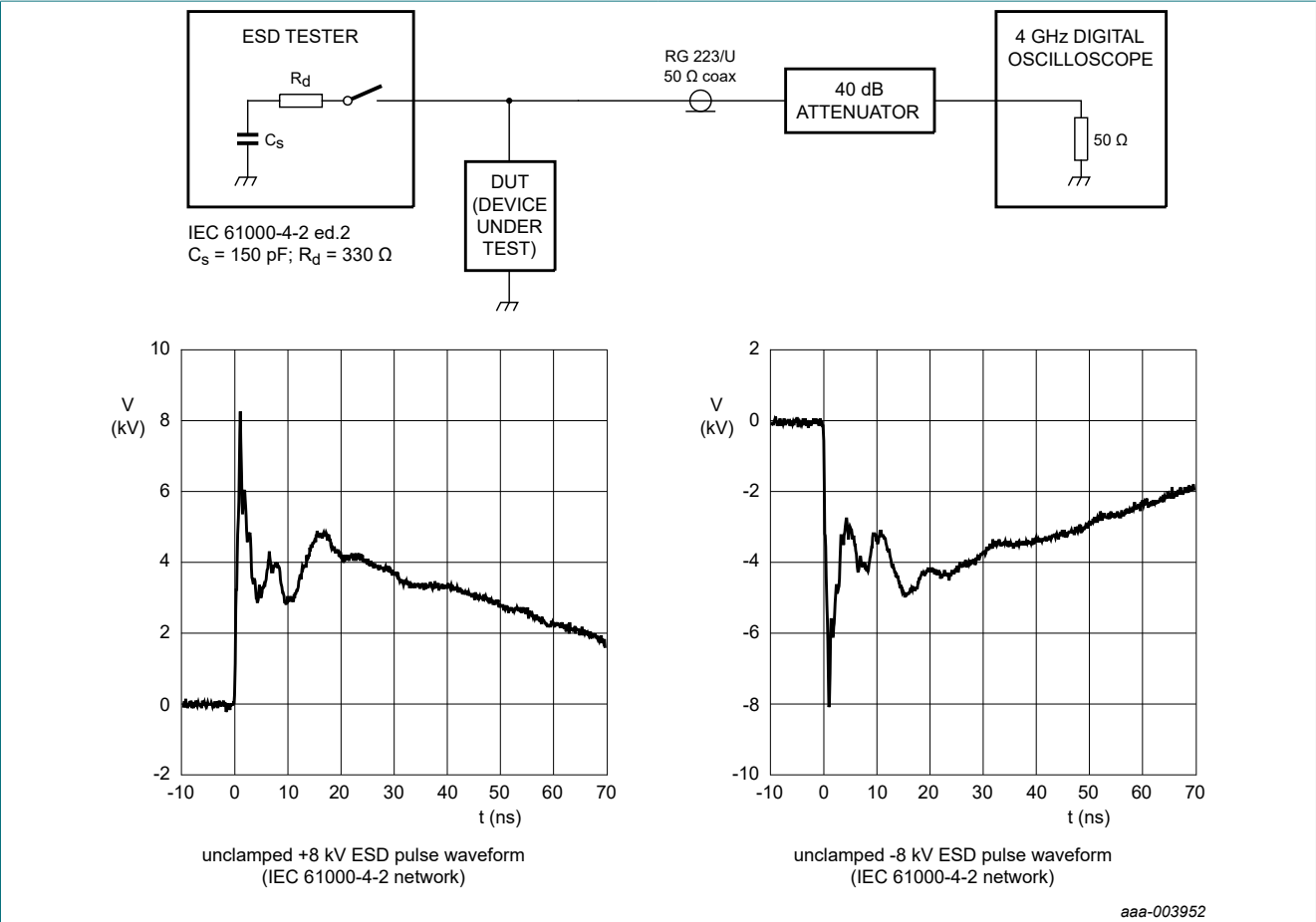


Fig. 10. ESD clamping test setup and waveforms

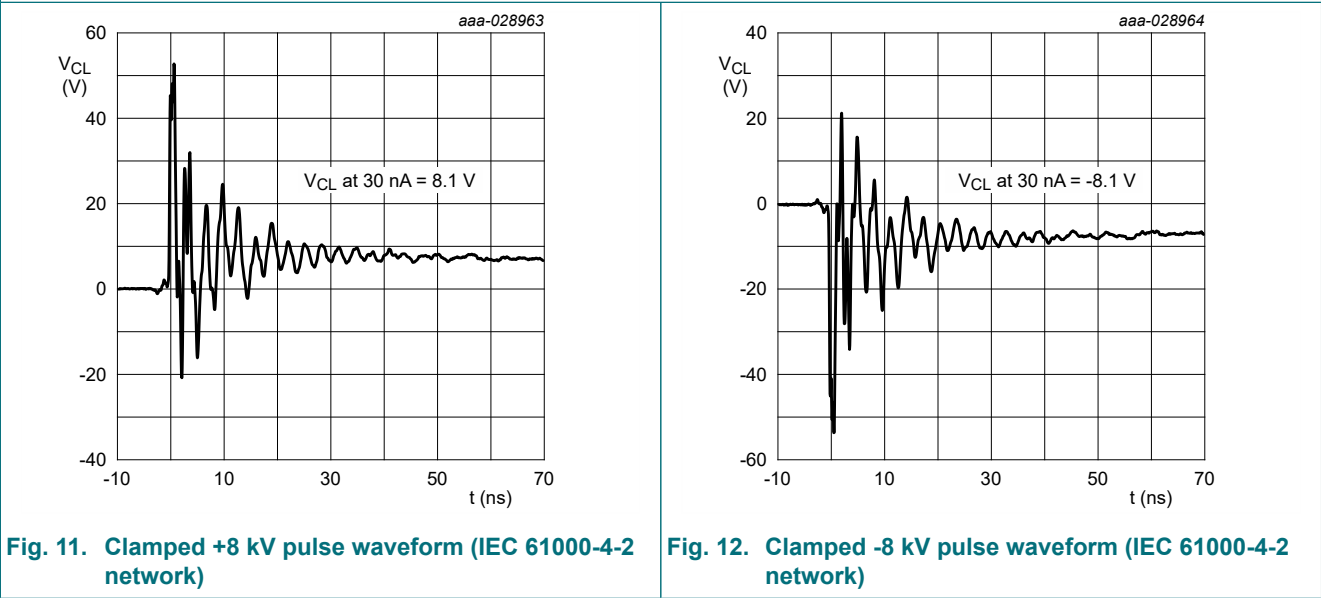


Fig. 11. Clamped +8 kV pulse waveform (IEC 61000-4-2 network)

Fig. 12. Clamped -8 kV pulse waveform (IEC 61000-4-2 network)

10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

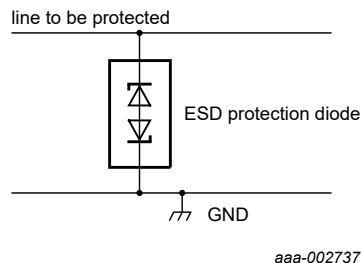


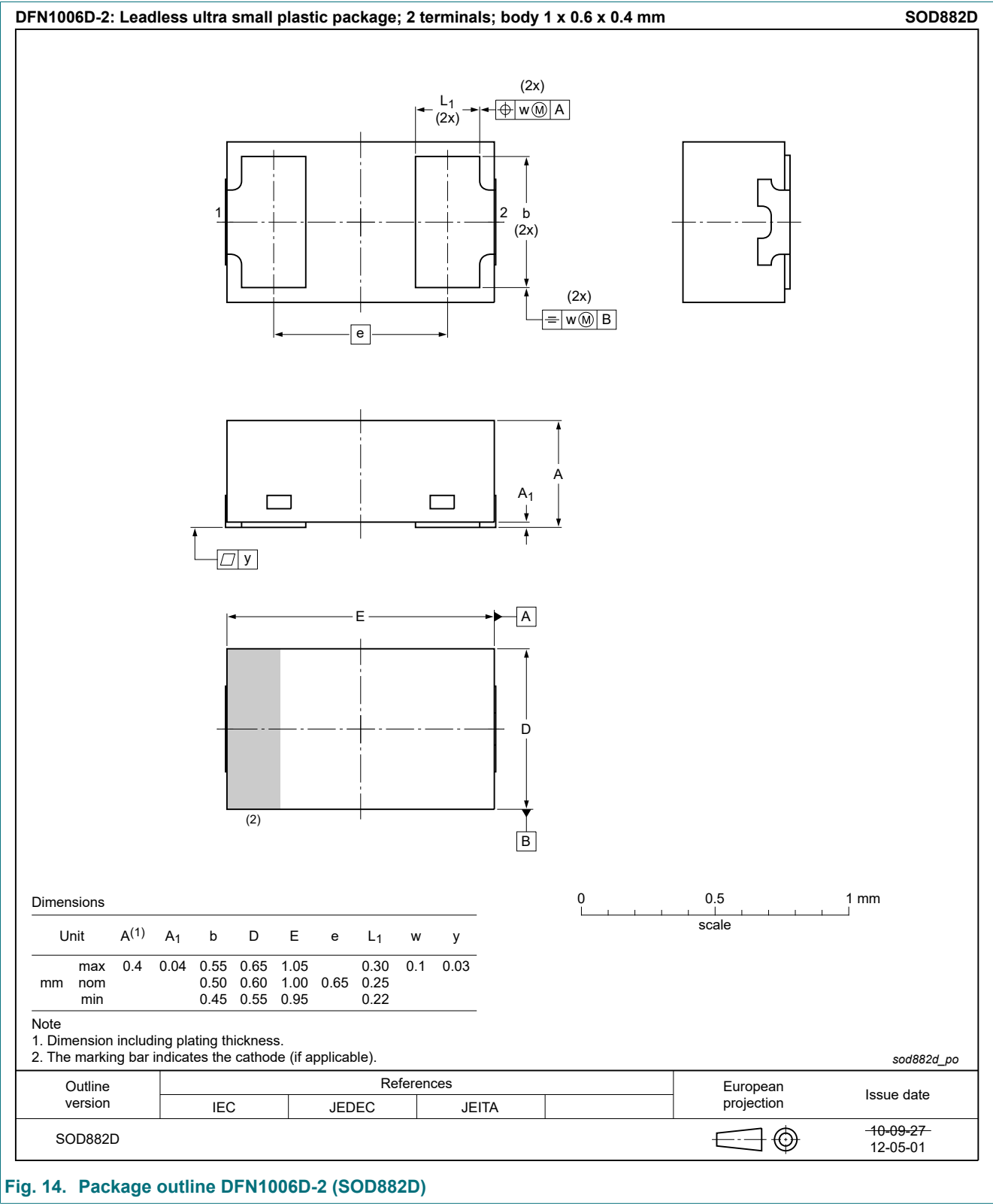
Fig. 13. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline



12. Soldering

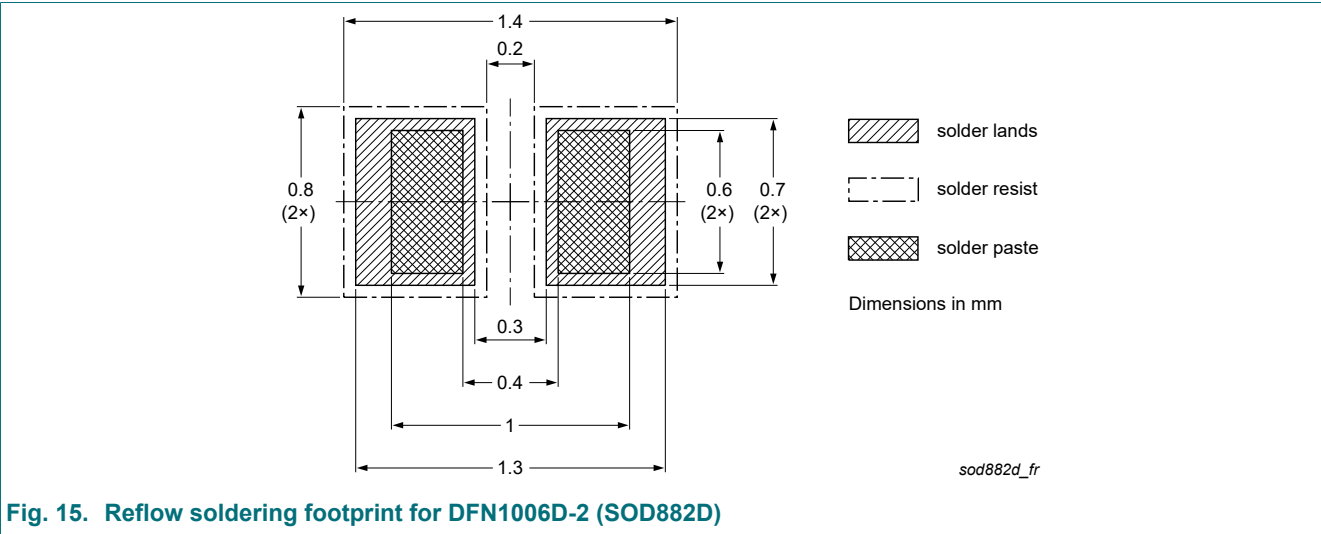


Fig. 15. Reflow soldering footprint for DFN1006D-2 (SOD882D)

13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD3V3T1BLD v.2	20240426	Product data sheet	-	PESD3V3T1BLD v.1
Modifications:	Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PESD3V3T1BLD v.1	20181121	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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Date of release: 26 April 2024

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