



**THE DATASHEET OF
TFI243-630-15_14MM**





TET ESTEL AS
ESTONIA

April
2016

Series
TFI243-630

High Frequency Inverter grade
Capsule Thyristor
Type TFI243-630

Low switching losses
Low reverse recovery charge
Distributed amplified gate for high di/dt

Maximum mean on-state current					I_{TAV}	630 A		
Maximum repetitive peak off-state and reverse voltage					U_{DRM}	800 ÷ 1500 V		
Turn-off time					U_{RRM}			
					t_q	16; 20; 25 μs		
U_{DRM}, U_{RRM}, V	800	900	1000	1100	1200	1300	1400	1500
Voltage code	8	9	10	11	12	13	14	15
$T_{vj}, °C$	- 60 ÷ 125							

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	TFI243-630	Conditions
I_{TAV}	Mean on-state current	A	630 890	$T_c=80 °C$, $T_c=55 °C$, 180° half-sine wave, 50 Hz
I_{TRMS}	RMS on-state current	A	989	$T_c=80 °C$
I_{TSM}	Surge on-state current	kA	11,0 12,0	$T_{vj}=125°C$ $T_{vj}=25°C$ tp=10 ms $U_R=0$
I^2t	Limiting load integral	kA^2s	605 720	$T_{vj}=125°C$ $T_{vj}=25°C$
U_{DRM}, U_{RRM}	Repetitive peak off-state and reverse voltage	V	800÷1500	$T_j \min \leq T_{vj} \leq T_{jM}$ 180° half-sine wave, 50 Hz Gate open
U_{DSM}, U_{RSM}	Non-repetitive peak off-state and reverse voltage	V	880÷1600	$T_j \min \leq T_{vj} \leq T_{jM}$ 180° half-sine wave tp=10 ms, Single pulse Gate open
(di/dt) crit	Critical rate of rise of on-state current : non - repetitive repetitive	A/μs	2000 1250	$T_{vj}=125°C$; $U_D=0,67 U_{DRM}$, Gate pulse : 10V, 5 Ω, 1 μs rise time, 10 μs
U_{RGM}	Peak reverse gate voltage	V	5	$T_j \min \leq T_{vj} \leq T_{jM}$
T_{stg}	Storage temperature	°C	-60÷80	
T_{vj}	Junction temperature	°C	-60÷125	

CHARACTERISTICS

U_{TM}	Peak on-state voltage	V	2,5	$T_{vj}=25°C$, $I_{TM}=3,14 I_{TAV}$
$U_{T(TO)}$	Threshold voltage	V	1,4	$T_{vj}=125°C$
R_T	On-state slope resistance	mΩ	0,46	$1,57 I_{TAV} < I_T < 4,71 I_{TAV}$
I_{DRM} I_{RRM}	Repetitive peak off-state and reverse current	mA	50 50	$T_{vj}=125°C$, $U_D = U_{DRM}$ $U_R = U_{RRM}$

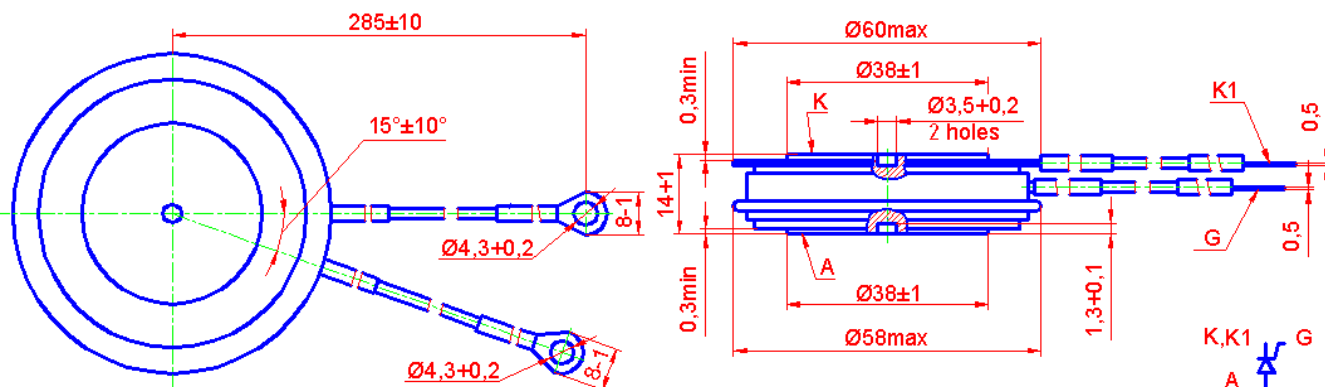
CHARACTERISTICS

Symbols and parameters		Units	TFI243-630	Conditions
I_L	Latching current	A	7	$T_{vj}=25^{\circ}\text{C}, U_D=12\text{V}$ Gate pulse : 10V, 5 Ω , 1 μs rise time, 10 μs
I_H	Holding current	A	0,5	$T_{vj}=25^{\circ}\text{C}, U_D=12\text{V}, \text{Gate open}$
U_{GT}	Gate trigger direct voltage	V	2,5 5,0	$T_{vj}=25^{\circ}\text{C},$ $T_{vj}=-60^{\circ}\text{C}$
I_{GT}	Gate trigger direct current	A	0,3 0,85	$T_{vj}=25^{\circ}\text{C},$ $T_{vj}=-60^{\circ}\text{C}$
U_{GD}	Gate non-trigger direct voltage	V	0,25	$T_{vj}=125^{\circ}\text{C}, U_D = 0,67 U_{DRM}$ Direct gate current
I_{GD}	Gate non-trigger direct current	mA	10	
tgd	Delay time	μs	1,6	$T_{vj}=25^{\circ}\text{C}, U_D=500\text{V}$ $I_{TM} = 630 \text{ A}$
tgt	Turn-on time	μs	2,5	Gate pulse : 10V, 5 Ω , 1 μs rise time, 10 μs
tq	Turn-off time	μs	16 \div 25 20 \div 32	$T_{vj}=125^{\circ}\text{C}, I_{TM} = 630 \text{ A}$ $di_R/dt = 10 \text{ A}/\mu\text{s}, U_R=100\text{V}$ $U_D = 0,67 U_{DRM}$ $du_D/dt=50 \text{ V}/\mu\text{s}$ $du_D/dt=200 \text{ V}/\mu\text{s}$
Qrr	Recovered charge	μC	210	
trr	Reverse recovery time	μs	3,5	$T_{vj}=125^{\circ}\text{C}, I_{TM} = 630 \text{ A}$
Irrm	Peak reverse recovery current	A	120	$di_R/dt = 50 \text{ A}/\mu\text{s}, U_R=100\text{V}$
$(du_D/dt)_{crit}$	Critical rate of rise of off-state voltage	V/ μs	500 1000	$T_{vj}=125^{\circ}\text{C}, U_D = 0,67 U_{DRM}$ Gate open
Rthjc	Thermal resistance junction to case	$^{\circ}\text{C}/\text{W}$	0,032	Direct current, double side cooled

ORDERING

	TFI	243	630	14	7	7	3	
	1	2	3	4	5	6	7	

- Fast thyristor with interdigitated gate structure.
- Design version.
- Mean on-state current, A.
- Voltage code (14=1400 V).
- Critical rate of rise of off-state voltage ($6 \geq 500 \text{ V}/\mu\text{s}$, $7 \geq 1000 \text{ V}/\mu\text{s}$).
- Group of turn-off time ($du_D/dt=50 \text{ V}/\mu\text{s}$, $5 \leq 25 \mu\text{s}$, $6 \leq 20 \mu\text{s}$, $7 \leq 16 \mu\text{s}$).
- Group of turn-on time ($3 \leq 2,5\mu\text{s}$).



Mounting force : 13 \div 19 kN

Weight : 210 grams

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