



## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$ Max	$I_D$ Max $T_A = +25^\circ C$
30V	21m $\Omega$ @ $V_{GS} = 10V$	10A

## Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

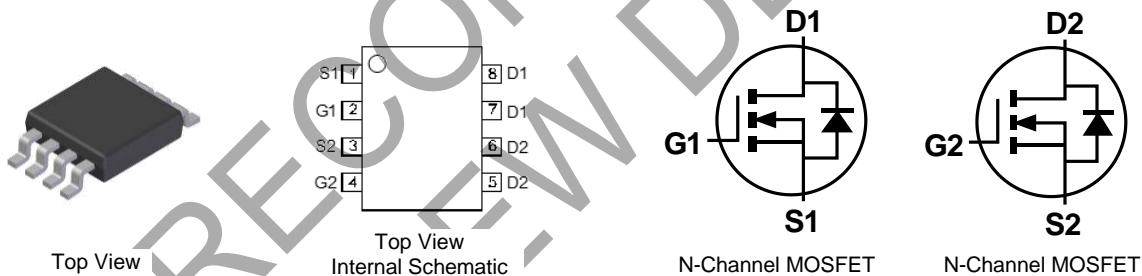
- General purpose interfacing switches
- Power management functions
- DC-DC converters
- Analog switches

## Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Low Input/Output Leakage
- Low Gate Resistance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DIODES™ DMG4822SSDQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**  
<https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish – Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (Approximate)

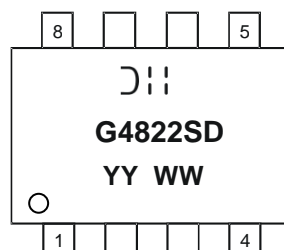


## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMG4822SSDQ-13	SO-8	2,500	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



DII = Manufacturer's Marking  
 G4822SD = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Year (ex: 22 = 2022)  
 WW = Week (01 to 53)

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	30	V
Gate-Source Voltage			$V_{GSS}$	$\pm 25$	V
Continuous Drain Current (Note 5) $V_{GS} = 10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	$I_D$	10	A
		$T_A = +85^\circ\text{C}$		6.6	
Pulsed Drain Current (Note 6)			$I_{DM}$	60	A
Avalanche Current (Notes 7 & 8)			$I_{AR}$	1.68	A
Repetitive Avalanche Energy, $L = 0.3\text{mH}$ (Notes 7 & 8)			$E_{AR}$	12.8	mJ

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_D$	1.42	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	88.4	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	30	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 25\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	13.4	21	m $\Omega$	$V_{GS} = 10\text{V}, I_D = 8.5\text{A}$
		—	19.5	32.5		$V_{GS} = 4.5\text{V}, I_D = 6\text{A}$
Forward Transfer Admittance	$ Y_{fs} $	—	20	—	mS	$V_{DS} = 5\text{V}, I_D = 8.5\text{A}$
Diode Forward Voltage	$V_{SD}$	—	0.4	1.0	V	$V_{GS} = 0\text{V}, I_S = 1\text{A}$
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	$C_{iss}$	—	478.9	—	pF	$V_{DS} = 16\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Output Capacitance	$C_{oss}$	—	96.7	—	pF	
Reverse Transfer Capacitance	$C_{riss}$	—	61.4	—	pF	
Gate Resistance	$R_g$	—	1.1	—	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge ( $V_{GS} = 4.5\text{V}$ )	$Q_g$	—	5	—	nC	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}, I_D = 8.5\text{A}$
Total Gate Charge ( $V_{GS} = 10\text{V}$ )	$Q_g$	—	10.5	—	nC	
Gate-Source Charge	$Q_{gs}$	—	1.8	—	nC	
Gate-Drain Charge	$Q_{gd}$	—	1.6	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	2.9	—	ns	
Turn-On Rise Time	$t_R$	—	7.9	—	ns	$V_{DS} = 15\text{V}, V_{GS} = 10\text{V}, R_L = 1.8\Omega, R_G = 3\Omega$
Turn-Off Delay Time	$t_{D(OFF)}$	—	14.6	—	ns	
Turn-Off Fall Time	$t_F$	—	3.1	—	ns	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
  - Device mounted on minimum recommended pad layout test board, 10 $\mu\text{s}$  pulse duty cycle = 1%.
  - Repetitive rating, pulse width limited by junction temperature.
  - $I_{AR}$  and  $E_{AR}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25^\circ\text{C}$ .
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

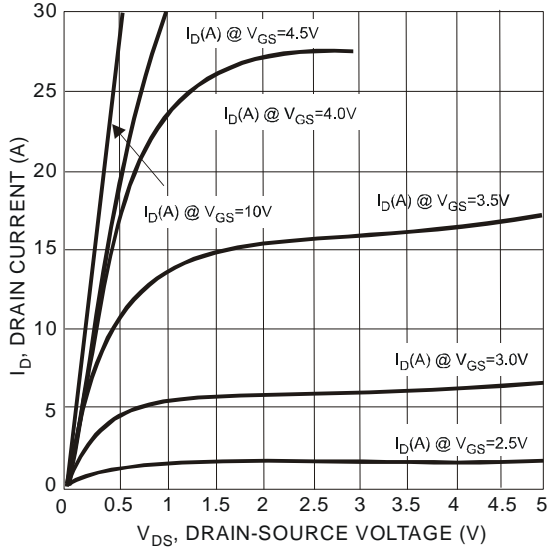


Fig. 1 Typical Output Characteristic

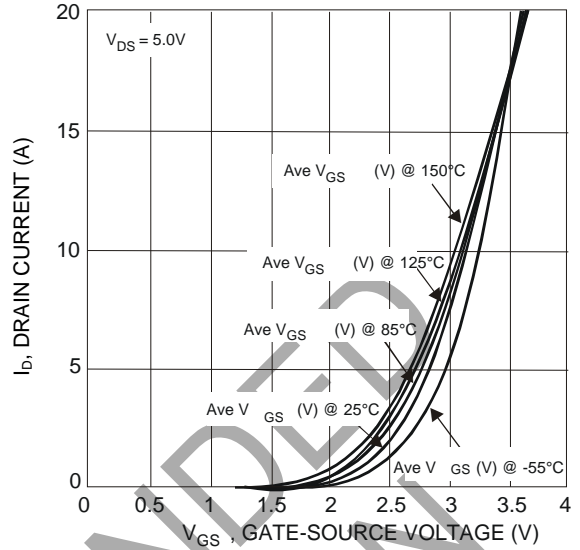


Fig. 2 Typical Transfer Characteristics

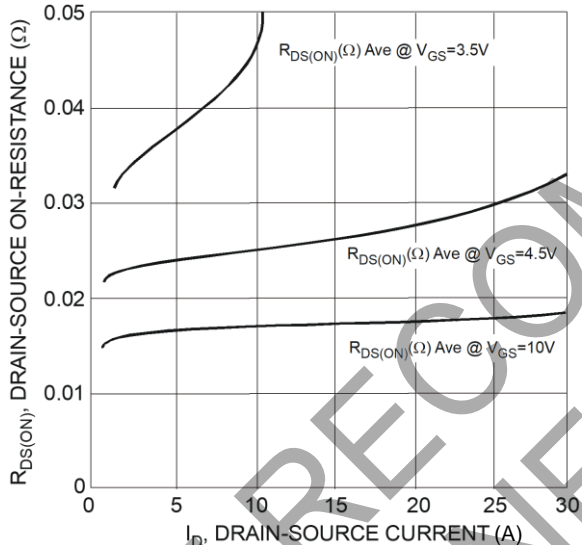


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

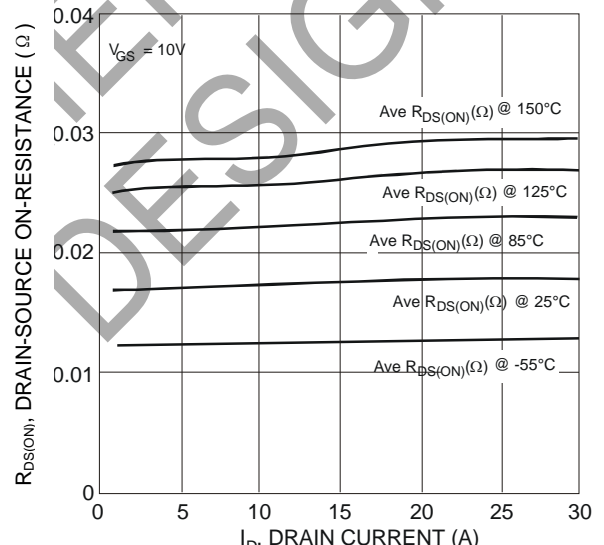


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

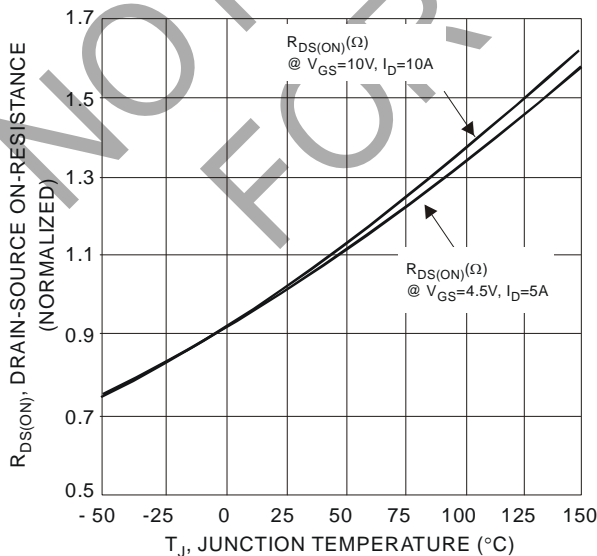


Fig. 5 On-Resistance Variation with Temperature

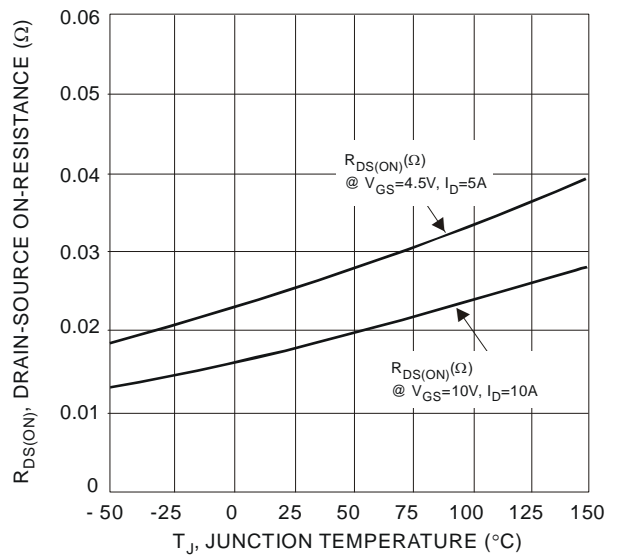


Fig. 6 On-Resistance Variation with Temperature

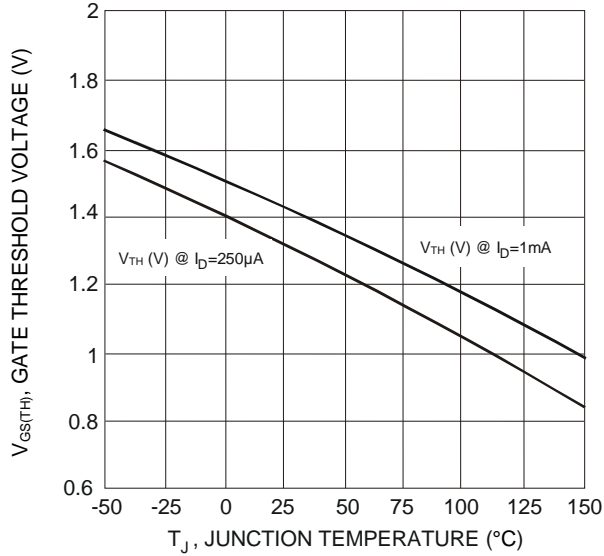


Fig. 7 Gate Threshold Variation vs. Junction Temperature

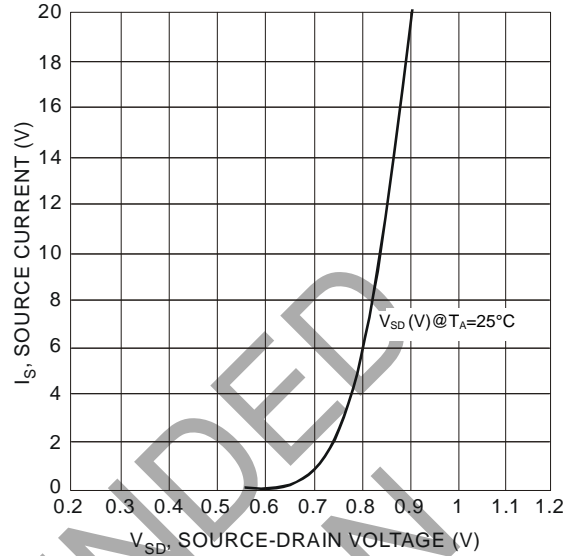


Fig.8 Diode Forward Voltage vs. Current

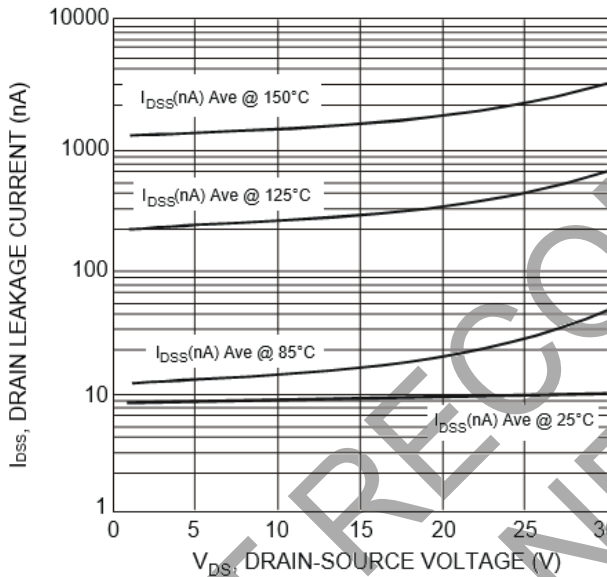


Fig. 9 Typical Drain-Source Leakage Current vs. Voltage

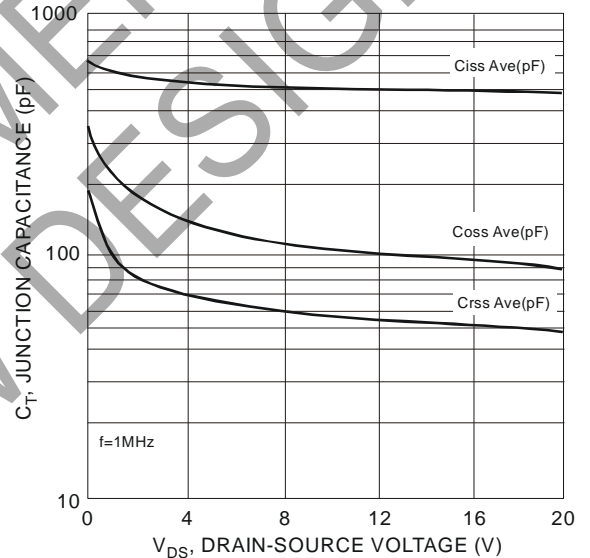


Fig. 10 Typical Junction Capacitance

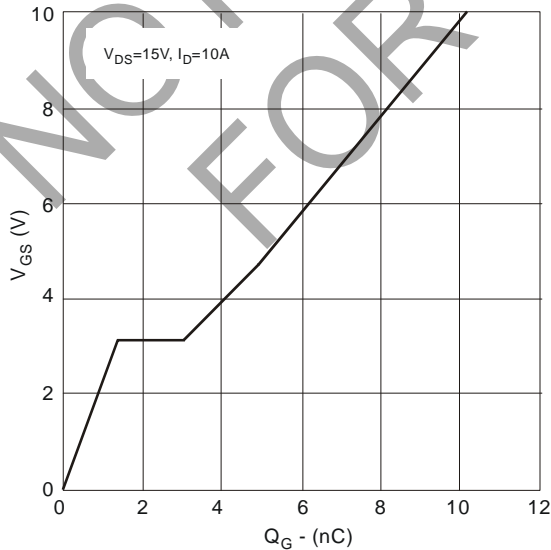
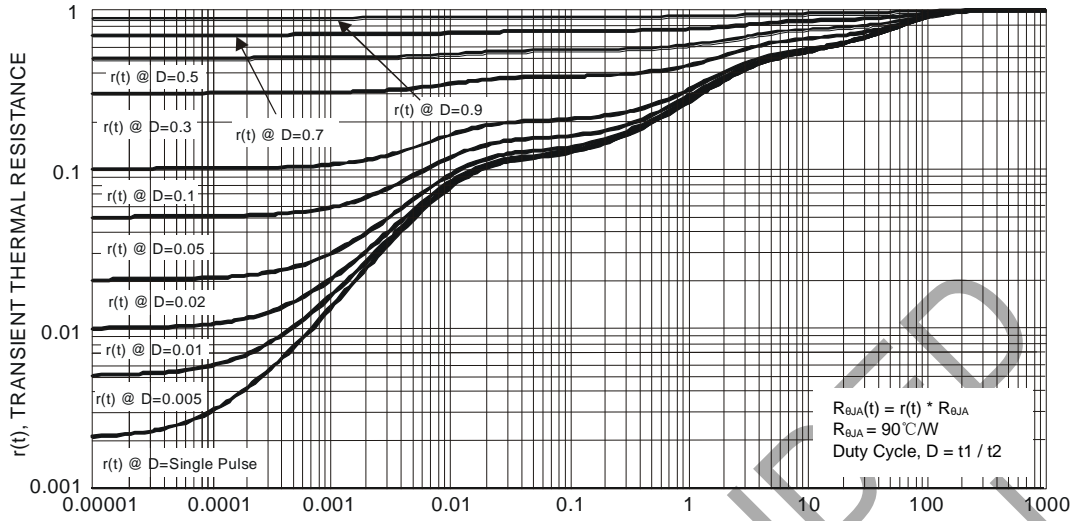


Fig. 11 Gate Charge



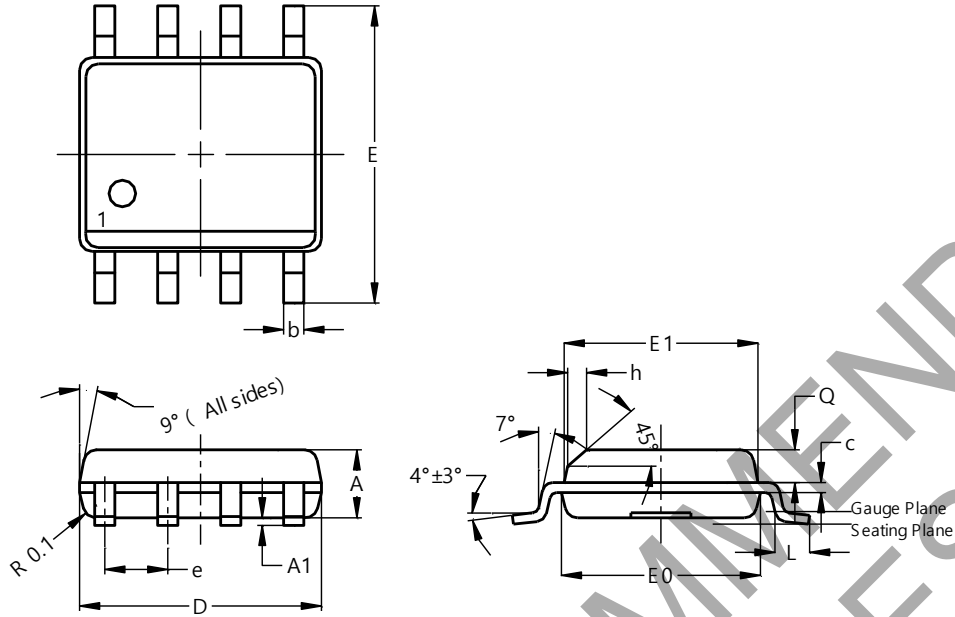
$t_1$ , PULSE DURATION TIME (sec)  
Fig. 12 Transient Thermal Resistance

NOT RECOMMENDED FOR NEW DESIGN

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SO-8**

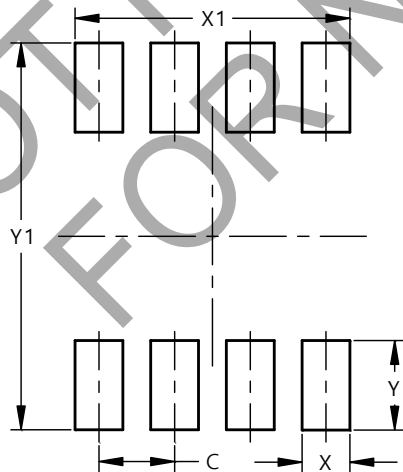


SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SO-8**



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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