



**THE DATASHEET OF
FST3383MTCX**



FST3383 10-Bit Low Power Bus-Exchange Switch

General Description

The FST3383 provides two sets of high-speed CMOS TTL-compatible bus switches. The low on resistance of the switch allows inputs to be connected to outputs without adding propagation delay or generating additional ground bounce noise. The device operates as a 10-bit bus switch or a 5-bit bus exchanger. The bus exchange (BX) signal provides nibble swapping of the AB and CD pairs of signals. This exchange configuration allows byte swapping of buses in systems. It can also be used as a quad 2-to-1 multiplexer and to create low delay barrel shifters. The bus enable (\overline{BE}) signal turns the switches ON.

Features

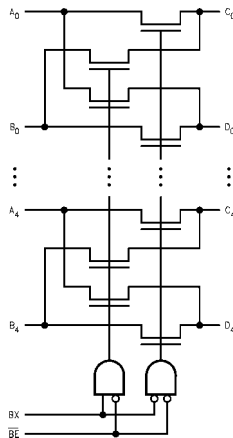
- 5Ω switch connection between two ports
- Zero propagation delay
- Ultra low power with $0.2\ \mu\text{A}$ typical I_{CC}
- Zero ground bounce in flow-through mode
- Control inputs compatible with TTL level

Ordering Code:

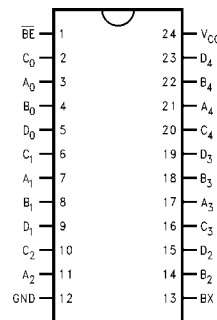
| Order Number | Package Number | Package Description |
|--------------|----------------|---|
| FST3383WM | M24B | 24-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide |
| FST3383QSC | MQA24 | 24-Lead Quarter Size Outline Package (QSOP), JEDEC MO-137, 0.150 Wide |
| FST3383MTC | MTC24 | 24-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide |

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Logic Diagram



Connection Diagram



Pin Descriptions

| Pin Names | Description |
|--------------------|-------------------|
| \overline{BE} | Bus Switch Enable |
| BX | Bus Exchange |
| A_0-A_4, B_0-B_4 | Buses A, B |
| C_0-C_4, D_0-D_4 | Buses C, D |

Truth Table

| \overline{BE} | BX | A_0-A_4 | B_0-B_4 | Function |
|-----------------|----|--------------|--------------|------------|
| H | X | High-Z State | High-Z State | Disconnect |
| L | L | C_0-C_4 | D_0-D_4 | Connect |
| L | H | D_0-D_4 | C_0-C_4 | Exchange |

Absolute Maximum Ratings (Note 1)

| | |
|---|-----------------|
| Supply Voltage (V_{CC}) | -0.5V to +7.0V |
| DC Switch Voltage (V_S) | -0.5V to +7.0V |
| DC Input Voltage (V_I) (Note 2) | -0.5V to +7.0V |
| DC Input Diode Current (I_{IN}) with $V_I < 0$ | -20 mA |
| DC Output (I_O) Sink Current | 120 mA |
| Storage Temperature Range (T_{STG}) | -65°C to +150°C |
| Power Dissipation | 0.5W |

Recommended Operating Conditions

| | |
|--|----------------|
| Supply Voltage (V_{CC}) | 4.0V to 5.5V |
| Free Air Operating Temperature (T_A) | -40°C to +85°C |

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

DC Electrical Characteristics

| Symbol | Parameter | V_{CC} (V) | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ | | | Units | Conditions |
|-----------------|---|-----------------|---|-----------------|----------|---------------|---|
| | | | Min | Typ (Note 3) | Max | | |
| V_{IK} | Maximum Clamp Diode Voltage | 4.75 | | | -1.2 | V | $I_{IN} = -18\text{ mA}$ |
| V_{IH} | Minimum High Level Input Voltage | 4.75-5.25 | 2.0 | | | V | |
| V_{IL} | Maximum Low Level Input Voltage | 4.75-5.25 | | | 0.8 | V | |
| I_{IN} | Maximum Input Leakage Current | 0 | | | 10 | μA | $0 \leq V_{IN} \leq 5.25\text{V}$ |
| | | 5.25 | | | ± 1 | | |
| I_{OZ} | Maximum 3-STATE I/O Leakage | 5.25 | | | ± 10 | μA | $0 \leq A, B \leq V_{CC}$ |
| I_{OS} | Short Circuit Current | 4.75 | 100 | | | mA | $V_I(A), V_I(B) = 0\text{V}, V_I(B), V_I(A) = 4.75\text{V}$ |
| R_{ON} | Switch On Resistance (Note 4) | 4.75 | | 5 | 7 | Ω | $V_I = 0\text{V}, I_{ON} = 30\text{ mA}$ |
| | | | | 10 | 15 | Ω | $V_I = 2.4\text{V}, I_{ON} = 15\text{ mA}$ |
| I_{CC} | Maximum Quiescent Supply Current | 5.25 | | 0.2 | 10 | μA | $V_I = V_{CC}, \text{GND}, I_O = 0$ |
| ΔI_{CC} | Increase in I_{CC} per Input (Note 5) | 5.25 | | | 2.5 | mA | $V_{IN} = 3.15\text{V}, I_O = 0, \text{Per Control Input}$ |

Note 3: All typical values are at $V_{CC} = 5.0\text{V}, T_A = 25^\circ\text{C}$.

Note 4: Measured by voltage drop between A and B pin at indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

Note 5: Per TTL driven input ($V_{IN} = 3.15\text{V}$, control inputs only). A and B pins do not contribute to I_{CC} .

AC Electrical Characteristics

| Symbol | Parameter | V _{CC} (V) | T _A = -40°C to +85°C, C _L = 50 pF | | | Units |
|-------------------------------------|---|---------------------|---|--------------|------|-------|
| | | | Min | Typ (Note 6) | Max | |
| t _{PLH} , t _{PHL} | Data Propagation Delay A _n to C _n , D _n or B _n to D _n , C _n (Note 7) | 4.75 | | | 0.25 | ns |
| t _{PLH} , t _{PHL} | Switch Exchange Time BX to A _n , B _n , C _n , D _n | 4.75 | 1.5 | | 6.5 | ns |
| t _{PZL} , t _{PZH} | Switch Enable Time BE to A _n , B _n , C _n or D _n | 4.75 | 1.5 | | 6.5 | ns |
| t _{PLZ} , t _{PHZ} | Switch Disable Time BE to A _n , B _n , C _n , or D _n | 4.75 | 1.5 | | 5.5 | ns |

Note 6: All typical values are at V_{CC} = 5.0V, T_A = 25°C.

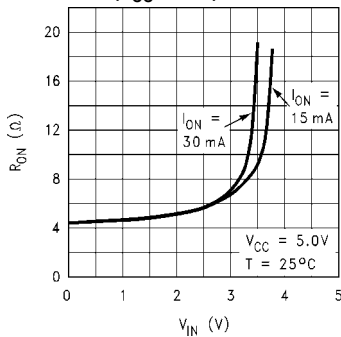
Note 7: This parameter is guaranteed by design but not tested. The bus switch contributes no propagation delay other than the RC delay of the On resistance of the switch and the load capacitance. The time constant for the switch and alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Capacitance (Note 8)

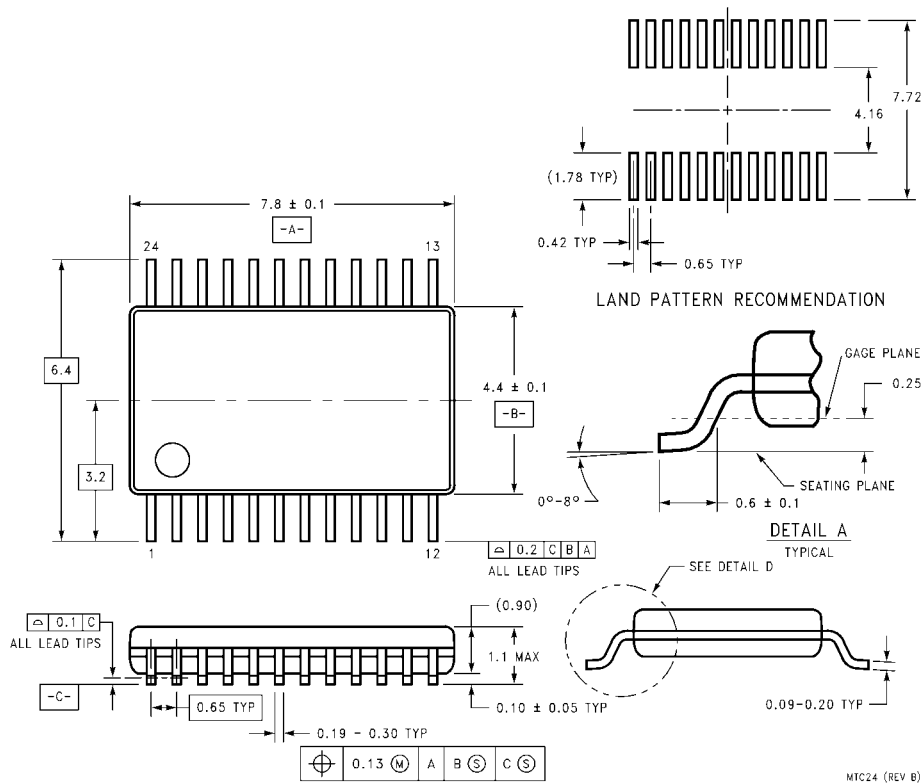
| Symbol | Parameter | Typ | Max | Units | Conditions |
|------------------------|---------------------------|-----|-----|-------|------------------------|
| C _{IN} | Control Input Capacitance | 4 | 6 | pF | V _{CC} = 5.0V |
| C _{I/O} (OFF) | Input/Output Capacitance | 9 | 13 | pF | V _{CC} = 5.0V |

Note 8: Capacitance is characterized but not tested.

On-Resistance (R_{ON}) vs Input Voltage (V_{CC} = 5.0V)



Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



**24-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide
Package Number MTC24**

Technology Description

The Fairchild Switch family derives from and embodies Fairchild's proven switch technology used for several years in its 74LVX3L384 (FST3384) bus switch product.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

-  [View FST3383MTCX on WIN SOURCE](#)
-  [Fairchild/ON Semiconductor Information](#)

Optimize Your Supply Chain with WIN SOURCE Solutions

-  Global Sourcing Solution
-  Obsolete Management
-  Cost Control Management
-  Shortage Management
-  Alternative Solution
-  Excess Inventory Management