



**THE DATASHEET OF
PBSS2540M,315**





PBSS2540M

40 V, 0.5 A NPN low V_{CEsat} (BISS) transistor

22 February 2018

Product data sheet

1. General description

Low V_{CEsat} NPN transistor in a SOT883 leadless ultra small plastic package.

PNP complement: PBSS3540M.

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High efficiency leading to reduced heat generation
- Reduced printed-circuit board requirements.
- AEC-Q101 qualified

3. Applications

- Power management:
 - DC-DC converter
 - Supply line switching
 - Battery charger
 - LCD backlighting.
- Peripheral driver:
 - Driver in low supply voltage applications (e.g. lamps and LEDs).
 - Inductive load drivers (e.g. relays, buzzers and motors).

4. Quick reference data

Table 1. Quick reference data

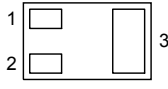
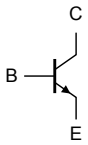
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	40	V
I_C	collector current		[1] [2]	-	500	mA
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	1	A
h_{FE}	DC current gain	$V_{CE} = 2$ V; $I_C = 10$ mA; $T_{amb} = 25$ °C	200	-	-	
R_{CEsat}	collector-emitter saturation resistance	$I_C = 500$ mA; $I_B = 50$ mA; $t_p \leq 300$ μ s; pulsed; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	380	500	m Ω

[1] Device mounted on an FR4 Printed-Circuit Board, (PCB), single-sided copper, tinplated, standard footprint, with 60 μ m copper strip line.

[2] Refer to SOT883 standard mounting conditions.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 <p>Transparent top view DFN1006-3 (SOT883)</p>	 <p>sym123</p>
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS2540M	DFN1006-3	DFN1006-3: leadless ultra small plastic package; 3 solder lands	SOT883

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS2540M	DC

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	40	V
V_{CEO}	collector-emitter voltage	open base		-	40	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I_C	collector current		[1] [2]	-	500	mA
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	1	A
I_{BM}	peak base current			-	100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1] [2]	-	250	mW
			[2] [3]	-	430	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board, (PCB), single-sided copper, tinplated, standard footprint, with 60 μ m copper strip line.
 [2] Refer to SOT883 standard mounting conditions.
 [3] Device mounted on an FR4 PCB, single-sided copper, tinplated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

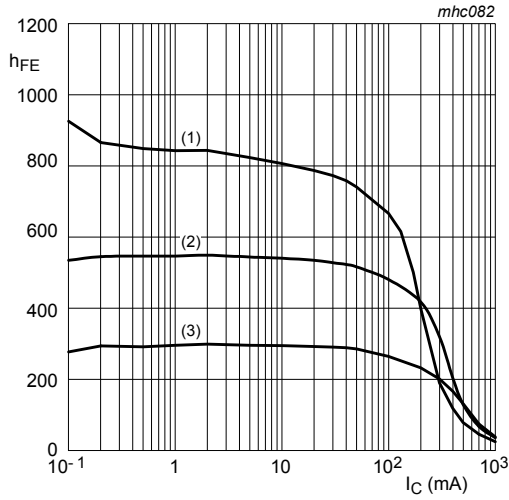
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	500	K/W
			[2] [3] [4]	-	-	290	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint, with 60 μ m copper strip line.
 [2] Refer to SOT883 standard mounting conditions.
 [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
 [4] Operated under pulsed conditions: duty cycle $\bar{d} \leq 20\%$, pulse width $t_p \leq 30$ ms.

10. Characteristics

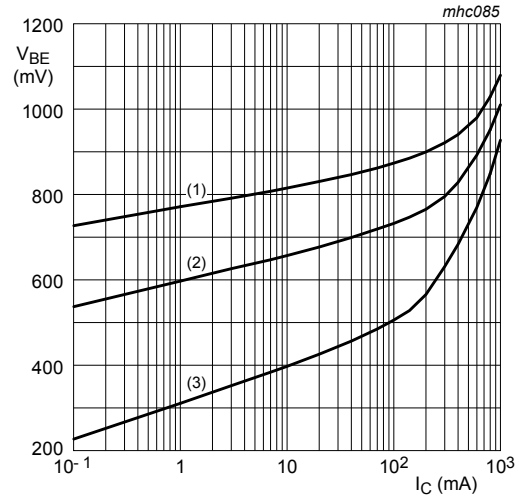
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
		V _{CB} = 30 V; I _E = 0 A; T _J = 150 °C	-	-	50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 10 mA; T _{amb} = 25 °C	200	-	-	
		V _{CE} = 2 V; I _C = 100 mA; t _p ≤ 300 μs; pulsed; δ ≤ 0.02 ; T _{amb} = 25 °C	150	-	-	
		V _{CE} = 2 V; I _C = 500 mA; t _p ≤ 300 μs; pulsed; δ ≤ 0.02 ; T _{amb} = 25 °C	50	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	-	50	mV
		I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C	-	-	100	mV
		I _C = 200 mA; I _B = 10 mA; T _{amb} = 25 °C	-	-	200	mV
		I _C = 500 mA; I _B = 50 mA; t _p ≤ 300 μs; pulsed; δ ≤ 0.02 ; T _{amb} = 25 °C	-	-	250	mV
R _{CEsat}	collector-emitter saturation resistance		-	380	500	mΩ
V _{BEsat}	base-emitter saturation voltage		-	-	1.2	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 2 V; I _C = 100 mA; T _{amb} = 25 °C	-	-	1.1	V
f _T	transition frequency	V _{CE} = 5 V; I _C = 100 mA; f = 100 MHz; T _{amb} = 25 °C	250	450	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	6	pF



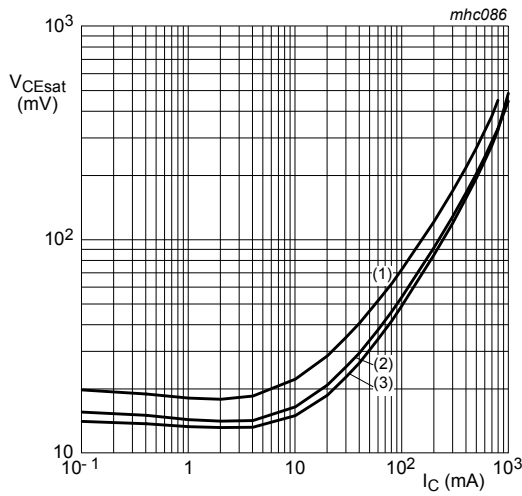
$V_{CE} = 2\text{ V}$
 (1) $T_{amb} = 150\text{ }^\circ\text{C}$
 (2) $T_{amb} = 25\text{ }^\circ\text{C}$
 (3) $T_{amb} = -55\text{ }^\circ\text{C}$

Fig. 1. DC current gain as a function of collector current; typical values



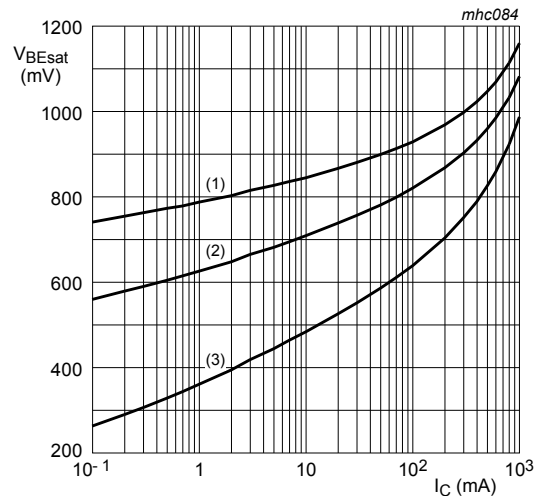
$V_{CE} = 2\text{ V}$
 (1) $T_{amb} = -55\text{ }^\circ\text{C}$
 (2) $T_{amb} = 25\text{ }^\circ\text{C}$
 (3) $T_{amb} = 150\text{ }^\circ\text{C}$

Fig. 2. Base-emitter voltage as a function of collector current; typical values



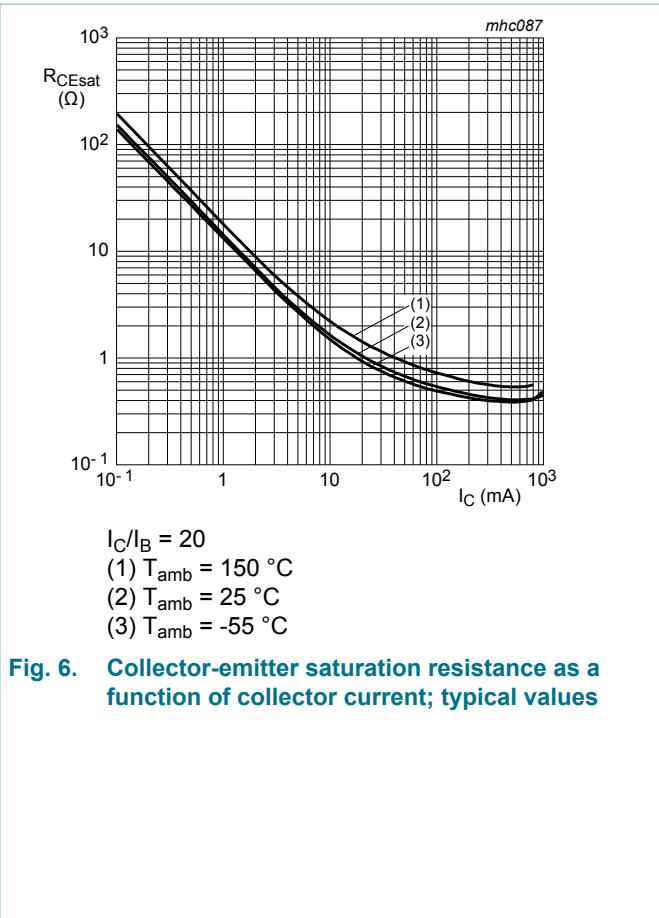
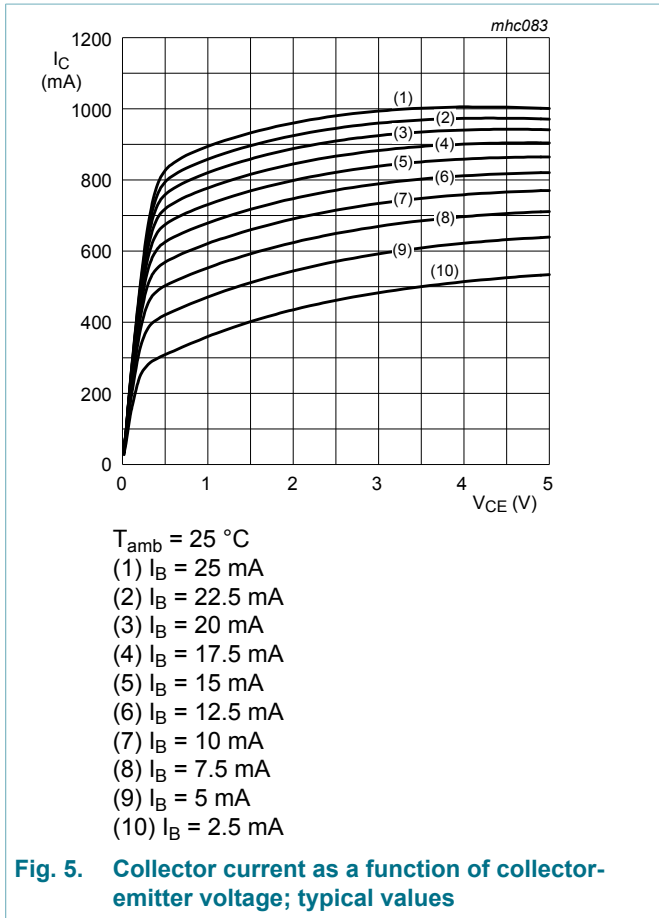
$I_C/I_B = 20$
 (1) $T_{amb} = 150\text{ }^\circ\text{C}$
 (2) $T_{amb} = 25\text{ }^\circ\text{C}$
 (3) $T_{amb} = -55\text{ }^\circ\text{C}$

Fig. 3. Collector-emitter saturation voltage as a function of collector current; typical values

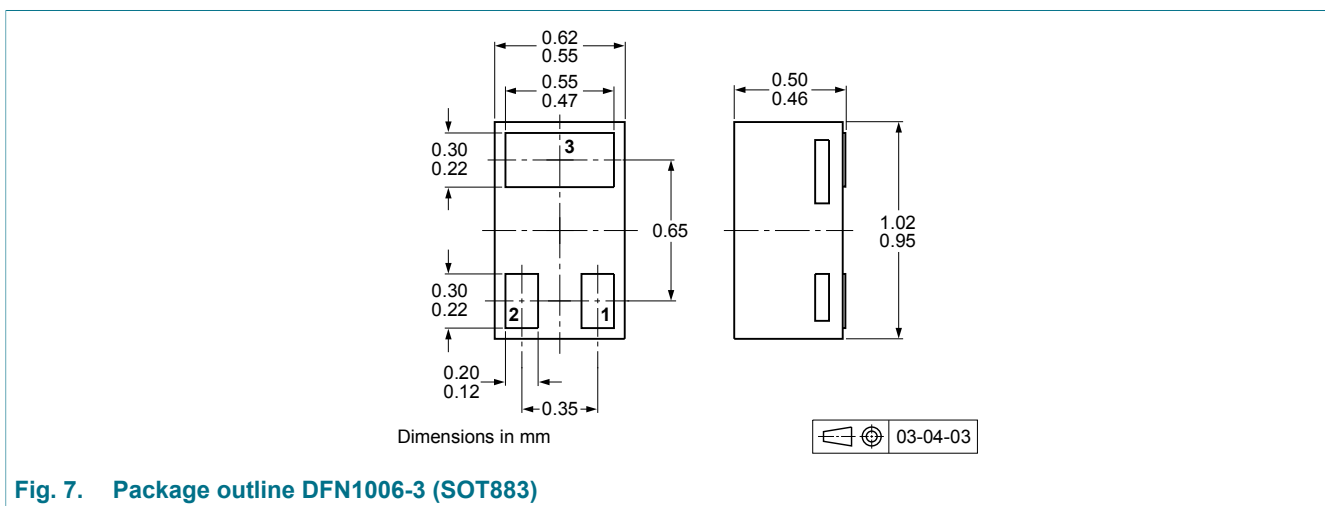


$I_C/I_B = 20$
 (1) $T_{amb} = 150\text{ }^\circ\text{C}$
 (2) $T_{amb} = 25\text{ }^\circ\text{C}$
 (3) $T_{amb} = -55\text{ }^\circ\text{C}$

Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values



11. Package outline



12. Soldering

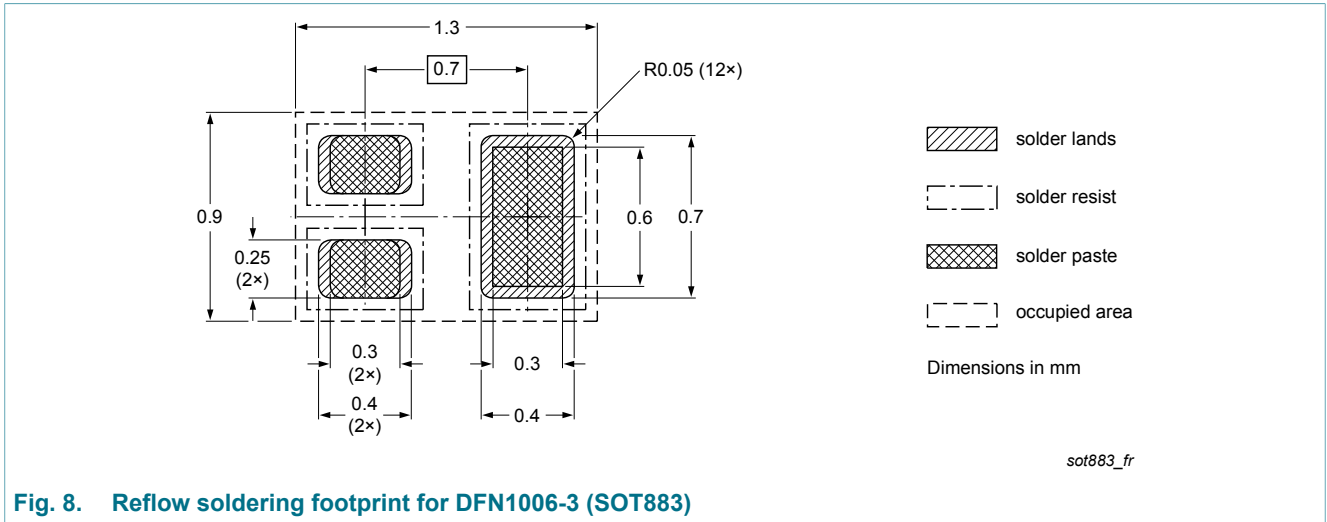


Fig. 8. Reflow soldering footprint for DFN1006-3 (SOT883)

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS2540M v.2	20180222	Product data sheet	-	PBSS2540M v.1
Modifications:	<ul style="list-style-type: none">The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.			
PBSS2540M v.1	20030722	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 22 February 2018

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