



**THE DATASHEET OF
74AHC1G07GV,125**





74AHC1G07

Buffer with open-drain output

Rev. 11 — 14 March 2025

Product data sheet

1. General description

The 74AHC1G07 is a single buffer with open-drain output. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

2. Features and benefits

- Wide supply voltage range from 2.0 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AHC1G07GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AHC1G07GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753
74AHC1G07GZ	-40 °C to +125 °C	XSON5	plastic thermal enhanced extremely thin small outline package with side-wettable flanks (SWF); no leads; 5 terminals; body 1.1 × 0.85 × 0.5 mm	SOT8065-1

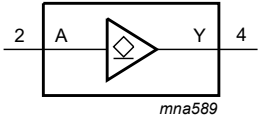

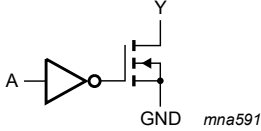
4. Marking

Table 2. Marking codes

Type number	Marking [1]
74AHC1G07GW	AS
74AHC1G07GV	A07
74AHC1G07GZ	AS

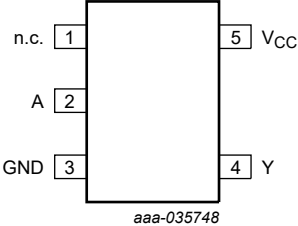
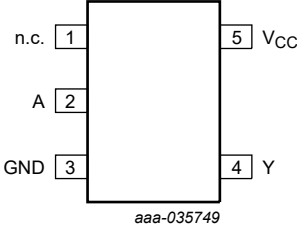
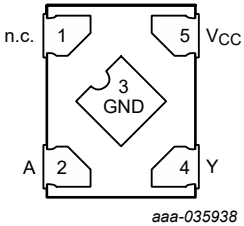
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram

 <p><i>mna589</i></p>	 <p><i>mna590</i></p>	 <p><i>mna591</i></p>
Fig. 1. Logic symbol	Fig. 2. IEC logic symbol	Fig. 3. Logic diagram

6. Pinning information

6.1. Pinning

<p>GW package SOT353-1 (TSSOP5)</p>  <p><i>aaa-035748</i></p>	<p>GV package SOT753 (SC-74A)</p>  <p><i>aaa-035749</i></p>	<p>GZ package SOT8065-1 (GX5)</p>  <p><i>aaa-035938</i> Transparent top view</p>
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6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
n.c.	1	not connected
A	2	data input
GND	3	ground (0 V)
Y	4	data output
V _{CC}	5	supply voltage

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state

Input	Output
A	Y
L	L
H	Z

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7.0	V
V_I	input voltage		-0.5	+7.0	V
I_{IK}	input clamping current	$V_I < -0.5$ V	-20	-	mA
I_{OK}	output clamping current	$V_O < -0.5$ V [1]	-	±20	mA
I_O	output current	$V_O > -0.5$ V	-	±25	mA
V_O	output voltage	active mode [1]	-0.5	+7.0	V
		high-impedance mode [1]	-0.5	+7.0	V
I_{CC}	supply current		-	75	mA
I_{GND}	ground current		-75	-	mA
T_{stg}	storage temperature		-65	+150	°C
P_{tot}	total power dissipation	$T_{amb} = -40$ °C to +125 °C [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

For SOT753 (SC-74A) package: P_{tot} derates linearly with 3.8 mW/K above 85 °C.

For SOT8065-1 (XSON5) package: P_{tot} derates linearly with 3.2 mW/K above 72 °C.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CC}	supply voltage		2.0	5.0	5.5	V
V_I	input voltage		0	-	5.5	V
V_O	output voltage	active mode	0	-	V_{CC}	V
		high-impedance mode	0	-	6.0	V
T_{amb}	ambient temperature		-40	+25	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 3.3$ V ± 0.3 V	-	-	100	ns/V
		$V_{CC} = 5.0$ V ± 0.5 V	-	-	20	ns/V

10. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
V _{IH}	HIGH-level input voltage	V _{CC} = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
		V _{CC} = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V _{CC} = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
		V _{CC} = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V _{CC} = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}								
		I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
I _O = 8.0 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V		
I _I	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _O = V _{CC} or GND; V _{CC} = 5.5 V	-	-	±0.25		±2.5		±10.0	μA
I _{CC}	supply current	V _I = V _{CC} or GND; I _O = 0 A; V _{CC} = 5.5 V	-	-	1.0	-	10	-	20	μA
C _I	input capacitance		-	1.5	10	-	10	-	10	pF

11. Dynamic characteristics

Table 8. Dynamic characteristics

GND = 0 V; t_r = t_f = ≤ 3.0 ns. For test circuit see Fig. 5.

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
t _{PZL}	OFF-state to LOW propagation delay	A to Y; see Fig. 4								
		V _{CC} = 3.0 V to 3.6 V; C _L = 15 pF [1]	-	3.5	5.6	1.0	6.3	1.0	7.0	ns
		V _{CC} = 3.0 V to 3.6 V; C _L = 50 pF [1]	-	5.0	8.0	1.0	9.0	1.0	10.0	ns
		V _{CC} = 4.5 V to 5.5 V; C _L = 15 pF [2]	-	2.5	3.9	1.0	4.6	1.0	4.9	ns
		V _{CC} = 4.5 V to 5.5 V; C _L = 50 pF [2]	-	3.6	5.5	1.0	6.5	1.0	7.0	ns

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
t _{PLZ}	LOW to OFF-state propagation delay	A to Y; see Fig. 4								
		V _{CC} = 3.0 V to 3.6 V; C _L = 15 pF [1]	-	5.8	7.9	1.0	8.4	1.0	8.9	ns
		V _{CC} = 3.0 V to 3.6 V; C _L = 50 pF [1]	-	8.3	11.5	1.0	12.0	1.0	12.5	ns
		V _{CC} = 4.5 V to 5.5 V; C _L = 15 pF [2]	-	4.2	5.1	1.0	5.6	1.0	6.1	ns
		V _{CC} = 4.5 V to 5.5 V; C _L = 50 pF [2]	-	6.0	7.5	1.0	8.0	1.0	8.5	ns
C _{PD}	power dissipation capacitance	per buffer; C _L = 50 pF; f = 1 MHz; V _I = GND to V _{CC} [3]	-	5	-	-	-	-	-	pF

- [1] Typical values are measured at V_{CC} = 3.3 V.
- [2] Typical values are measured at V_{CC} = 5.0 V.
- [3] C_{PD} is used to determine the dynamic power dissipation P_D (μW).
 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:
 f_i = input frequency in MHz;
 f_o = output frequency in MHz;
 C_L = output load capacitance in pF;
 V_{CC} = supply voltage in Volts

11.1. Waveforms and test circuit

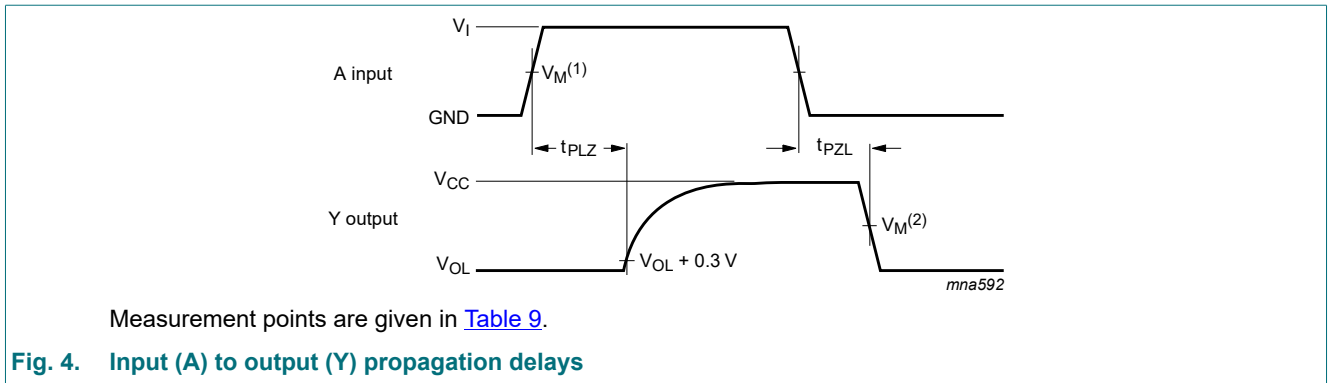
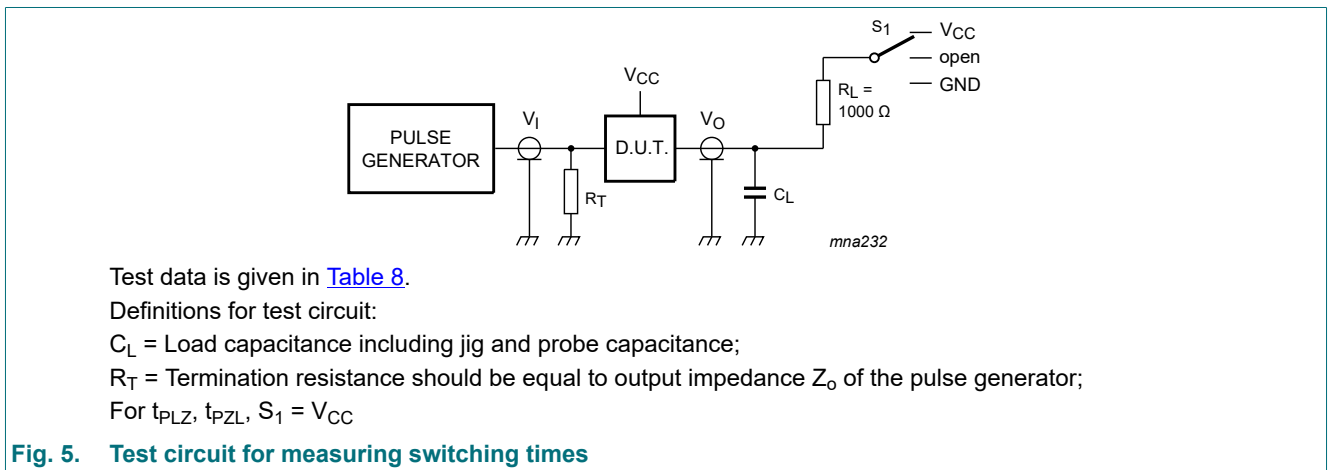


Table 9. Measurement point

Input		Output
V _I	V _M ⁽¹⁾	V _M ⁽²⁾
GND to V _{CC}	0.5 × V _{CC}	0.5 × V _{CC}



12. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1

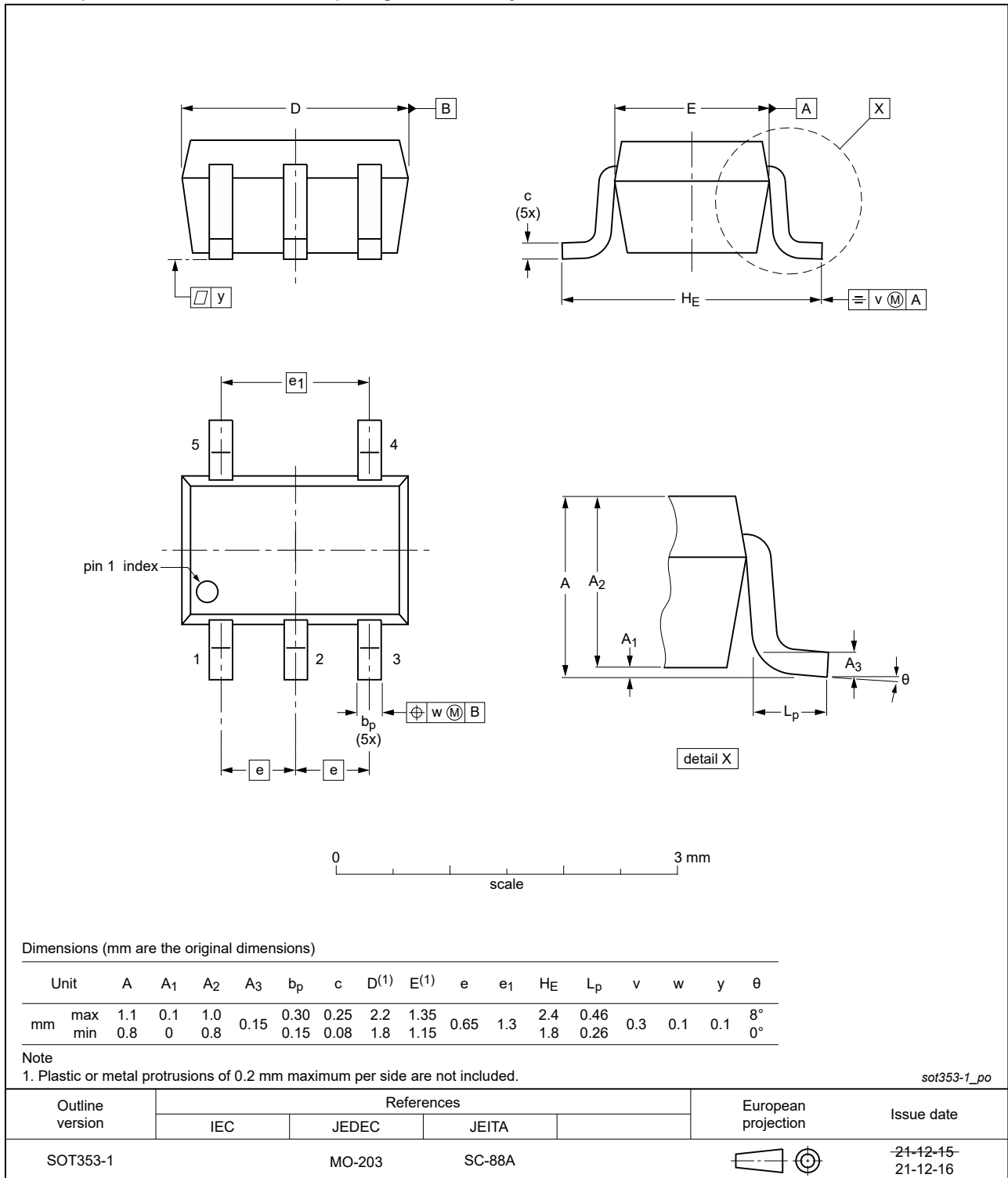


Fig. 6. Package outline SOT353-1 (TSSOP5)

Plastic surface-mounted package; 5 leads

SOT753

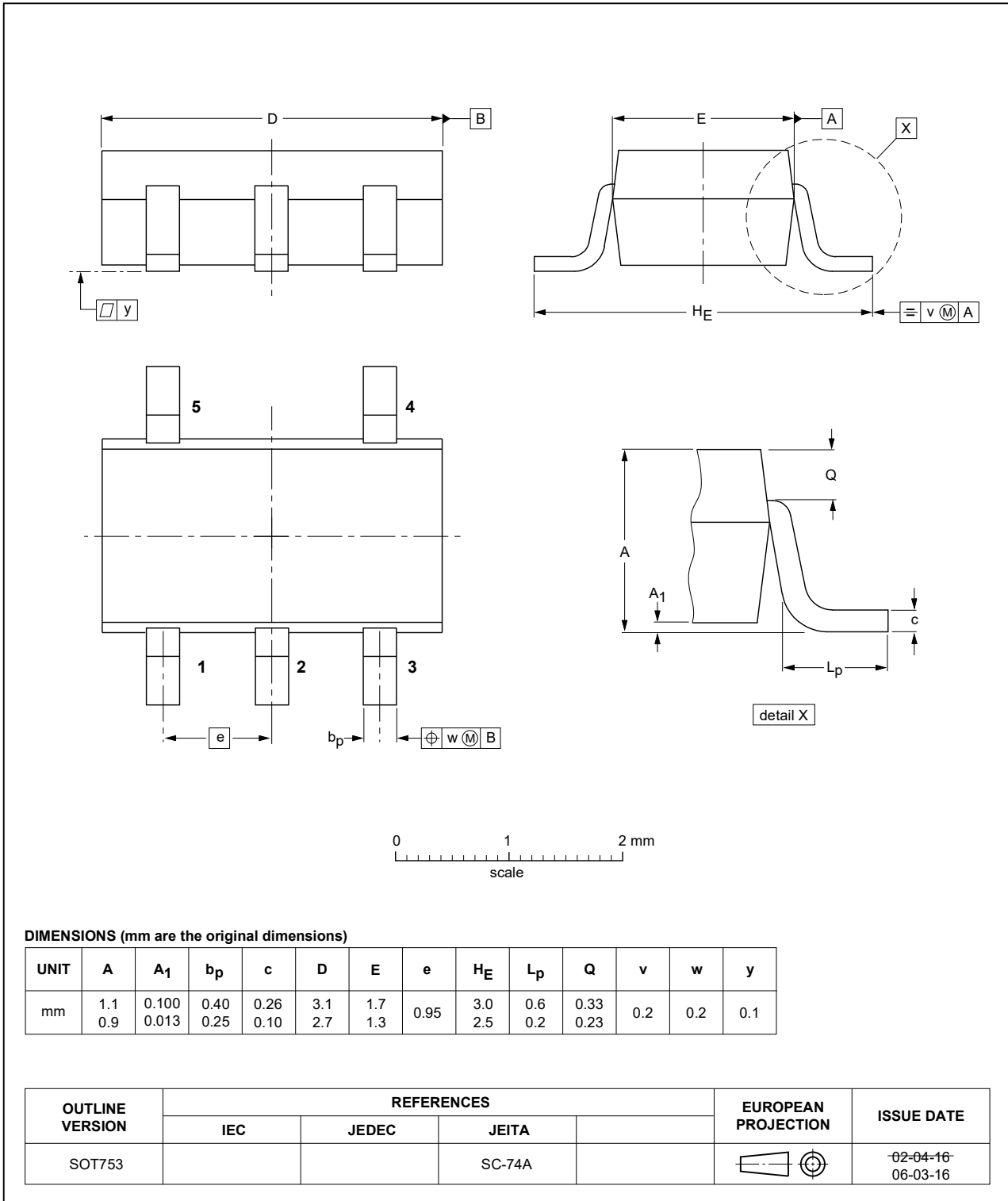


Fig. 7. Package outline SOT753 (SC-74A)

XSON5: Plastic thermal enhanced extremely thin small outline package with side-wettable flanks (SWF); no leads; 5 terminals; body 1.1 × 0.85 × 0.5 mm

SOT8065-1

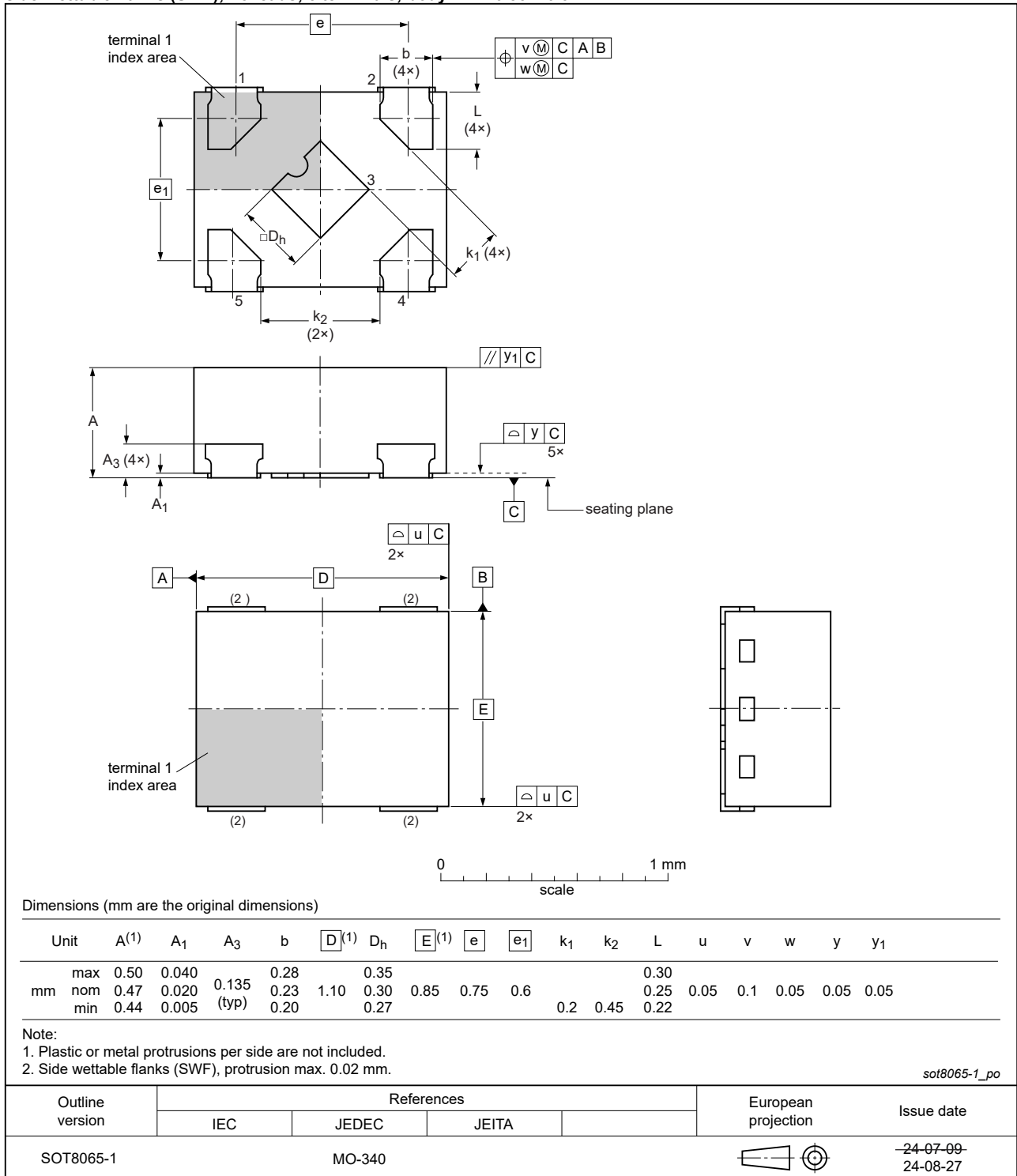


Fig. 8. Package outline SOT8065-1 (XSON5)

13. Abbreviations

Table 10. Abbreviations

Acronym	Description
ANSI	American National Standards Institute
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
HBM	Human Body Model
JEDEC	Joint Electron Device Engineering Council

14. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHC1G07 v.11	20250314	Product data sheet	-	74AHC1G07 v.10
Modifications:	<ul style="list-style-type: none"> Type number 74AHC1G07GZ (SOT8065-1/XSON5) added. 			
74AHC1G07 v.10	20231005	Product data sheet	-	74AHC1G07 v.9
Modifications:	<ul style="list-style-type: none"> Section 2: ESD specification updated according to the latest JEDEC standard. 			
74AHC1G07 v.9	20220111	Product data sheet	-	74AHC1G07 v.8
Modifications:	<ul style="list-style-type: none"> Section 1 and Section 2 updated. Section 8: Derating values for P_{tot} total power dissipation updated. Fig. 6: Package outline drawing SOT353-1 (TSSOP5) updated. 			
74AHC1G07 v.8	20190225	Product data sheet	-	74AHC_AHCT1G07 v.7
Modifications:	<ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type numbers 74AHCT1G07GW (SOT353-1) and 74AHCT1G07GV (SOT753) removed. 			
74AHC_AHCT1G07 v.7	20141118	Product data sheet	-	74AHC_AHCT1G07 v.6
Modifications:	<ul style="list-style-type: none"> Section 4: table note added. 			
74AHC_AHCT1G07 v.6	20070607	Product data sheet	-	74AHC_AHCT1G07 v.5
Modifications:	<ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. Package SOT353 changed to SOT353-1 in Section 3 and Section 12. Quick reference data and Soldering sections removed. 			
74AHC_AHCT1G07 v.5	20021002	Product specification	-	74AHC_AHCT1G07 v.4
74AHC_AHCT1G07 v.4	20020606	Product specification	-	74AHC_AHCT1G07 v.3
74AHC_AHCT1G07 v.3	20020221	Product specification	-	74AHC_AHCT1G07 v.2
74AHC_AHCT1G07 v.2	20010209	Product specification	-	74AHC_AHCT1G07 v.1
74AHC_AHCT1G07 v.1	20000502	Product specification	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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