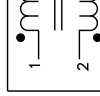


** Not all features are shown in this drawing. For more detailed information, please contact your Fair-Rite representative.

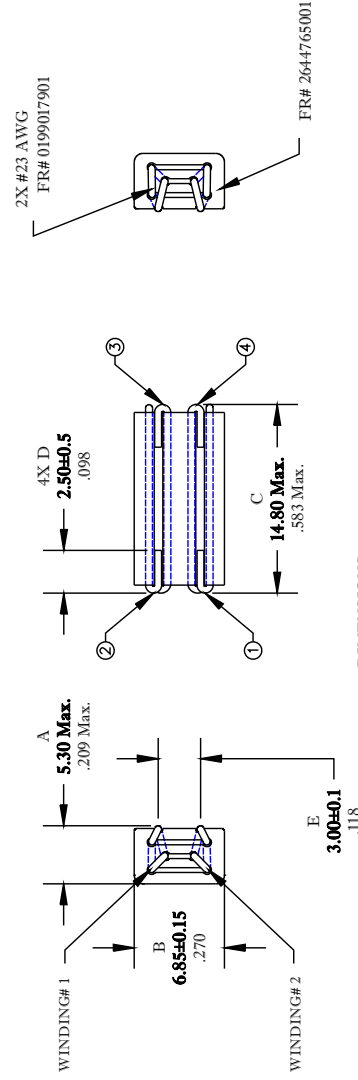
UNCONTROLLED ~CATALOG~ DRAWING

SIZE
B



IMPEDANCE VALUES AT
TERMINALS 1 AND 4
HP4191A, USING

| Impedance | 100 | 200 |
|-----------|-----|-----|
| 25 MHz | | |
| 116 Min. | | |



DIMENSIONS
BOLD NUMBERS ARE IN MILLIMETERS
LIGHT NUMBERS ARE IN INCHES

NOTES:

- 1 - PARTS MEETS FAIR-RITE PRODUCTS VISUAL INSPECTION CRITERIA FOR SURFACE MOUNT BEADS.
- 2 - PARTS MEET FAIR-RITE PRODUCTS SOP# ELC-27, FOR THERMAL SHOCK RESISTANCE.
- 3 - WIRE MEETS SOLDERABILITY SPECIFICATION WHEN TESTED PER SOP# ELC-57.
- 4 - COPLANARITY BETWEEN LEADS: 0.15mm [.006"] Max.
- 5 - PARTS ARE SUPPLIED TAPED AND REELED PER IEC 60286-3 AND EIA 481 STANDARDS.
- 7 - PARTS ARE TO BE 100% TESTED & 100% CERTIFIED FOR BREAKDOWN VOLTAGE BETWEEN CONDUCTORS: 1000 Vdc.

FR# 44

| | | | |
|------------------|------------|------------|-----|
| FAIR-RITE | | TITLE | |
| | | MULTI-TURN | |
| DRAWN BY | DATE | SIZE | SCA |
| JSERGENSICS | 06-29-2018 | B | |
| CHKD BY | | | |
| APPROV BY | | | |

Looking for pricing, stock, or lifecycle information?

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- ⊖ [View 2744065447 on WIN SOURCE](#)
- ⊖ [FAIR-RITE PRODUCTS](#) Information

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- ✓ Obsolete Management
- ✓ Cost Control Management
- ✓ Shortage Management
- ✓ Alternative Solution
- ✓ Excess Inventory Management