Ordering number : ENA0774A

SANYO Semiconductors

DATA SHEET

N-Channel Silicon MOSFET

2SK4096LS — General-Purpose Switching Device Applications

Features

- Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- Avalanche resistance guarantee.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		500	V
Gate-to-Source Voltage	V_{GSS}		±30	V
Drain Current (DC)	I_{Dc}*1	Limited only by maximum temperature	8	A
	I_{Dpack}*2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	7.1	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	32	A
Allowable Power Dissipation	P_D		2.0	W
		Tc=25°C (SANYO's ideal heat dissipation condition)*3	33	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		–55 to +150	°C
Avalanche Energy (Single Pulse) *4	E_{AS}		397	mJ
Avalanche Current *5	I_{AV}		8	A

*1 Shows chip capability

*2 Package limited

*3 SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

*4 V_{DD}=99V, L=10mH, I_{AV}=8A

*5 L≤10mH, single pulse

Marking : K4096

SANYO Semiconductor Co., Ltd.

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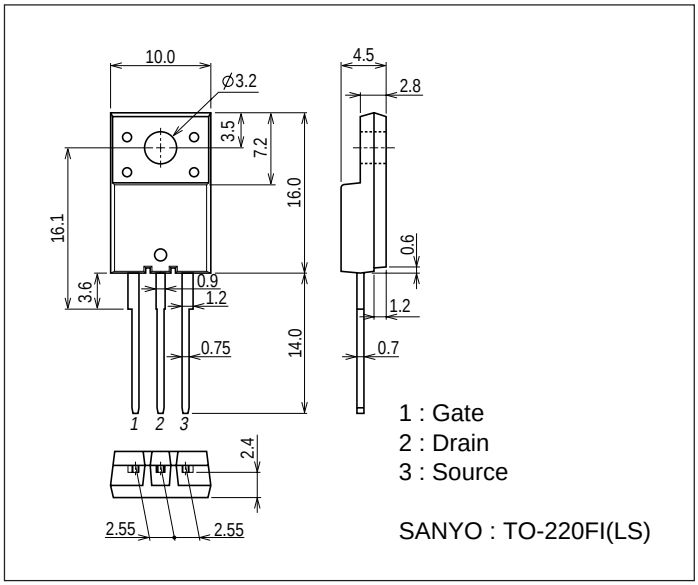
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=10mA, V_{GS}=0V	500			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=400V, V_{GS}=0V			100	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0V			±100	nA
Cutoff Voltage	$V_{GS}(off)$	V_{DS}=10V, I_D=1mA	3		5	V
Forward Transfer Admittance	$\|yfs\|$	V_{DS}=10V, I_D=4A	2.2	4.5		S
Static Drain-to-Source On-State Resistance	$R_{DS}(on)$	I_D=4A, V_{GS}=10V		0.65	0.85	Ω
Input Capacitance	Ciss	V_{DS}=30V, f=1MHz		600		pF
Output Capacitance	Coss	V_{DS}=30V, f=1MHz		130		pF
Reverse Transfer Capacitance	Crss	V_{DS}=30V, f=1MHz		28		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		18.5		ns
Rise Time	t_r	See specified Test Circuit.		46		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		75		ns
Fall Time	t_f	See specified Test Circuit.		33		ns
Total Gate Charge	Qg	V_{DS}=200V, V_{GS}=10V, I_D=8A		24		nC
Gate-to-Source Charge	Qgs	V_{DS}=200V, V_{GS}=10V, I_D=8A		4.5		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=200V, V_{GS}=10V, I_D=8A		14		nC
Diode Forward Voltage	V_{SD}	I_S=8A, V_{GS}=0V		0.9	1.2	V

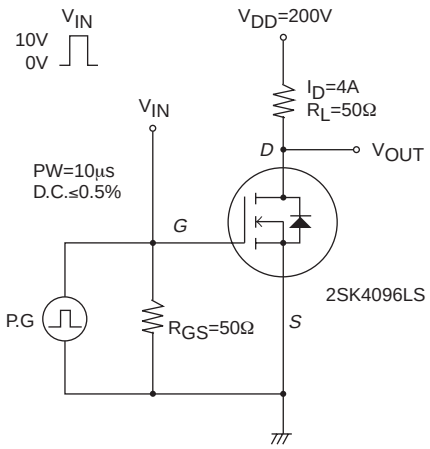
Package Dimensions

unit : mm (typ)

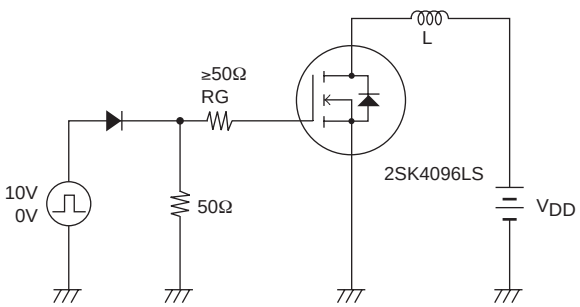
7509-002

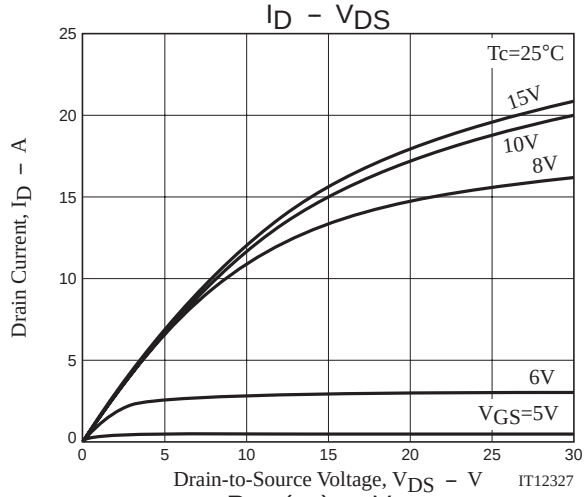


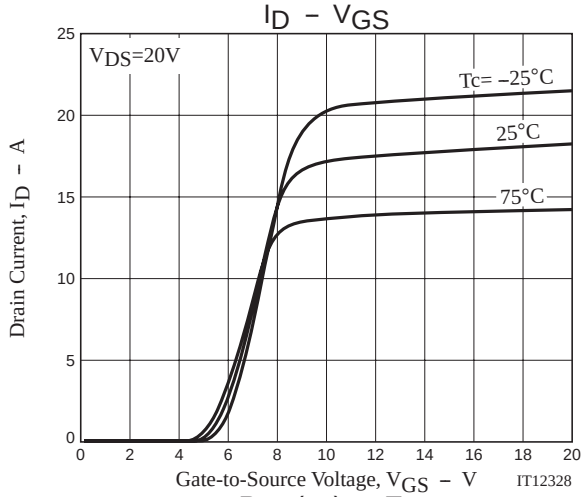
Switching Time Test Circuit

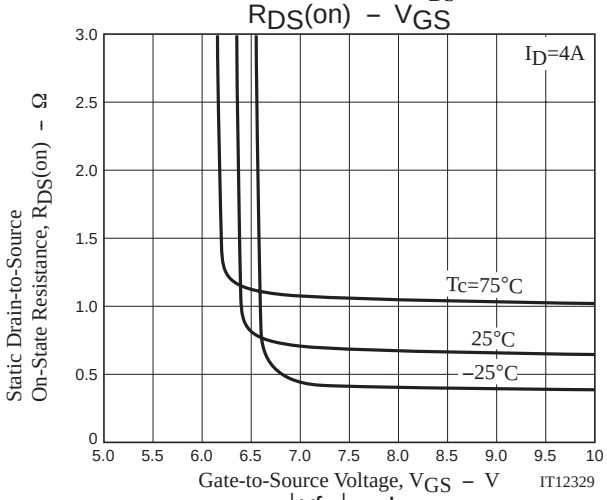


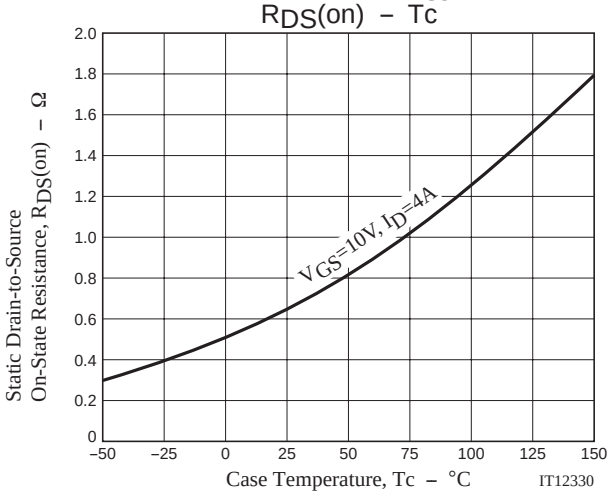
Avalanche Resistance Test Circuit

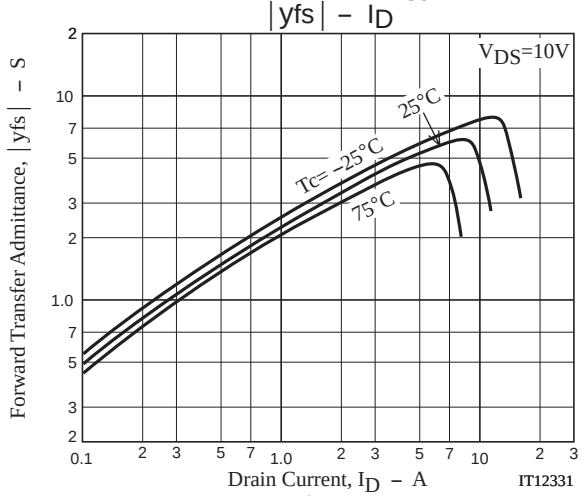


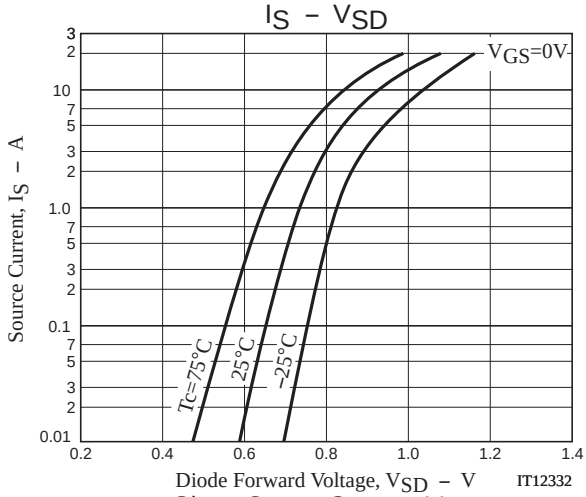

ID – VDS
Tc=25°C
15V
10V
8V
6V
VGS=5V
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT12327

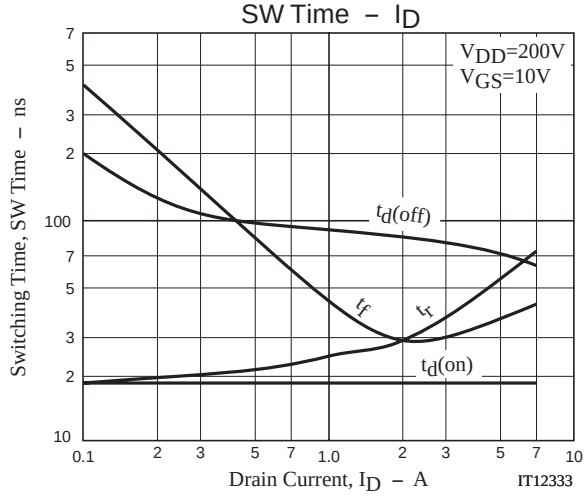

ID – VGS
VDS=20V
Tc= –25°C
25°C
75°C
Drain Current, ID – A
Gate-to-Source Voltage, VGS – V
IT12328

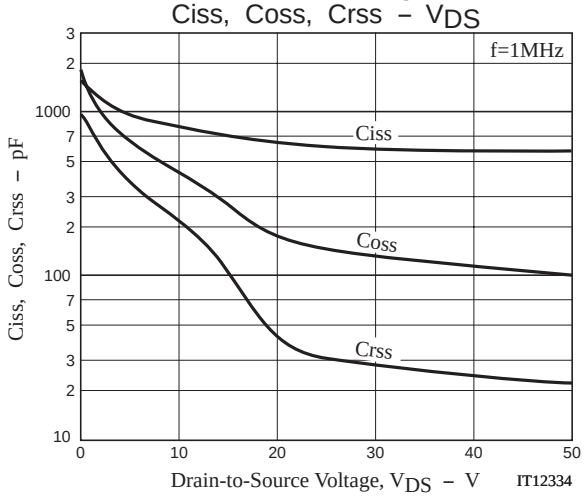

RDS(on) – VGS
ID=4A
Tc=75°C
25°C
–25°C
Static Drain-to-Source On-State Resistance, RDS(on) – Ω
Gate-to-Source Voltage, VGS – V
IT12329

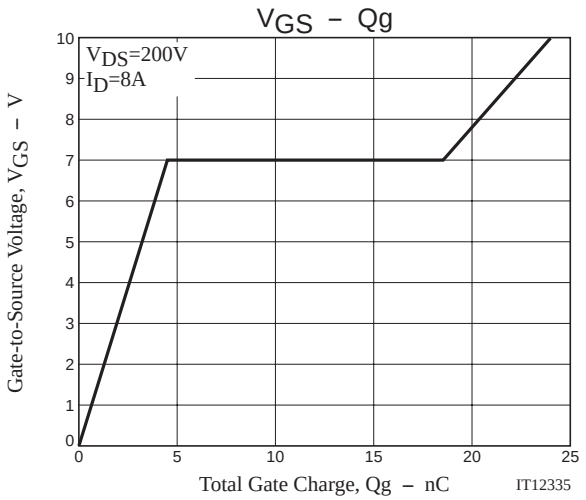

RDS(on) – Tc
VGS=10V, ID=4A
Static Drain-to-Source On-State Resistance, RDS(on) – Ω
Case Temperature, Tc – °C
IT12330

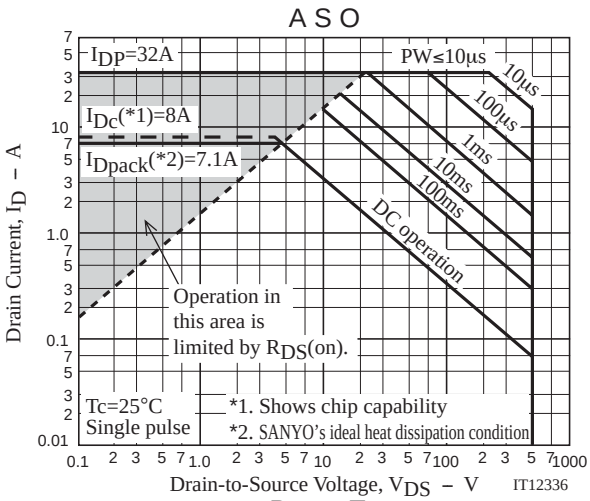

|yfs| – ID
VDS=10V
25°C
Tc= –25°C
75°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A
IT12331

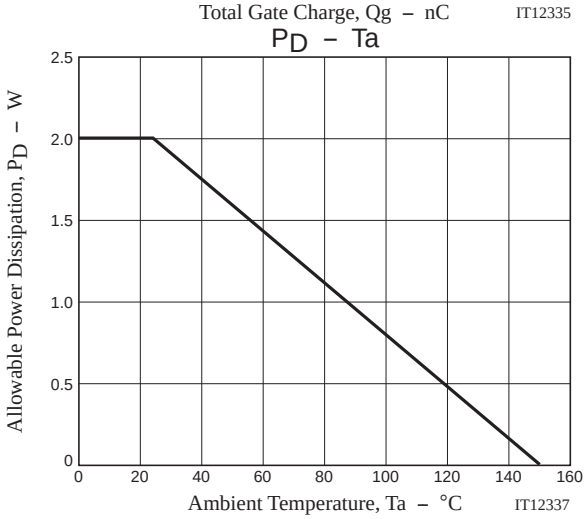

IS – VSD
VGS=0V
Tc=75°C
25°C
–25°C
Source Current, IS – A
Diode Forward Voltage, VSD – V
IT12332

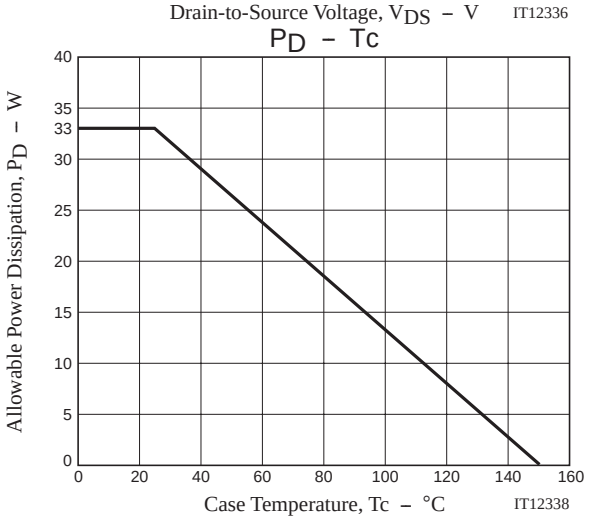

SW Time – ID
VDD=200V
VGS=10V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A
IT12333

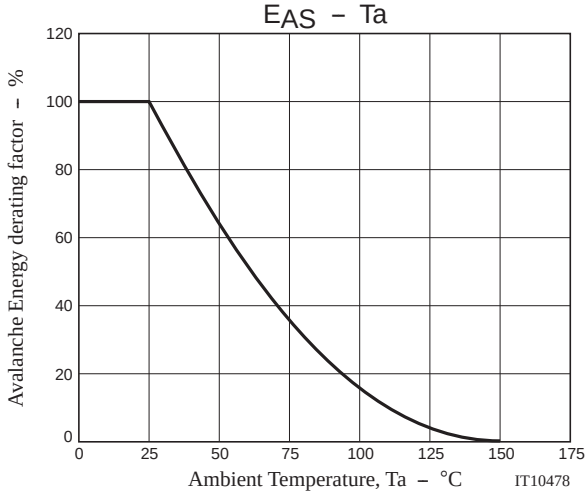

Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT12334


VGS – Qg
VDS=200V
ID=8A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT12335


A S O
IDP=32A
PW≤10μs
10μs
100μs
1ms
10ms
100ms
DC operation
IDC(*1)=8A
IDpack(*2)=7.1A
Operation in this area is limited by RDS(on).
Tc=25°C
Single pulse
*1. Shows chip capability
*2. SANYO's ideal heat dissipation condition
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT12336


PD – Ta
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT12337


PD – Tc
Allowable Power Dissipation, PD – W
Case Temperature, Tc – °C
IT12338


EAS – Ta
Avalanche Energy derating factor – %
Ambient Temperature, Ta – °C
IT10478

Note on usage : Since the 2SK4096LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

This catalog provides information as of October, 2007. Specifications and information herein are subject to change without notice.

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