

NE662M04 / 2SC5508 JEITA Part No.

Data Sheet

NPN SILICON RF TRANSISTOR
 FOR LOW-NOISE, HIGH-GAIN AMPLIFICATION
 FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04)

R09DS0055EJ0200
 Rev.2.00
 Mar 5, 2013

FEATURES

- Ideal for low-noise, high-gain amplification applications
- $NF = 1.1$ dB TYP., $G_a = 16$ dB TYP. @ $V_{CE} = 2$ V, $I_C = 5$ mA, $f = 2$ GHz
- Maximum available power gain: $MAG = 19$ dB TYP. @ $V_{CE} = 2$ V, $I_C = 20$ mA, $f = 2$ GHz
- $f_T = 25$ GHz technology adopted
- Flat-lead 4-pin thin-type super minimold (M04) package

<R> ORDERING INFORMATION

Part Number	Order Number	Quantity	Package	Supplying Form
NE662M04 2SC5508	NE662M04-A 2SC5508-A	50 pcs (Non reel)	Flat-lead 4-pin thin-type super minimold (M04) (Pb-Free)	<ul style="list-style-type: none"> • 8 mm wide embossed taping • Pin 1 (Emitter), Pin 2 (Collector) face the perforation side of the tape
NE662M04-T2 2SC5508-T2	NE662M04-T2-A 2SC5508-T2-A	3 kpcs/reel		
NE662M04-T2B 2SC5508-T2B	NE662M04-T2B-A 2SC5508-T2B-A	15 kpcs/reel		

Remark To order evaluation samples, please contact your nearby sales office.
 The unit sample quantity is 50 pcs.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$)

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V_{CBO}	15	V
Collector to Emitter Voltage	V_{CEO}	3.3	V
Emitter to Base Voltage	V_{EBO}	1.5	V
Collector Current	I_C	35	mA
Total Power Dissipation	P_{tot} <small>Note</small>	115	mW
Junction Temperature	T_j	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-65 to +150	$^\circ\text{C}$

Note Free air.

THERMAL RESISTANCE

Parameter	Symbol	Ratings	Unit
Junction to Case Resistance	$R_{th\ j-c}$	150	$^\circ\text{C} / \text{W}$
Junction to Ambient Resistance	$R_{th\ j-a}$	650	$^\circ\text{C} / \text{W}$

CAUTION

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

ELECTRICAL CHARACTERISTICS (T_A = +25 °C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Collector Cut-off Current	I _{CBO}	V _{CB} = 5 V, I _E = 0	–	–	200	nA
Emitter Cut-off Current	I _{EBO}	V _{EB} = 1 V, I _C = 0	–	–	200	nA
DC Current Gain	h _{FE} ^{Note 1}	V _{CE} = 2 V, I _C = 5 mA	50	70	100	–
RF Characteristics						
Gain Bandwidth Product	f _T	V _{CE} = 3 V, I _C = 30 mA, f = 2 GHz	20	25	–	GHz
Insertion Power Gain	S _{21e} ²	V _{CE} = 2 V, I _C = 20 mA, f = 2 GHz	14	17	–	dB
Noise Figure	NF	V _{CE} = 2 V, I _C = 5 mA, f = 2 GHz, Z _S = Z _{opt}	–	1.1	1.5	dB
Reverse Transfer Capacitance	C _{re} ^{Note 2}	V _{CB} = 2 V, I _E = 0, f = 1 MHz	–	0.18	0.24	pF
Maximum Available Power Gain	MAG ^{Note 3}	V _{CE} = 2 V, I _C = 20 mA, f = 2 GHz	–	19	–	dB
Maximum Stable Power Gain	MSG ^{Note 4}	V _{CE} = 2 V, I _C = 20 mA, f = 2 GHz	–	20	–	dB
Gain 1 dB Compression Output Power	P _{O(1 dB)}	V _{CE} = 2 V, I _C = 20 mA ^{Note 5} , f = 2 GHz	–	11	–	dBm
3rd Order Intermodulation Distortion Output Intercept Point	OIP ₃	V _{CE} = 2 V, I _C = 20 mA ^{Note 5} , f = 2 GHz	–	22	–	dBm

Notes 1. Pulse measurement: PW ≤ 350 μs, Duty Cycle ≤ 2%

2. Collector to base capacitance when the emitter grounded

$$3. \text{ MAG} = \left| \frac{S_{21}}{S_{12}} \right| (K - \sqrt{K^2 - 1})$$

$$4. \text{ MSG} = \left| \frac{S_{21}}{S_{12}} \right|$$

5. Collector current when P_{O(1 dB)} is output

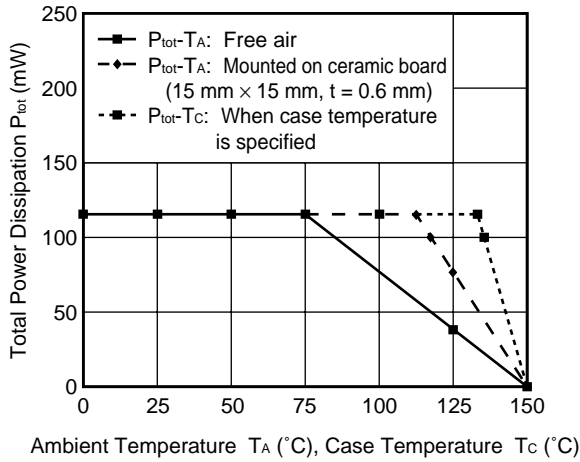
h_{FE} CLASSIFICATION

Rank	FB/YFB
Marking	T79
h _{FE} Value	50 to 100

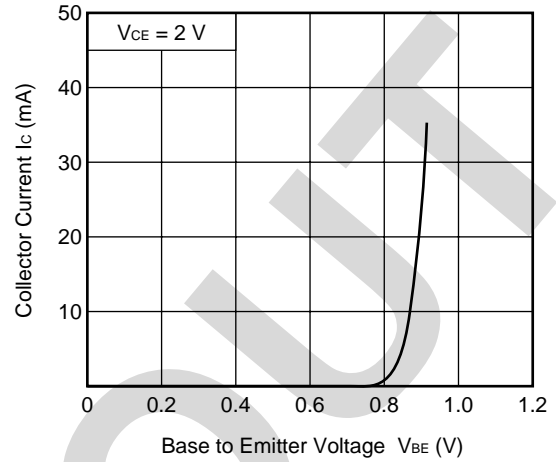
TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

Thermal/DC Characteristics

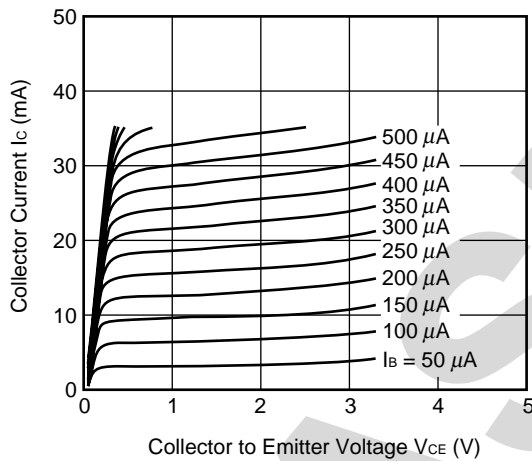
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE, CASE TEMPERATURE



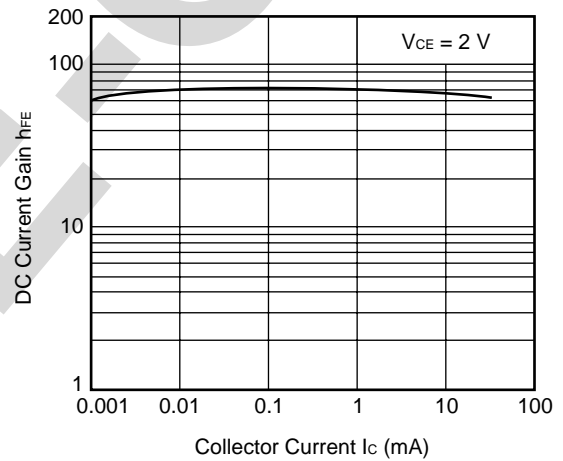
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

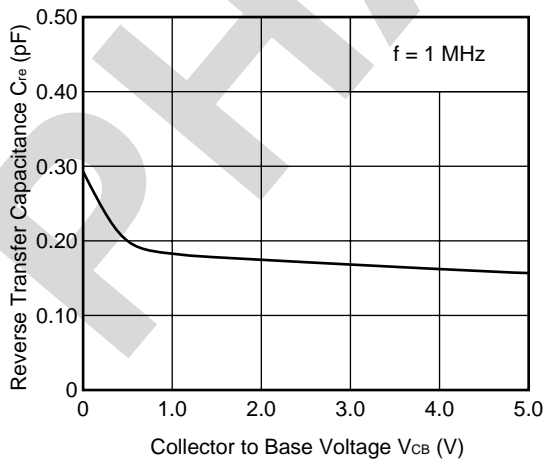


DC CURRENT GAIN vs. COLLECTOR CURRENT

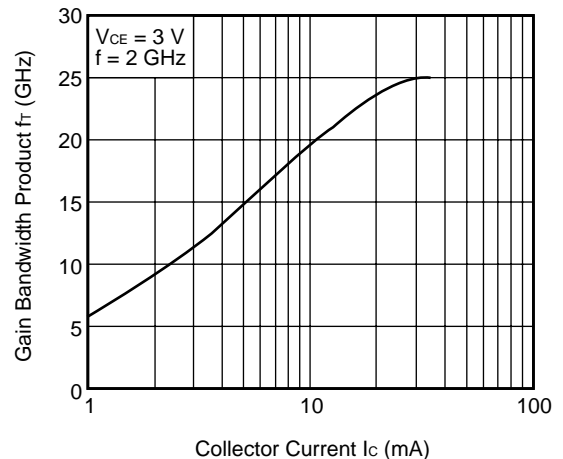


Capacitance/f_r Characteristics

REVERSE TRANSFER CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE



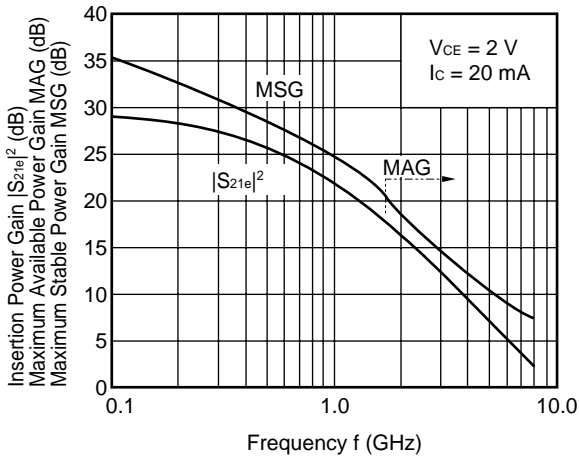
GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT



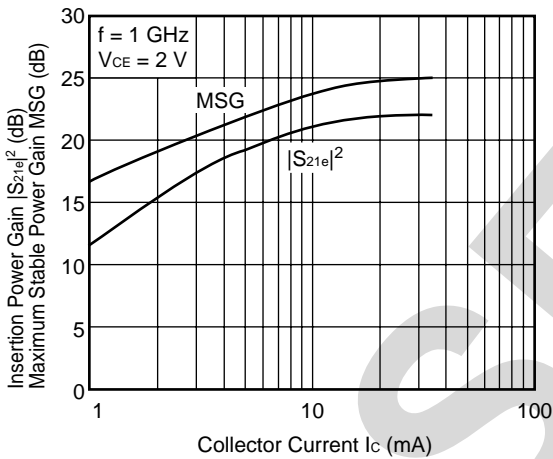
Remark The graphs indicate nominal characteristics.

Gain Characteristics

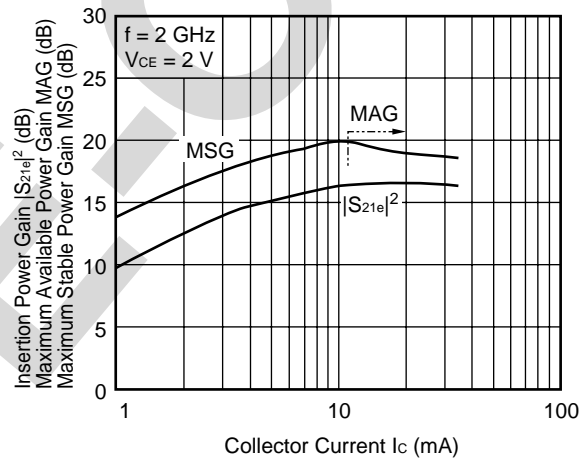
INSERTION POWER GAIN, MAG, MSG vs. FREQUENCY



INSERTION POWER GAIN, MSG vs. COLLECTOR CURRENT

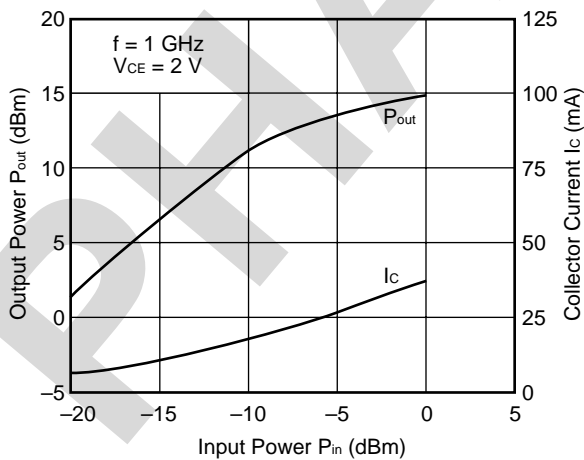


INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT

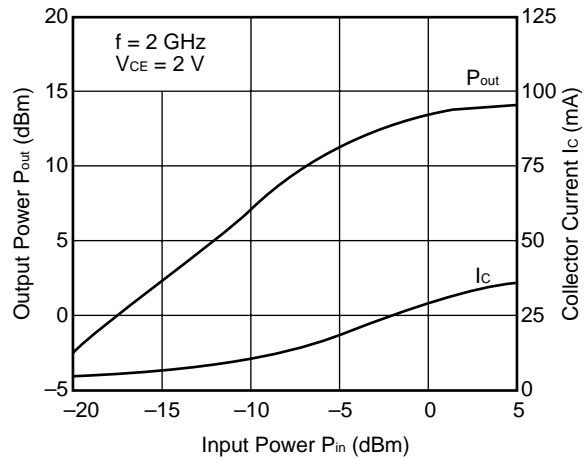


Output Characteristics

OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER

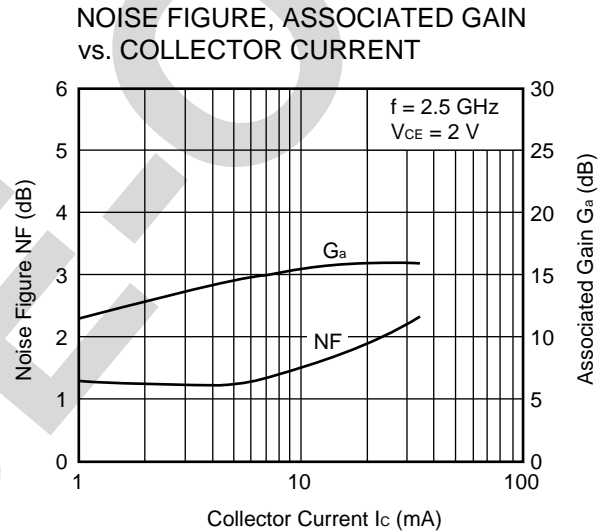
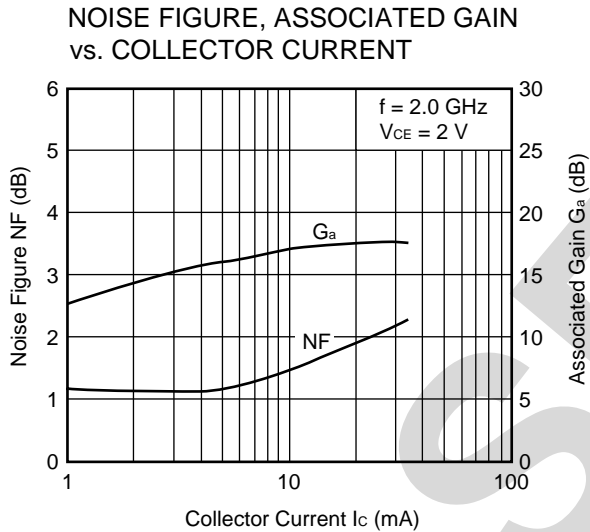
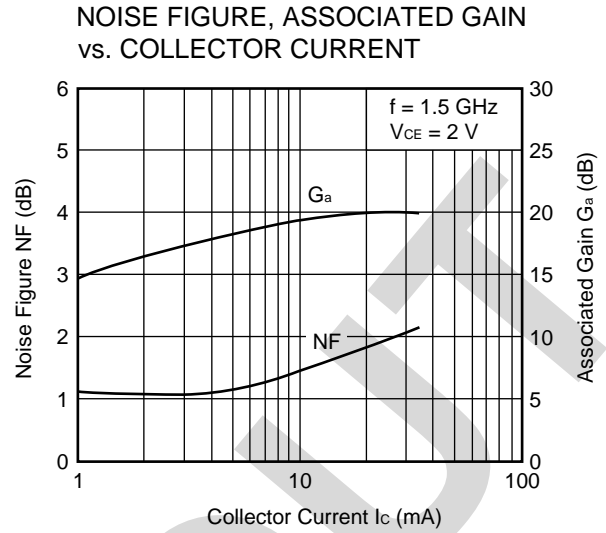
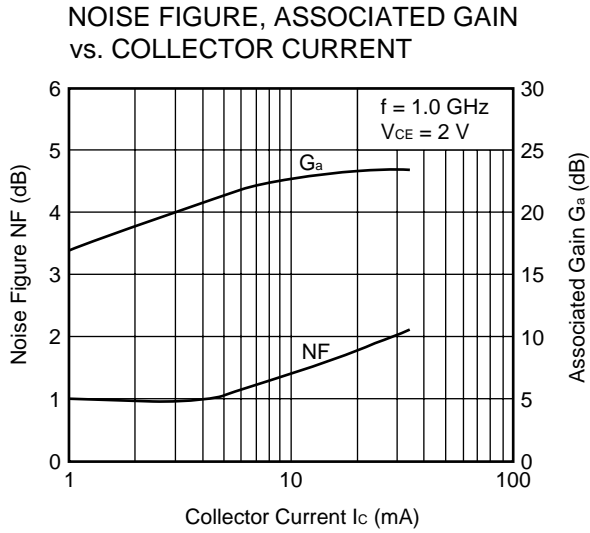


OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER



Remark The graphs indicate nominal characteristics.

Noise Characteristics



Remark The graphs indicate nominal characteristics.

<R> **S-PARAMETERS**

S-parameters and noise parameters are provided on our web site in a form (S2P) that enables direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.

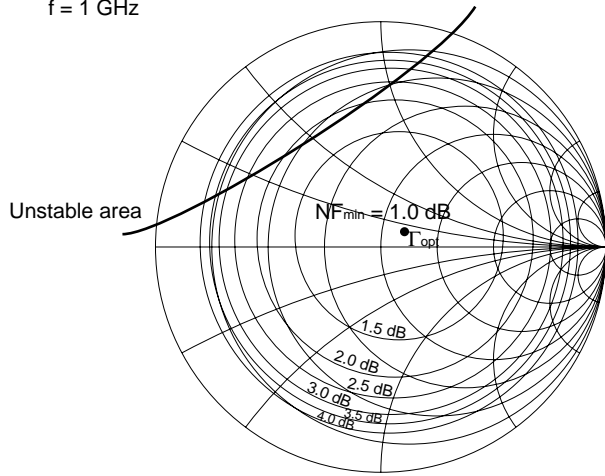
Click here to download S-parameters.

[Products] → [RF Devices] → [Device Parameters]

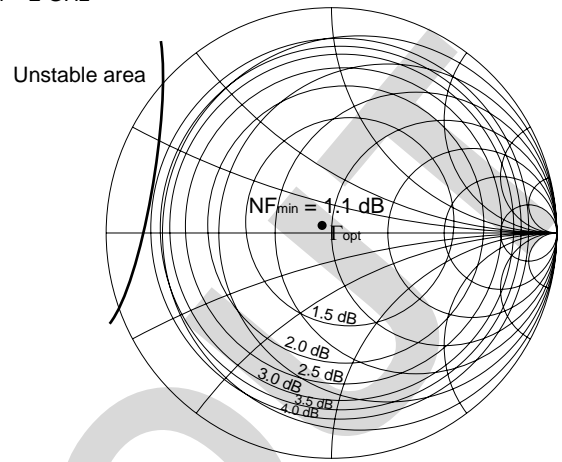
URL <http://www.renesas.com/products/microwave/>

EQUAL NF CIRCLE

$V_{CE} = 2\text{ V}$
 $I_C = 5\text{ mA}$
 $f = 1\text{ GHz}$



$V_{CE} = 2\text{ V}$
 $I_C = 5\text{ mA}$
 $f = 2\text{ GHz}$



PHASE-O

NOISE PARAMETERS

 $V_{CE} = 2\text{ V}, I_C = 3\text{ mA}$

f (GHz)	NF _{min} (dB)	G _a (dB)	Γ _{opt}		Rn/50
			MAG.	ANG.	
0.8	0.78	21.4	0.26	31.7	0.17
0.9	0.80	20.7	0.26	32.7	0.17
1.0	0.82	20.0	0.26	34.7	0.17
1.5	0.93	17.0	0.23	57.0	0.16
1.8	1.00	15.6	0.20	78.0	0.14
1.9	1.02	15.2	0.19	86.0	0.14
2.0	1.04	14.8	0.19	94.2	0.13
2.5	1.15	13.5	0.20	138.3	0.10

 $V_{CE} = 2\text{ V}, I_C = 5\text{ mA}$

f (GHz)	NF _{min} (dB)	G _a (dB)	Γ _{opt}		Rn/50
			MAG.	ANG.	
0.8	0.93	22.5	0.12	28.1	0.15
0.9	0.94	21.8	0.12	28.8	0.15
1.0	0.96	21.1	0.12	31.7	0.15
1.5	1.03	18.1	0.09	71.1	0.14
1.8	1.07	16.7	0.08	106.2	0.13
1.9	1.09	16.3	0.08	118.5	0.13
2.0	1.10	15.9	0.08	130.5	0.12
2.5	1.17	14.3	0.14	-179.7	0.11

 $V_{CE} = 2\text{ V}, I_C = 10\text{ mA}$

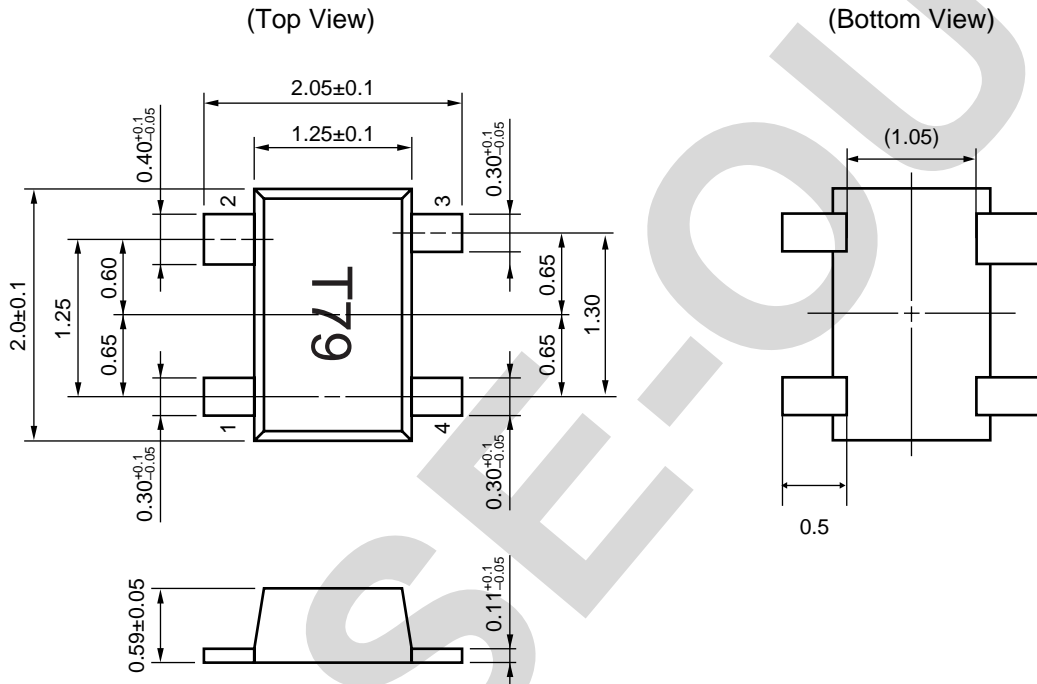
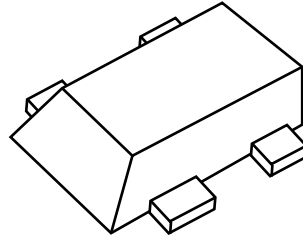
f (GHz)	NF _{min} (dB)	G _a (dB)	Γ _{opt}		Rn/50
			MAG.	ANG.	
0.8	1.28	23.7	0.07	-159.4	0.13
0.9	1.29	23.0	0.07	-157.5	0.13
1.0	1.30	22.3	0.08	-155.7	0.13
1.5	1.37	19.3	0.13	-149.2	0.13
1.8	1.41	17.8	0.16	-146.1	0.13
1.9	1.43	17.3	0.17	-145.0	0.13
2.0	1.44	16.9	0.19	-143.9	0.13
2.5	1.51	15.3	0.25	-136.7	0.13

 $V_{CE} = 2\text{ V}, I_C = 20\text{ mA}$

f (GHz)	NF _{min} (dB)	G _a (dB)	Γ _{opt}		Rn/50
			MAG.	ANG.	
0.8	1.59	24.5	0.26	-158.1	0.12
0.9	1.61	23.7	0.26	-155.5	0.13
1.0	1.63	23.0	0.27	-153.1	0.13
1.5	1.72	19.9	0.30	-142.6	0.14
1.8	1.78	18.3	0.33	-137.3	0.15
1.9	1.79	17.9	0.34	-135.7	0.06
2.0	1.81	17.5	0.35	-134.1	0.16
2.5	1.90	15.8	0.40	-126.5	0.18

<R> **PACKAGE DIMENSIONS**

FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04) PACKAGE (UNIT: mm)



PIN CONNECTIONS

- 1. Emitter
- 2. Collector
- 3. Emitter
- 4. Base

Revision History**NE662M04 / 2SC5508 Data Sheet**

Rev.	Date	Description	
		Page	Summary
1.00	Sep 9, 2004	–	First edition issued
2.00	Mar 5, 2013	Throughout	Renesas format is applied to this data sheet.
		p.1	ORDERING INFORMATION is modified.
		p.5	Up to date S-PARAMETERS.
		p.8	Added a drawing backside to PACKAGE DIMENSIONS.

All trademarks and registered trademarks are the property of their respective owners.

NOTICE

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. California Eastern Laboratories and Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
2. California Eastern Laboratories has used reasonable care in preparing the information included in this document, but California Eastern Laboratories does not warrant that such information is error free. California Eastern Laboratories and Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
3. California Eastern Laboratories and Renesas Electronics do not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of California Eastern Laboratories or Renesas Electronics or others.
4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below. "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc. "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc. Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. California Eastern Laboratories and Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by California Eastern Laboratories or Renesas Electronics.
6. You should use the Renesas Electronics products described in this document within the range specified by California Eastern Laboratories, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. California Eastern Laboratories shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or systems manufactured by you.
8. Please contact a California Eastern Laboratories sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. California Eastern Laboratories and Renesas Electronics assume no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations.
10. It is the responsibility of the buyer or distributor of California Eastern Laboratories, who distributes, disposes of, or otherwise places the Renesas Electronics product with a third party, to notify such third party in advance of the contents and conditions set forth in this document, California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics products.
11. This document may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of California Eastern Laboratories.
12. Please contact a California Eastern Laboratories sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

NOTE 1: "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

NOTE 2: "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.



NOTE 3: Products and product information are subject to change without notice.

CEL Headquarters • 4590 Patrick Henry Drive, Santa Clara, CA 95054 • Phone (408) 919-2500 • www.cel.com

For a complete list of sales offices, representatives and distributors,
Please visit our website: www.cel.com/contactus

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

-  [View 2SC5508-T2-A on WIN SOURCE](#)
-  [CEL Information](#)

Optimize Your Supply Chain with WIN SOURCE Solutions

-  Global Sourcing Solution
-  Obsolete Management
-  Cost Control Management
-  Shortage Management
-  Alternative Solution
-  Excess Inventory Management