



THE DATASHEET OF MMST5086

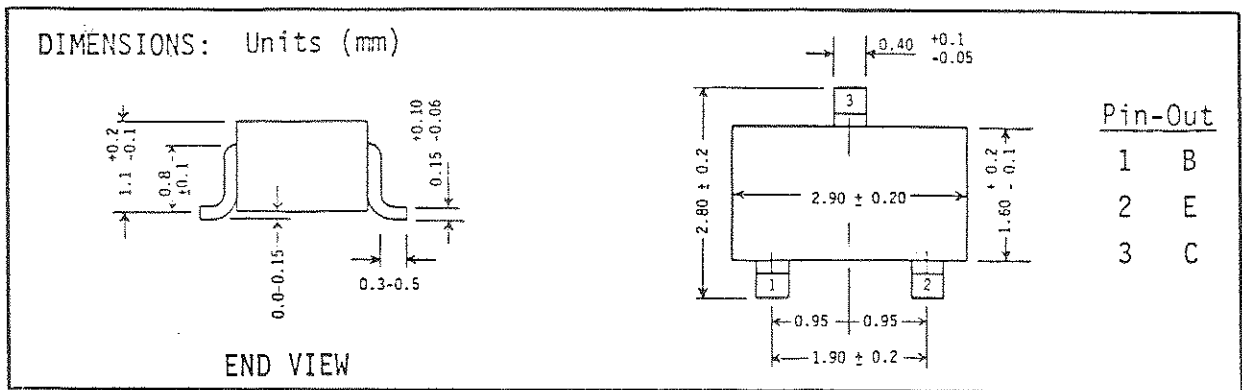


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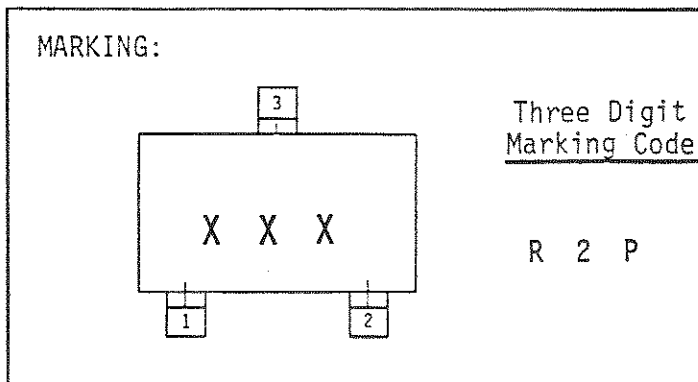
SUBJECT
SOT-23 TRANSISTOR, PNP, SILICON

ABSOLUTE MAXIMUM RATINGS: (Ta = 25°C)

Collector-Base Voltage	V _{CB0}	50 V
Collector-Emitter Voltage	V _{CE0}	50 V
Emitter-Base Voltage	V _{EB0}	3 V
Collector Current	I _C	200 mA
Power Dissipation-Free Air	P _D	200 mW
Power Dissipation-Ceramic Substrate	P _D	350 mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to 150 °C
Solder Temperature (10 seconds)		260 °C



THE JAPANESE STYLE SC-59 PACKAGE



- PACKAGING:
- _____ BULK, 500 per BAG
 - _____ MAGAZINES OF 50 EACH
 - _____ 8mm T&R, T- 146 3K/REEL
 - _____ 8mm T&R, T- 147 3K/REEL
 - _____ 8mm T&R, T- 24610K/REEL
 - _____ 8mm T&R, T- 24710K/REEL

REMARKS: PROCESS: A-22 Thermal Resistance R_{θJA} 625 °C/Watt
FREE AIR, T_A = 25°C



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ELECTRICAL CHARACTERISTICS: (Ta = 25°C Unless Otherwise Specified)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV _{CB0}	I _C = 100 μ A	50			V
BV _{CEO}	I _C = 1.0 mA	50			V
I _{CB0}	V _{CB} = 45 V		0.7	100	nA
I _{CEO}	V _{CE} = 40 V		0.7	100	nA
I _{EB0}	V _{EB} = 4.5 V		0.7	100	nA
h _{FE}	I _C = 100 μ A, V _{CE} = 5.0 V	150		500	
h _{FE}	I _C = 1.0 mA, V _{CE} = 5.0 V	150			
h _{FE}	I _C = 10 mA, V _{CE} = 5.0 V	150			
V _{CE(SAT)}	I _C = 10 mA, I _B = 1.0 mA		0.05	0.30	V
V _{BE(ON)}	I _C = 1.0 mA, V _{CE} = 5.0 V		0.60	0.85	V
f _T	I _C = 500 μ A, V _{CE} = 5 V, f = 20 MHz	40			MHz
h _{fe}	I _C = 1.0 mA, V _{CE} = 5 V, f = 1.0KHz	150		600	
C _{cb}	V _{CB} = 5.0 V, I _E = 0, f = 100 KHz		5.0	6.0	pF
NF	I _C = 20 μ A, V _{CE} = 5.0V, R _s =10K Ω f = 10 Hz to 15.7 KHz			3.0	dB
NF	I _C = 100 μ A, V _{CE} = 5.0V, R _s =3K Ω f = 1.0 KHz			3.0	dB

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MASTER



APPROVAL
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DESIGN

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