



**THE DATASHEET OF
PMV16UN,215**



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Kind regards,

Team Nexperia



PMV16UN

20 V, 5.8 A N-channel Trench MOSFET

Rev. 1 — 4 April 2011

Product data sheet

1. Product profile

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology

1.3 Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quick reference data

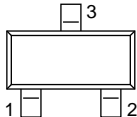
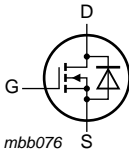
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ °C}$	-	-	20	V
V_{GS}	gate-source voltage		-8	-	8	V
I_D	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}$	[1]	-	5.8	A
Static characteristics						
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}; I_D = 5.8\text{ A}; T_j = 25\text{ °C}$	-	15	18	m Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 <p>SOT23 (TO-236AB)</p>	 <p>mbb076</p>
2	S	source		
3	D	drain		

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMV16UN	TO-236AB	plastic surface-mounted package; 3 leads	SOT23

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PMV16UN	KV%

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5. Limiting values

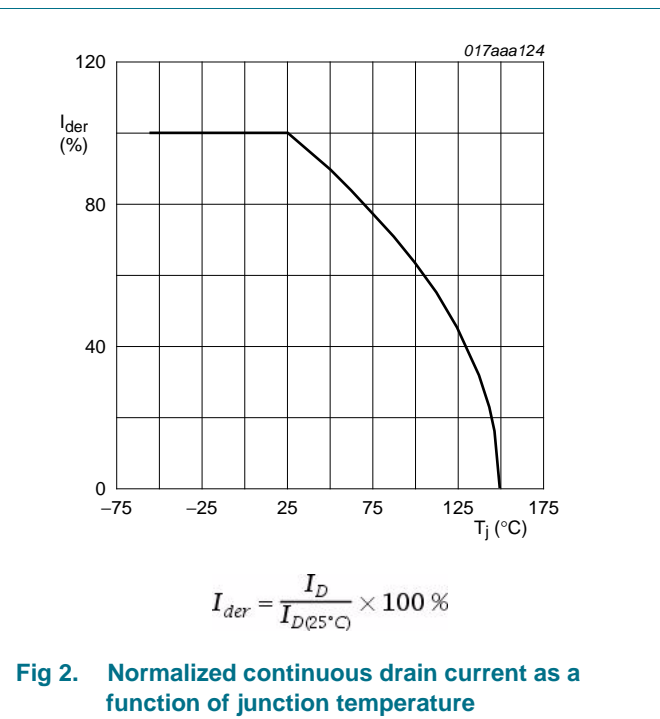
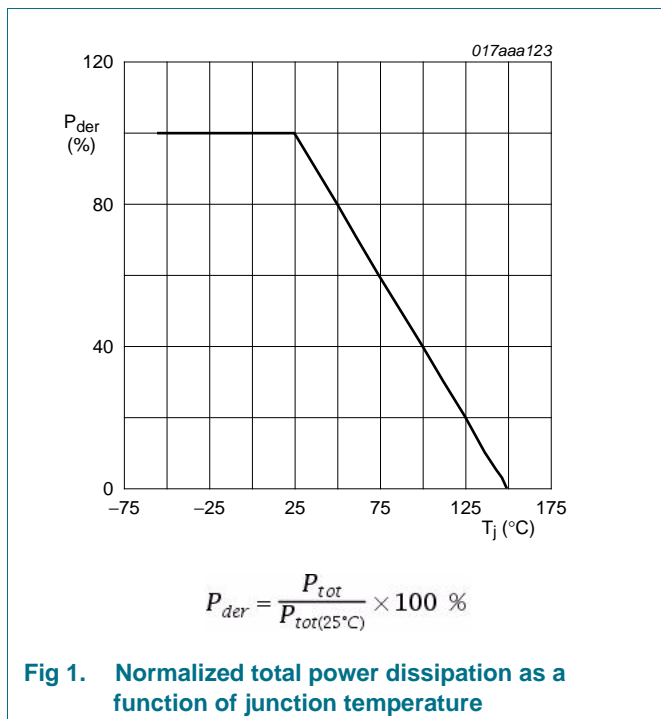
In accordance with the Absolute Maximum Rating System (IEC 60134).

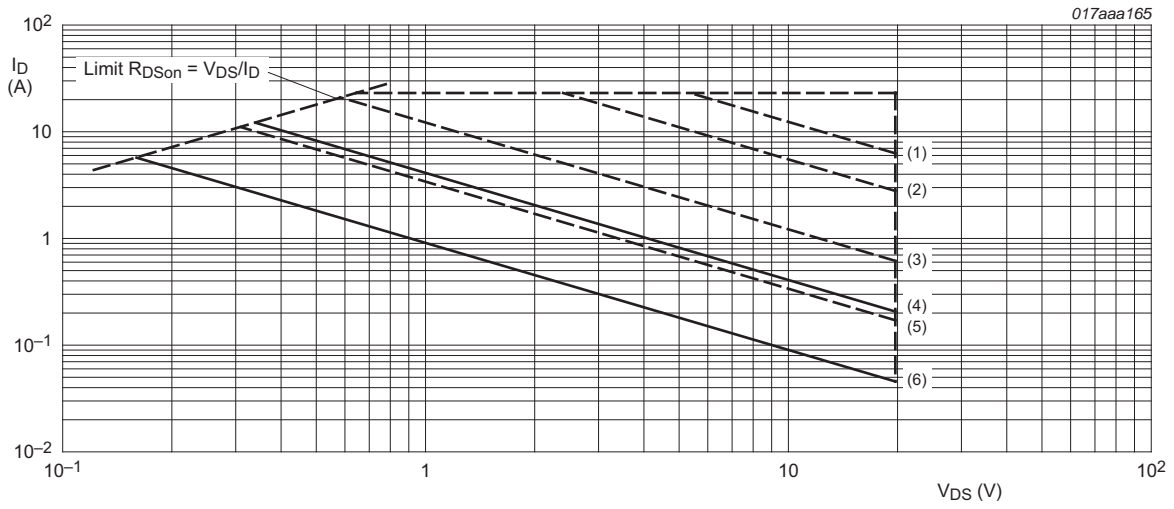
Symbol	Parameter	Conditions	Min	Max	Unit	
V _{DS}	drain-source voltage	T _j = 25 °C	-	20	V	
V _{GS}	gate-source voltage		-8	8	V	
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	5.8	A
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	3.6	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs	-	25	A	
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	510	mW
			[1]	-	930	mW
		T _{sp} = 25 °C		-	4170	mW
T _j	junction temperature		-55	150	°C	
T _{amb}	ambient temperature		-55	150	°C	
T _{stg}	storage temperature		-65	150	°C	

Source-drain diode

I _S	source current	T _{amb} = 25 °C	[1]	-	1	A
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- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.





I_{DM} = single pulse

(1) $t_p = 100 \mu s$

(2) $t_p = 1 ms$

(3) $t_p = 10 ms$

(4) DC; $T_{sp} = 25 \text{ }^\circ\text{C}$

(5) $t_p = 100 ms$

(6) DC; $T_{amb} = 25 \text{ }^\circ\text{C}$; drain mounting pad 6 cm^2

Fig 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	207	245	K/W
			[2]	-	116	135	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	20	30	K/W	

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

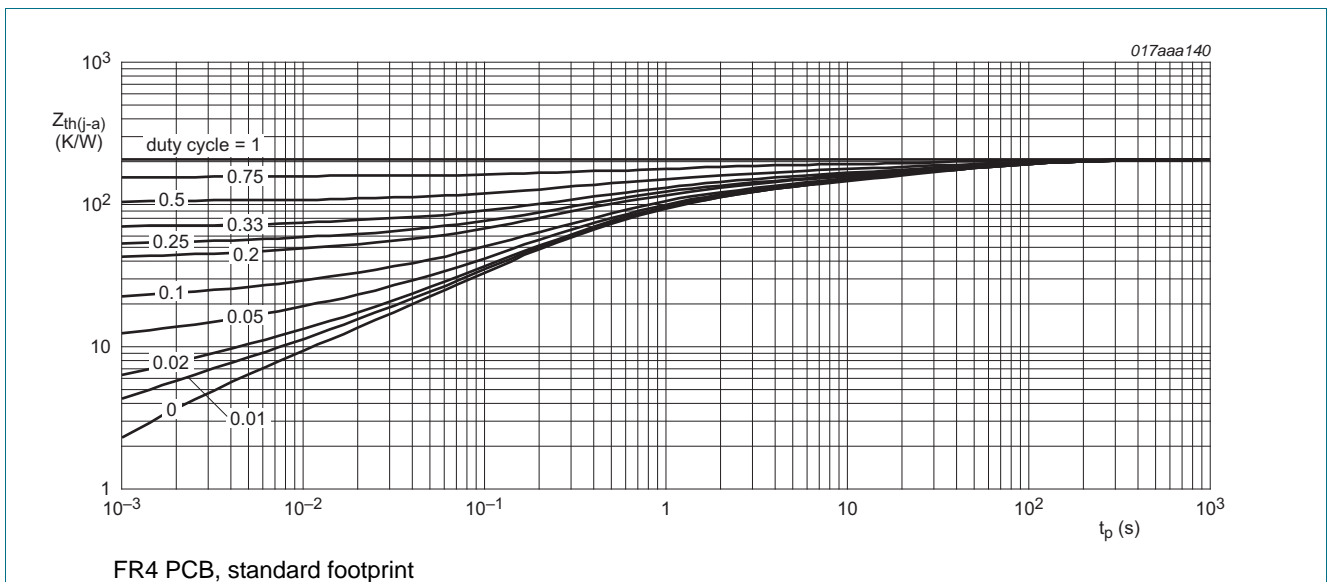


Fig 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

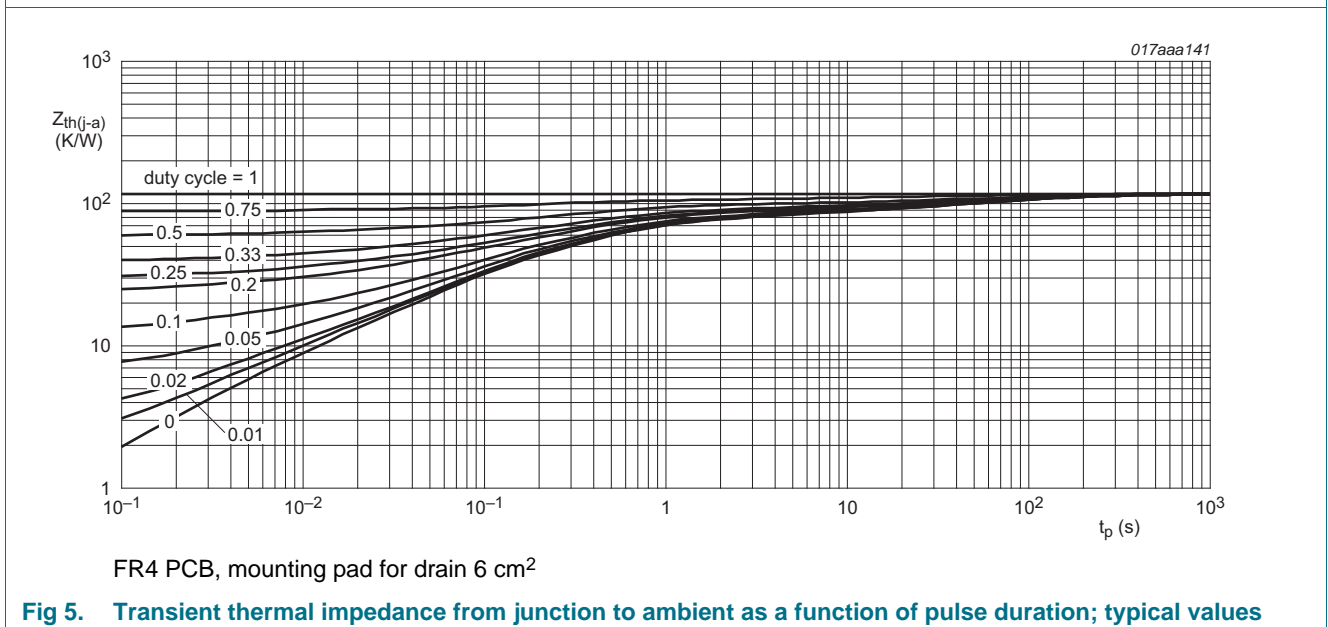
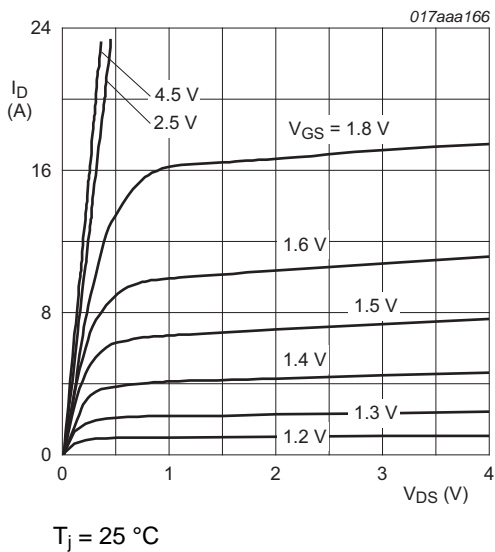


Fig 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

7. Characteristics

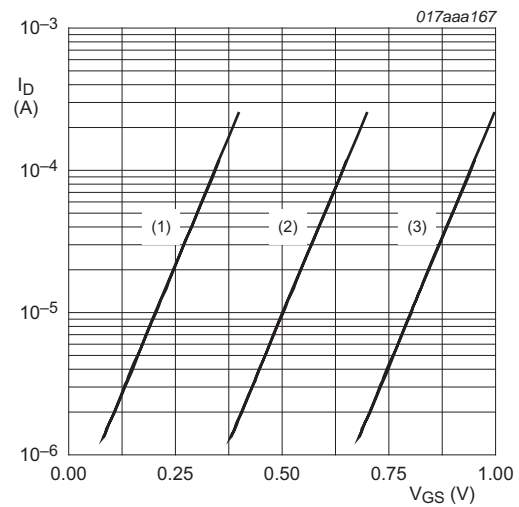
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu\text{A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ }^\circ\text{C}$	20	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \mu\text{A}$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ }^\circ\text{C}$	0.4	0.7	1	V
I_{DSS}	drain leakage current	$V_{DS} = 20 \text{ V}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	1	μA
		$V_{DS} = 20 \text{ V}$; $V_{GS} = 0 \text{ V}$; $T_j = 150 \text{ }^\circ\text{C}$	-	-	20	μA
I_{GSS}	gate leakage current	$V_{GS} = 8 \text{ V}$; $V_{DS} = 0 \text{ V}$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	100	nA
		$V_{GS} = -8 \text{ V}$; $V_{DS} = 0 \text{ V}$; $T_j = 25 \text{ }^\circ\text{C}$	-	-	100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}$; $I_D = 5.8 \text{ A}$; $T_j = 25 \text{ }^\circ\text{C}$	-	15	18	m Ω
		$V_{GS} = 4.5 \text{ V}$; $I_D = 5.8 \text{ A}$; $T_j = 150 \text{ }^\circ\text{C}$	-	23	28	m Ω
		$V_{GS} = 2.5 \text{ V}$; $I_D = 5.1 \text{ A}$; $T_j = 25 \text{ }^\circ\text{C}$	-	18	23	m Ω
		$V_{GS} = 1.8 \text{ V}$; $I_D = 3.9 \text{ A}$; $T_j = 25 \text{ }^\circ\text{C}$	-	25	40	m Ω
g_{fs}	forward transconductance	$V_{DS} = 5 \text{ V}$; $I_D = 3 \text{ A}$; $T_j = 25 \text{ }^\circ\text{C}$	-	18	-	S
Dynamic characteristics						
$Q_{G(tot)}$	total gate charge	$I_D = 3 \text{ A}$; $V_{DS} = 10 \text{ V}$; $V_{GS} = 4.5 \text{ V}$; $T_j = 25 \text{ }^\circ\text{C}$	-	7.4	11	nC
Q_{GS}	gate-source charge		-	1	-	nC
Q_{GD}	gate-drain charge		-	1.9	-	nC
C_{iss}	input capacitance	$V_{GS} = 0 \text{ V}$; $V_{DS} = 10 \text{ V}$; $f = 1 \text{ MHz}$; $T_j = 25 \text{ }^\circ\text{C}$	-	670	-	pF
C_{oss}	output capacitance		-	195	-	pF
C_{rss}	reverse transfer capacitance		-	85	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DS} = 10 \text{ V}$; $V_{GS} = 4.5 \text{ V}$; $R_{G(ext)} = 10 \text{ }^\circ\Omega$; $T_j = 25 \text{ }^\circ\text{C}$; $I_D = 5.8 \text{ A}$	-	12	-	ns
t_r	rise time		-	40	-	ns
$t_{d(off)}$	turn-off delay time		-	170	-	ns
t_f	fall time		-	85	-	ns
Source-drain diode						
V_{SD}	source-drain voltage	$I_S = 1 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ }^\circ\text{C}$	-	0.7	1.2	V



$T_j = 25\text{ }^\circ\text{C}$

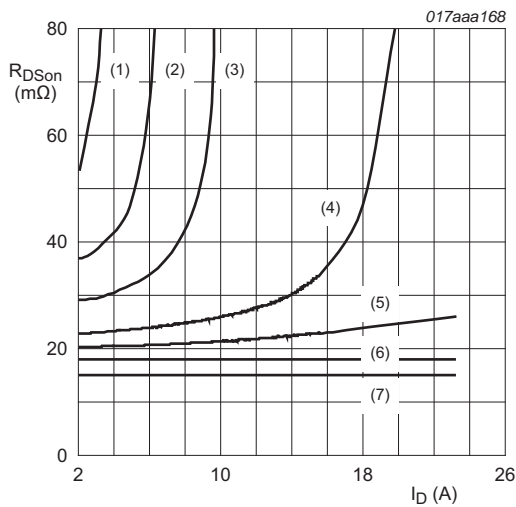
Fig 6. Output characteristics: drain current as a function of drain-source voltage; typical values



$T_j = 25\text{ }^\circ\text{C}; V_{DS} = 5\text{ V}$

- (1) minimum values
- (2) typical values
- (3) maximum values

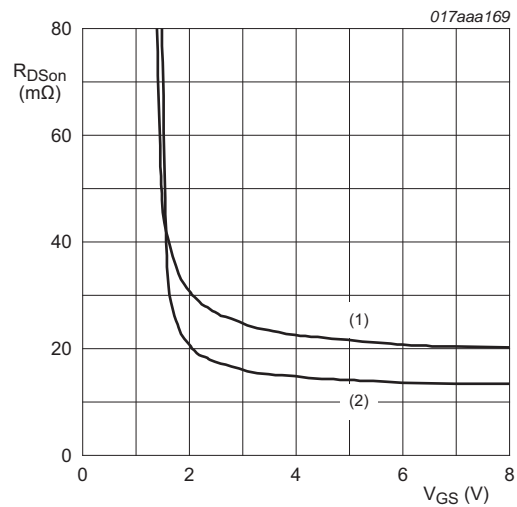
Fig 7. Sub-threshold drain current as a function of gate-source voltage



$T_j = 25\text{ }^\circ\text{C}$

- (1) $V_{GS} = 1.4\text{ V}$
- (2) $V_{GS} = 1.5\text{ V}$
- (3) $V_{GS} = 1.6\text{ V}$
- (4) $V_{GS} = 1.8\text{ V}$
- (5) $V_{GS} = 2.0\text{ V}$
- (6) $V_{GS} = 2.5\text{ V}$
- (7) $V_{GS} = 4.5\text{ V}$

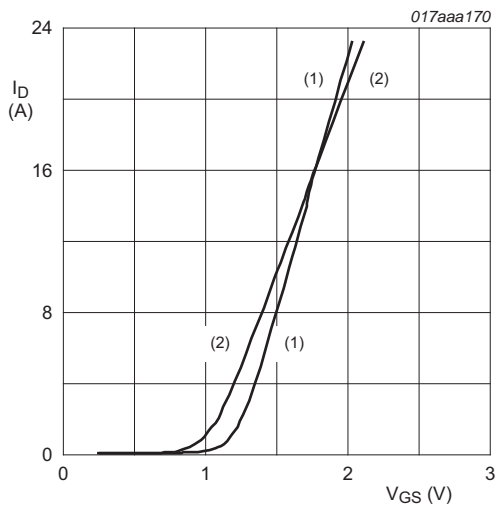
Fig 8. Drain-source on-state resistance as a function of drain current; typical values



$I_D = 5.7\text{ A}$

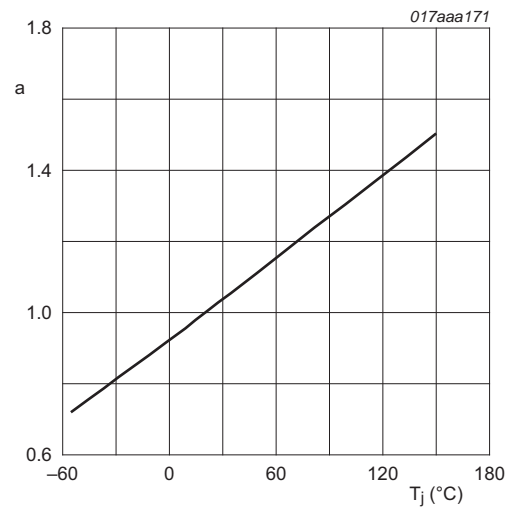
- (1) $T_j = 150\text{ }^\circ\text{C}$
- (2) $T_j = 25\text{ }^\circ\text{C}$

Fig 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



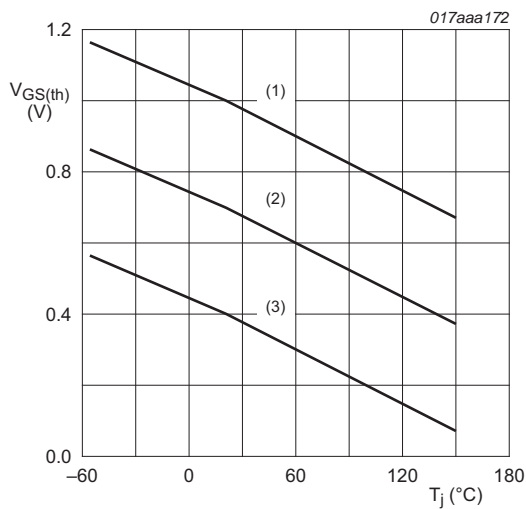
$V_{DS} > I_D \times R_{DS(on)}$
 (1) $T_j = 25\text{ °C}$
 (2) $T_j = 150\text{ °C}$

Fig 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values



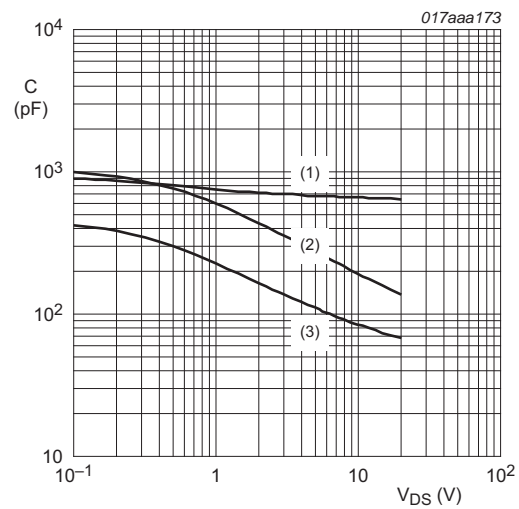
$$a = \frac{R_{DS(on)}}{R_{DS(on)@25^\circ C}}$$

Fig 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values



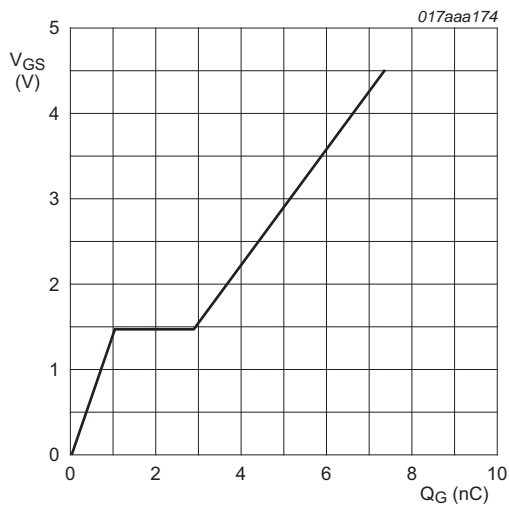
$I_D = 0.25\text{ mA}; V_{DS} = V_{GS}$
 (1) maximum values
 (2) typical values
 (3) minimum values

Fig 12. Gate-source threshold voltage as a function of junction temperature



$f = 1\text{ MHz}; V_{GS} = 0\text{ V}$
 (1) C_{iss}
 (2) C_{oss}
 (3) C_{rss}

Fig 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 3$ A; $V_{DS} = 10$ V; $T_{amb} = 25$ °C

Fig 14. Gate-source voltage as a function of gate charge; typical values

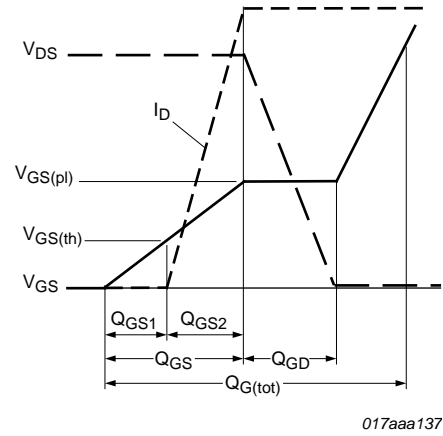
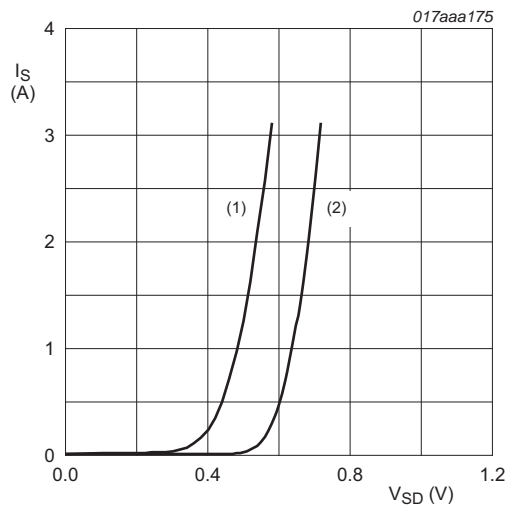


Fig 15. Gate charge waveform definitions



$V_{GS} = 0$ V
 (1) $T_j = 150$ °C
 (2) $T_j = 25$ °C

Fig 16. Source current as a function of source-drain voltage; typical values

8. Test information

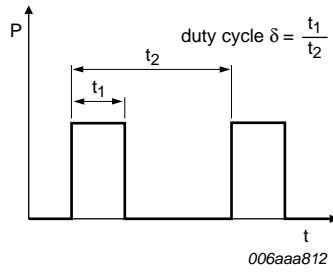


Fig 17. Duty cycle definition

9. Package outline

Plastic surface-mounted package; 3 leads

SOT23

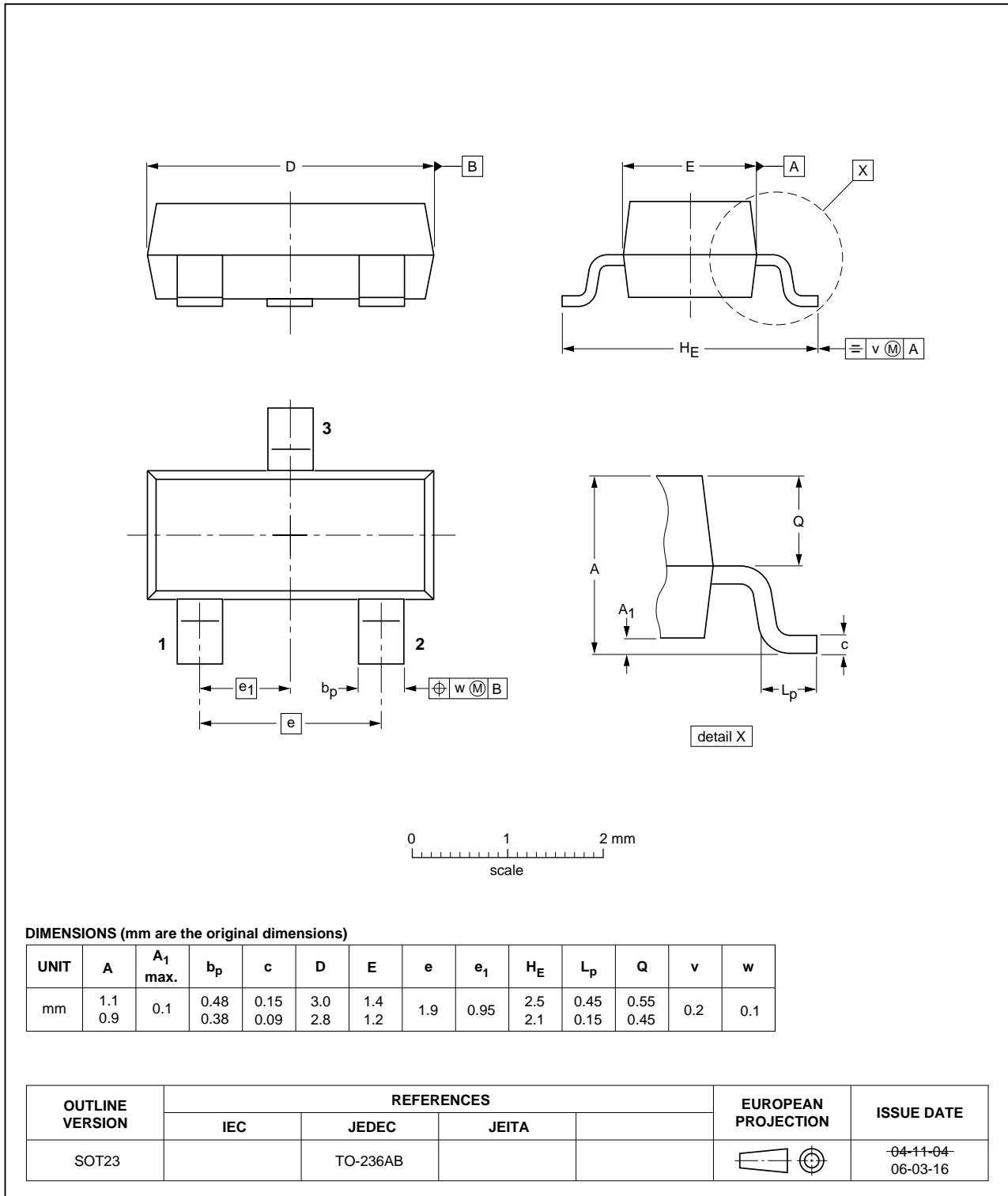


Fig 18. Package outline SOT23 (TO-236AB)

10. Soldering

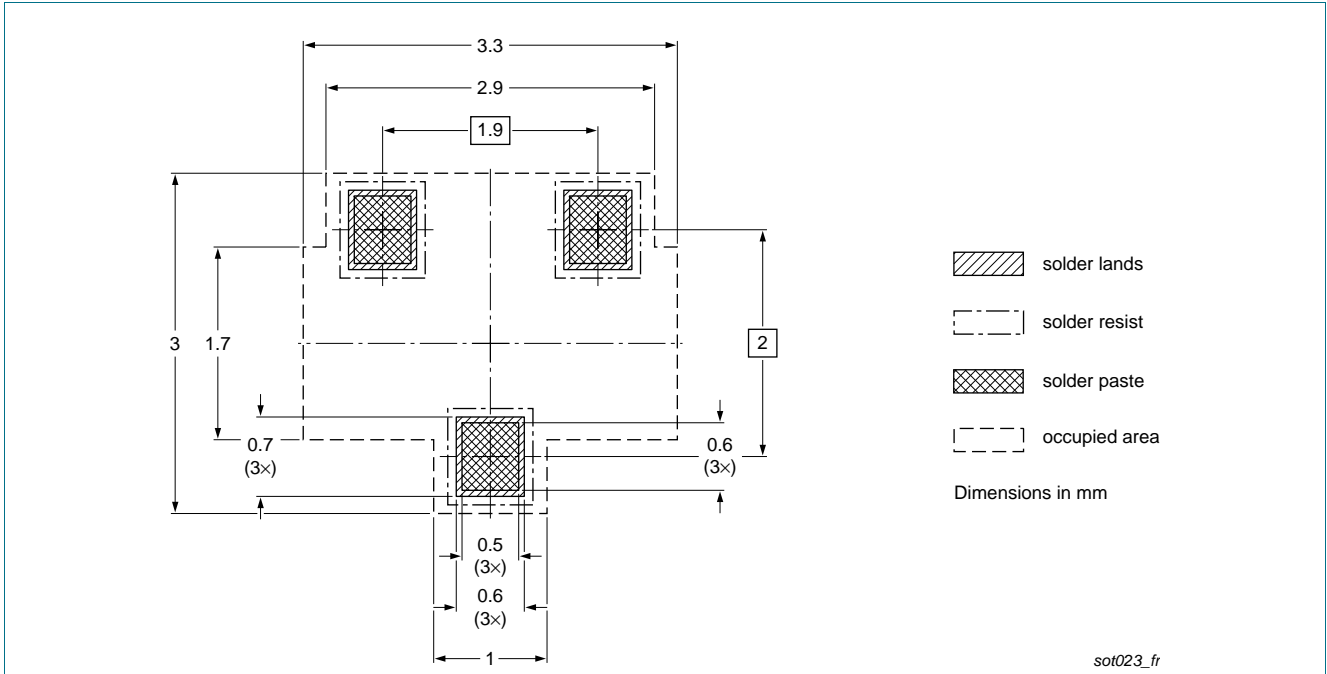


Fig 19. Reflow soldering footprint for SOT23 (TO-236AB)

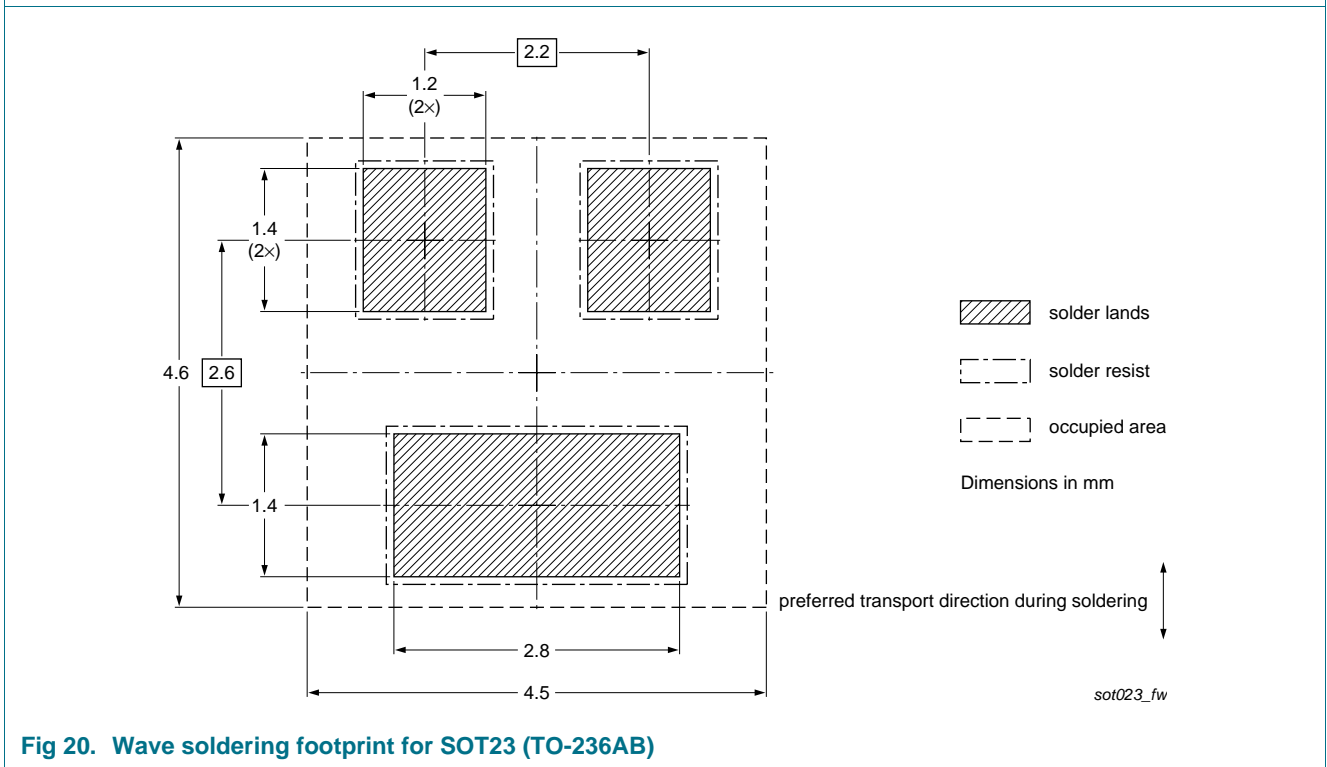


Fig 20. Wave soldering footprint for SOT23 (TO-236AB)

11. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMV16UN v.1	20110404	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^[1] ^[2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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Date of release: 4 April 2011

Document identifier: PMV16UN

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