



**THE DATASHEET OF
SBR2U30P1-7**



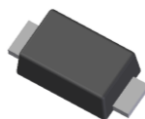
Features

- Patented Super Barrier Rectifier SBR[®] Technology
- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- ±16KV ESD Protection (HBM, 3B)
- ±25KV ESD Protection (IEC61000-4-2 Level 4, Air Discharge)
- **Lead Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: POWERDI[®]123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity Indicator: Cathode Band
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ^(E3)
- Weight: 0.018 grams (Approximate)

POWERDI[®]123



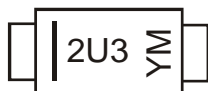
Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
SBR2U30P1-7	POWERDI [®] 123	3,000/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



2U3 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: C = 2015)
 M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	T	U	V	W	X	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	30	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _{RM}		
RMS Reverse Voltage	V _{R(RMS)}	21	V
Average Rectified Output Current (See Figure 1)	I _O	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	75	A
Non-Repetitive Avalanche Energy (T _J = +25°C, I _{AS} = 5A, L = 8.5mH)	E _{AS}	105	mJ
Repetitive Peak Avalanche Energy (T _P = 1μs, T _J = +25°C)	P _{ARM}	1,100	W

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance	R _{θJS} R _{θJA} R _{θJA}	5 178 123	°C/W
Thermal Resistance Junction to Soldering (Note 5)			
Thermal Resistance Junction to Ambient (Note 6)			
Thermal Resistance Junction to Ambient (Note 7)			
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V _{(BR)R}	30	-	-	V	I _R = 400μA
Forward Voltage Drop	V _F	-	0.22	0.26	V	I _F = 0.1A, T _J = +25°C
		-	0.31	0.35		I _F = 1.0A, T _J = +25°C
		-	0.36	0.40		I _F = 2.0A, T _J = +25°C
		-	0.12	0.15		I _F = 0.1A, T _J = +125°C
		-	0.27	0.30		I _F = 1.0A, T _J = +125°C
		-	0.30	0.33		I _F = 2.0A, T _J = +125°C
Leakage Current (Note 8)	I _R	-	75	150	μA	V _R = 5V, T _J = +25°C
		-	150	400	μA	V _R = 30V, T _J = +25°C
		-	6	15	mA	V _R = 5V, T _J = +125°C
		-	12	20	mA	V _R = 30V, T _J = +125°C

- Notes:
- Theoretical R_{θJS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.
 - FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com/datasheets/ap02001.pdf>.
 - Polymide PCB, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com/datasheets/ap02001.pdf>.
 - Short duration pulse test used to minimize self-heating effect.

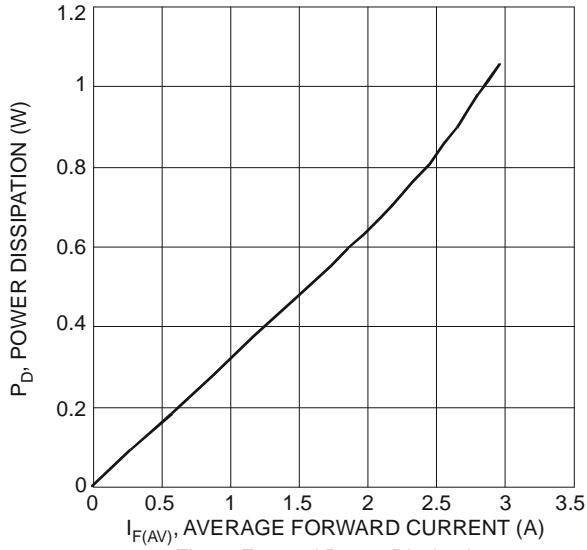


Fig. 1 Forward Power Dissipation

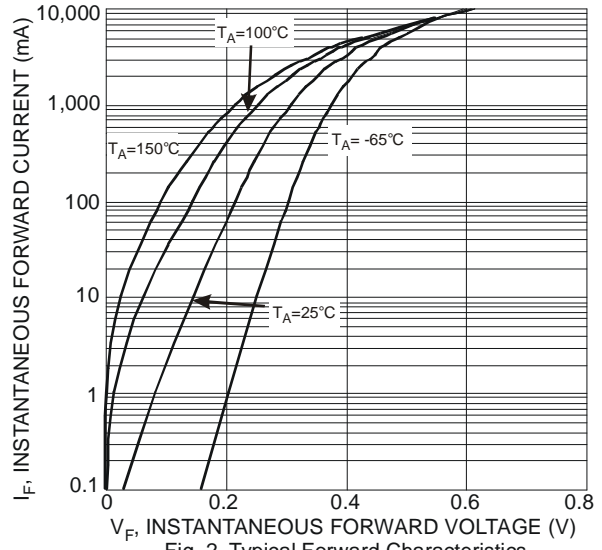


Fig. 2 Typical Forward Characteristics

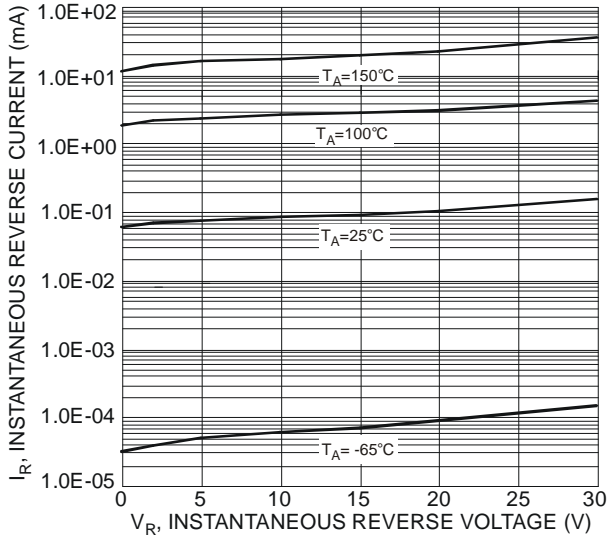


Fig. 3 Typical Reverse Characteristics

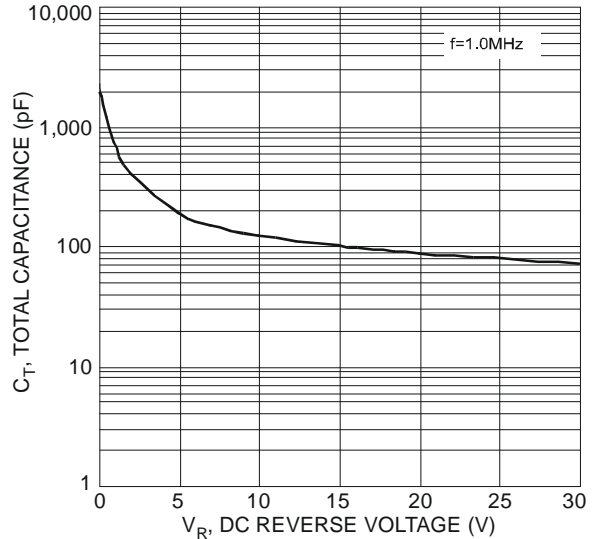


Fig. 4 Total Capacitance vs. Reverse Voltage

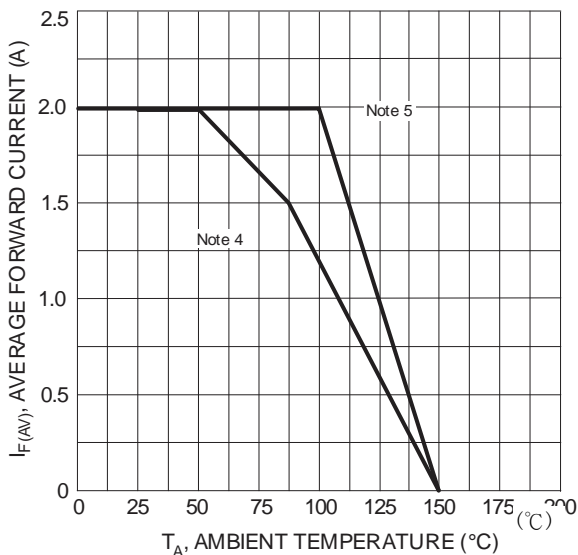


Fig. 5 Forward Current Derating Curve

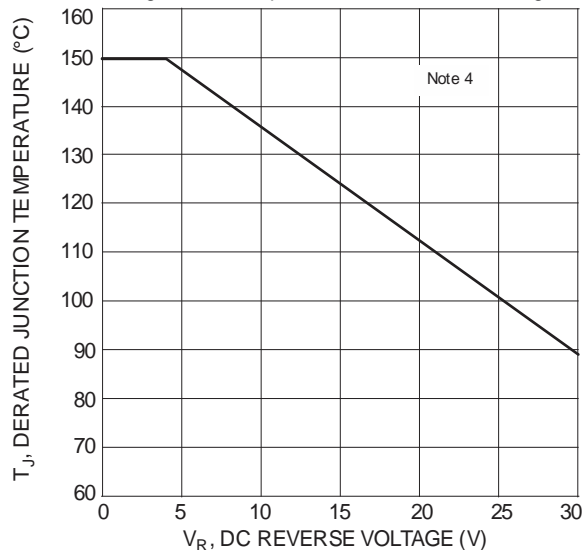


Fig. 6 Operating Temperature Derating

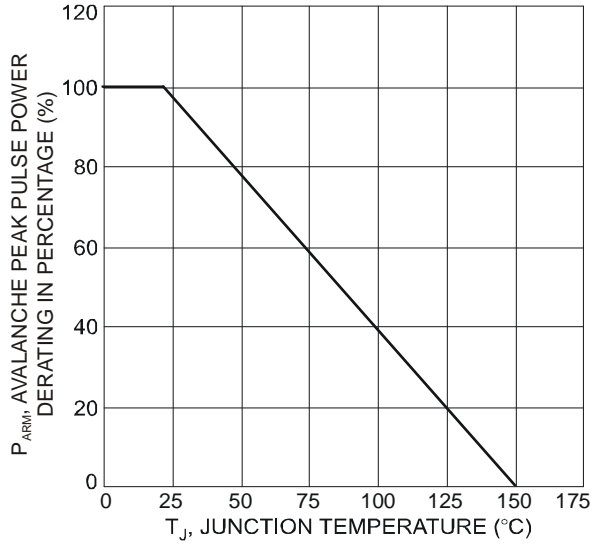


Fig. 7 Pulse Derating Curve

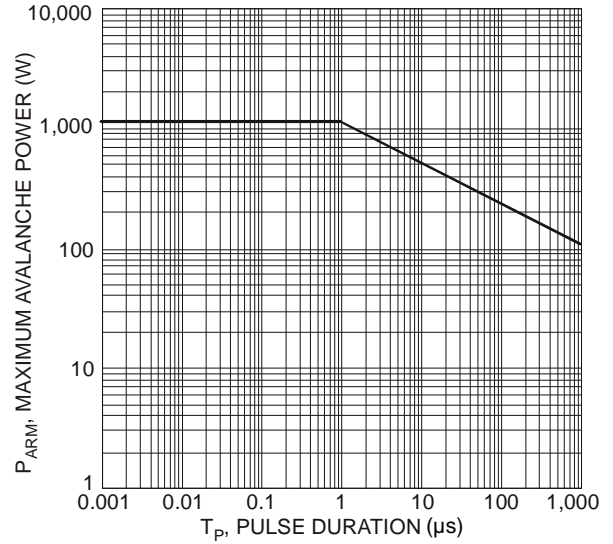


Fig. 8 Maximum Avalanche Power Curve

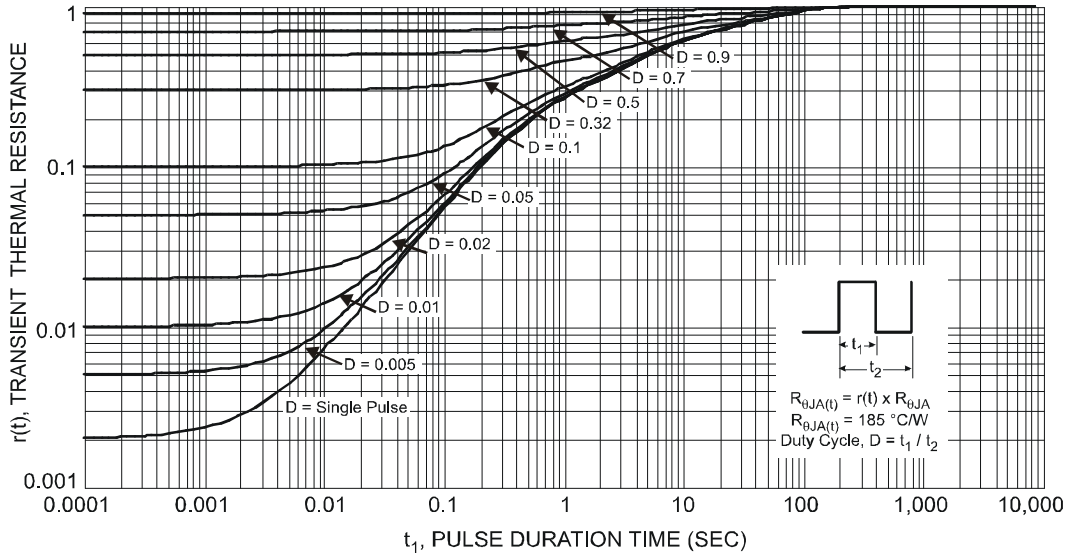
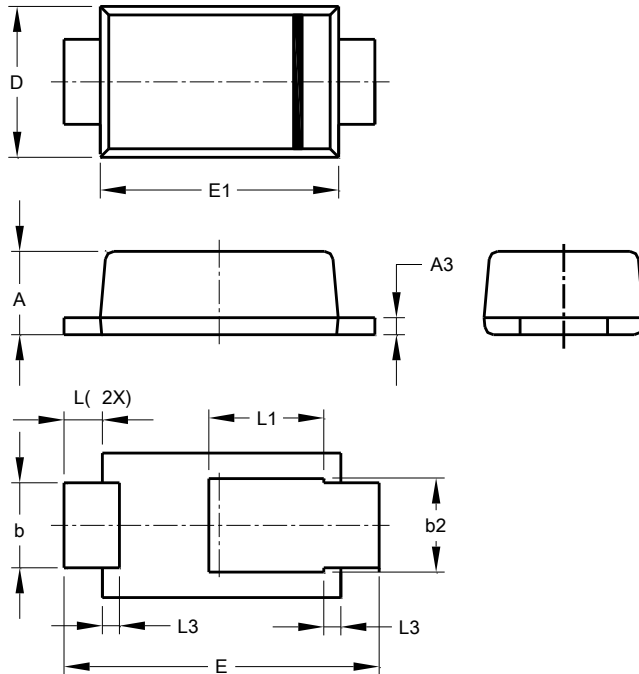


Fig. 9 Transient Thermal Resistance

Package Outline Dimensions

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.

POWERDI[®]123

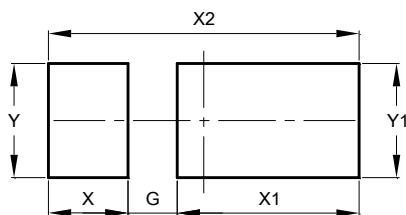


POWERDI [®] 123			
Dim	Min	Max	Typ
A	0.93	1.00	0.98
A3	0.15	0.25	0.20
b	0.85	1.25	1.00
b2	1.025	1.125	1.10
D	1.63	1.93	1.78
E	3.50	3.90	3.70
E1	2.60	3.00	2.80
L	0.40	0.50	0.45
L1	1.25	1.40	1.35
L3	0.125	0.275	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.

POWERDI[®]123



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

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