



**THE DATASHEET OF**  
**SI4486EY-T1-E3**



## N-Channel 100-V (D-S) MOSFET

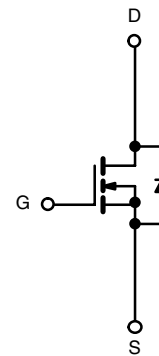
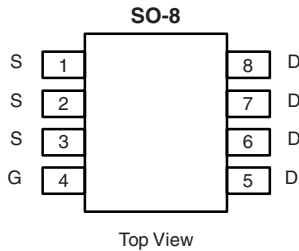
PRODUCT SUMMARY		
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
100	0.025 at $V_{GS} = 10$ V	7.9
	0.028 at $V_{GS} = 6.0$ V	7.5

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETs
- 175 °C Maximum Junction Temperature
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
Available



N-Channel MOSFET

Ordering Information: Si4486EY-T1-E3 (Lead (Pb)-free)  
Si4486EY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted					
Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	100		V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$			
Continuous Drain Current ( $T_J = 175$ °C) <sup>a</sup>	$I_D$	$T_A = 25$ °C	7.9	5.4	A
		$T_A = 85$ °C	6.1	4.2	
Pulsed Drain Current	$I_{DM}$	40			
Avalanche Current	$I_{AR}$	30		mJ	
Repetitive Avalanche Energy (Duty Cycle $\leq 1$ %)		$E_{AR}$	45		
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	3.1	1.5	A	
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25$ °C	3.8	1.8	W
		$T_A = 85$ °C	2.3	1.1	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 175		°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ s	33	40	°C/W
		Steady State	70	85	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	17	21		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

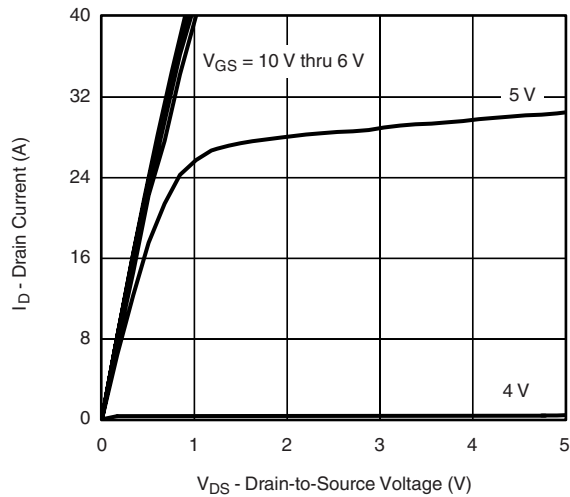
SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 80\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 80\text{ V}, V_{GS} = 0\text{ V}, T_J = 85\text{ }^\circ\text{C}$			20	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	40			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 7.9\text{ A}$		0.021	0.025	$\Omega$
		$V_{GS} = 6.0\text{ V}, I_D = 7.5\text{ A}$		0.023	0.028	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 7.9\text{ A}$		35		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 3.1\text{ A}, V_{GS} = 0\text{ V}$		0.8	1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 50\text{ V}, V_{GS} = 10\text{ V}, I_D = 7.9\text{ A}$		36	44	nC
Gate-Source Charge	$Q_{gs}$			10		
Gate-Drain Charge	$Q_{gd}$			8.6		
Gate Resistance	$R_g$		0.5	1.27	2.2	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 50\text{ V}, R_L = 50\text{ }\Omega$ $I_D \cong 1\text{ A}, V_{GEN} = 10\text{ V}, R_g = 6\text{ }\Omega$		20	40	ns
Rise Time	$t_r$			10	20	
Turn-Off Delay Time	$t_{d(off)}$			46	90	
Fall Time	$t_f$			26	50	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 3.1\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		50	80	

Notes:

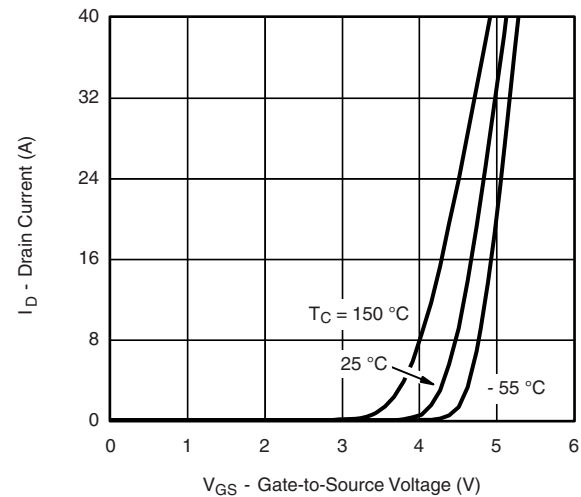
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS**  $25\text{ }^\circ\text{C}$ , unless otherwise noted

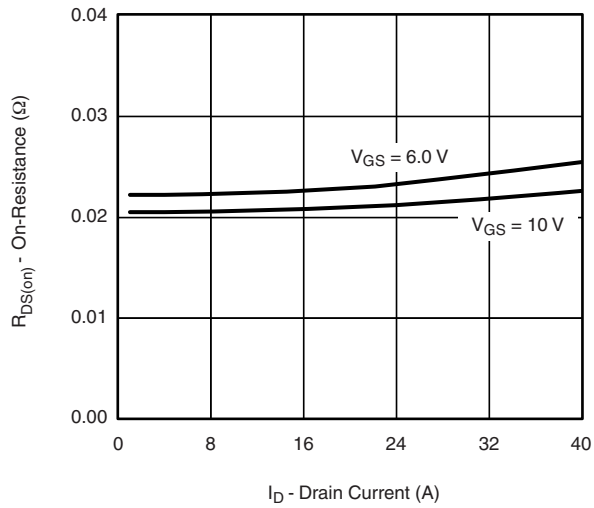


**Output Characteristics**

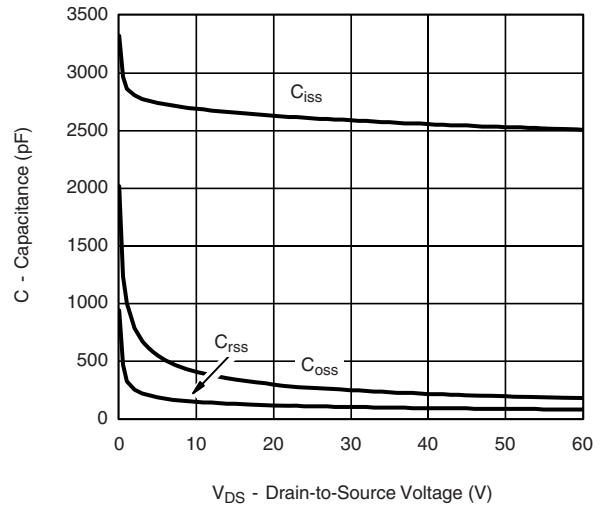


**Transfer Characteristics**

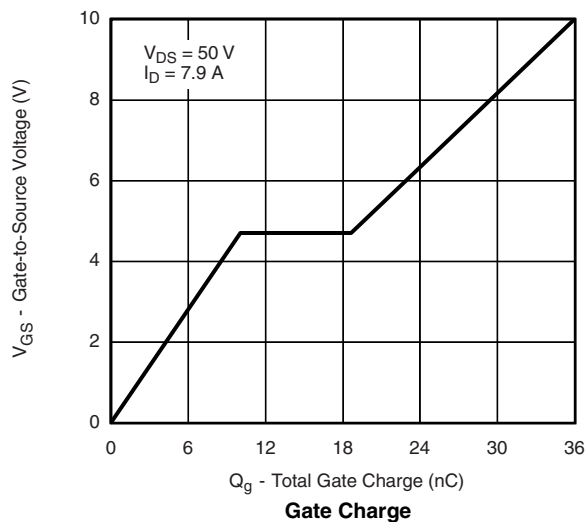
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



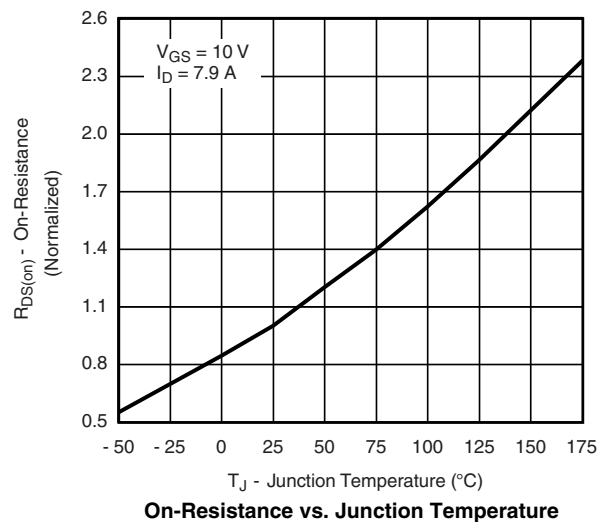
**On-Resistance vs. Drain Current**



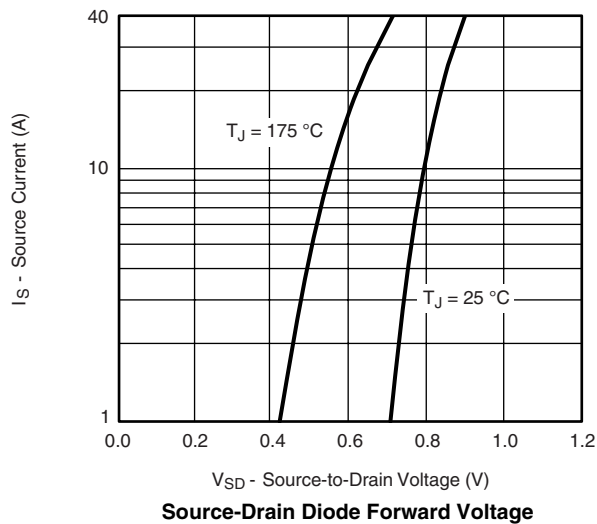
**Capacitance**



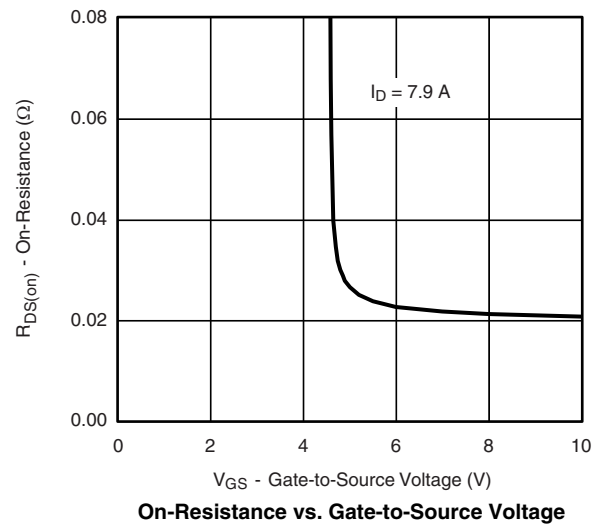
**Gate Charge**



**On-Resistance vs. Junction Temperature**

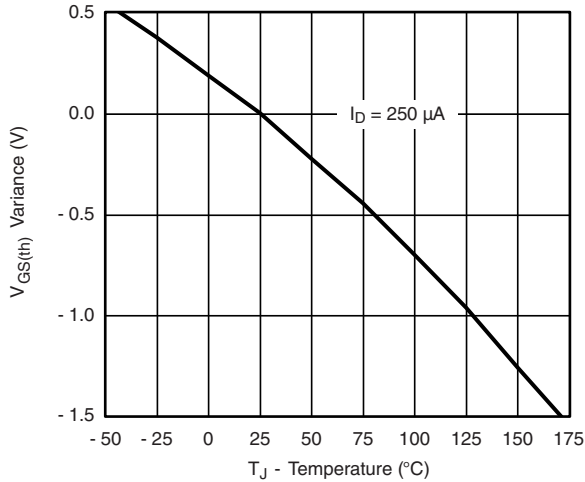


**Source-Drain Diode Forward Voltage**

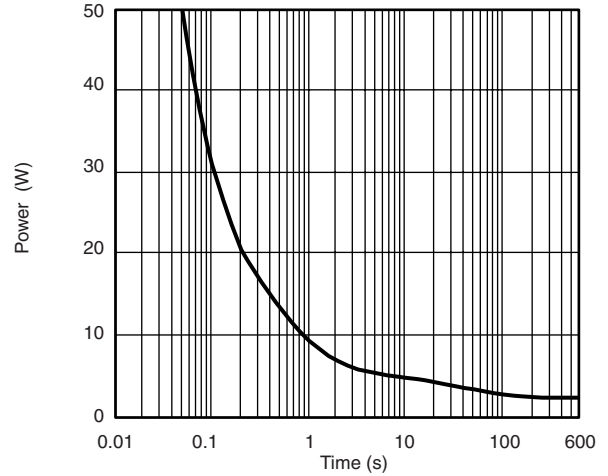


**On-Resistance vs. Gate-to-Source Voltage**

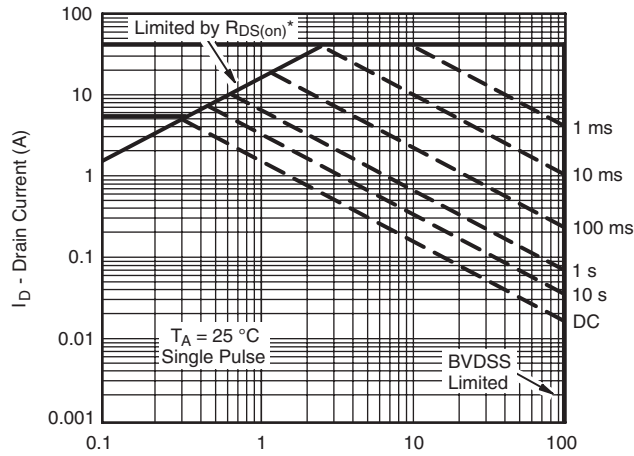
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



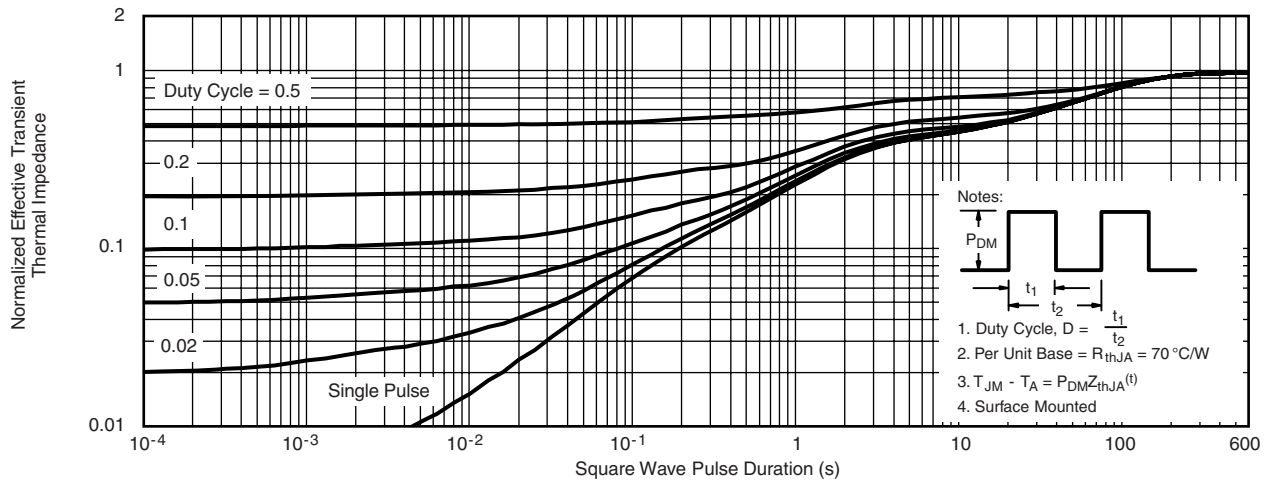
**Threshold Voltage**



**Single Pulse Power**



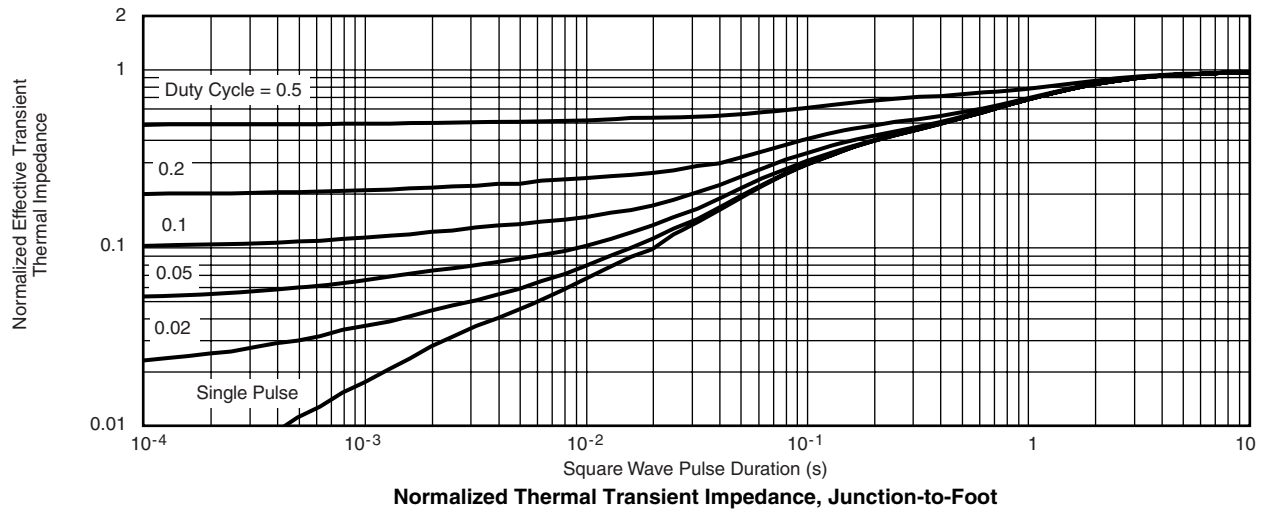
**Safe Operating Area**



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

- Notes:
- Duty Cycle,  $D = \frac{t_1}{t_2}$
  - Per Unit Base =  $R_{thJA} = 70 \text{ }^\circ\text{C/W}$
  - $T_{JM} - T_A = P_{DM} Z_{thJA}^{(t)}$
  - Surface Mounted

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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