


Features

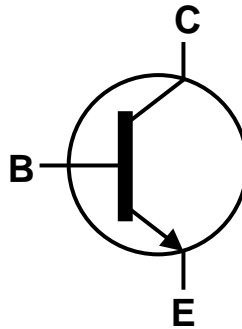
- $BV_{CE0} > 300V$
- $I_C = 500mA$ High Continuous Current
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

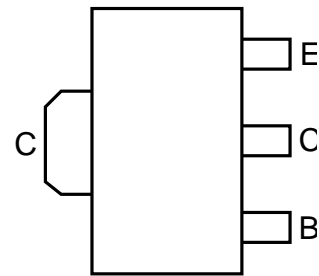
- Case: SOT89
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 
- Weight: 0.052 grams (Approximate)



Top View



Device Symbol



Top View Pin-Out

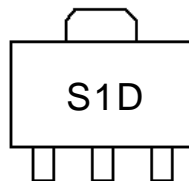
Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
SXTA42TA	S1D	7	12	1,000
SXTA42TC	S1D	13	12	4,000
SXTA42-13R	S1D	13	12	4,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

SOT89



S1D = Product Type Marking Code

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	300	V
Collector-Emitter Voltage	V _{CEO}	300	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	I _C	500	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

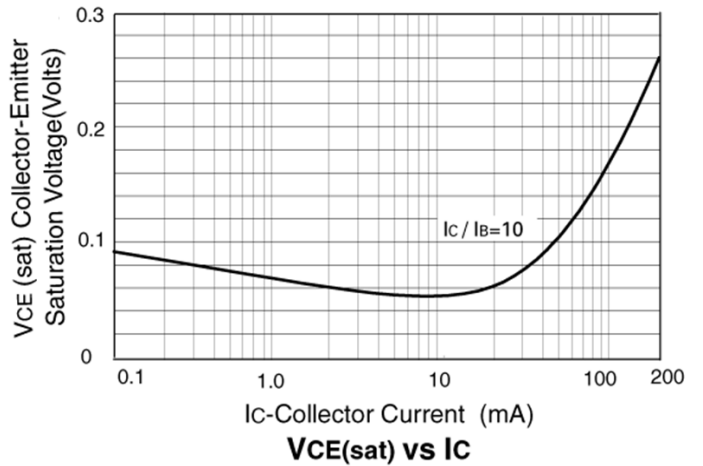
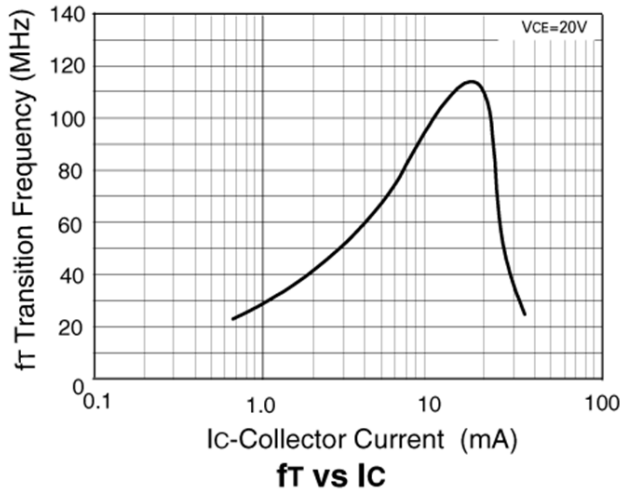
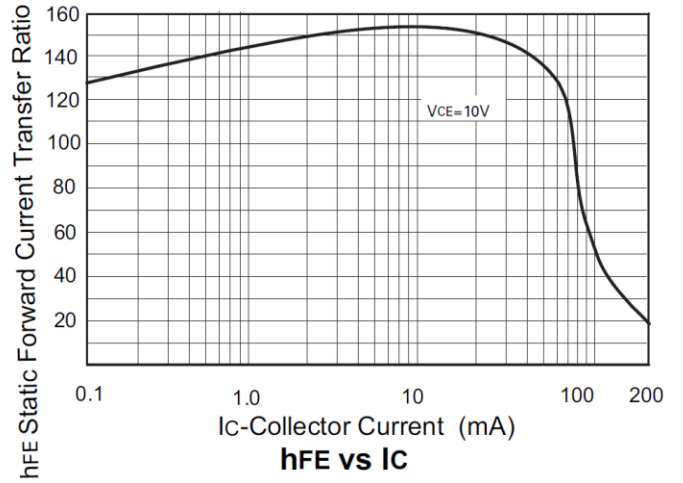
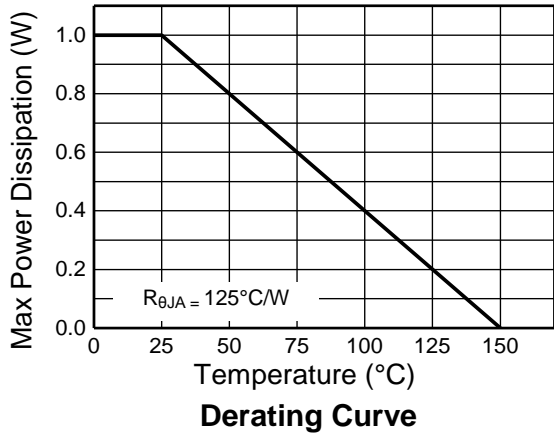
Characteristic	Symbol	Value	Unit
Collector Power Dissipation	P _D	1	W
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{θJA}	125	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	300	—	—	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 6)	BV _{CEO}	300	—	—	V	I _C = 1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	7	—	—	V	I _E = 100μA
Collector Cut-Off Current	I _{CBO}	—	—	0.1	μA	V _{CB} = 200V
Emitter Cut-Off Current	I _{EBO}	—	—	0.1	μA	V _{EB} = 6V
DC Current Transfer Static Ratio (Note 6)	h _{FE}	25	—	—	—	I _C = 1mA, V _{CE} = 10V
		40	—	—	—	I _C = 10mA, V _{CE} = 10V
		40	—	—	—	I _C = 30mA, V _{CE} = 10V
Collector-Emitter Saturation Voltage (Note 6)	V _{CE(sat)}	—	—	0.5	V	I _C = 20mA, I _B = 2mA
Base-Emitter Saturation Voltage (Note 6)	V _{BE(sat)}	—	—	0.9	V	I _C = 20mA, I _B = 2mA
Transitional Frequency	f _T	50	—	—	MHz	I _C = 10mA, V _{CE} = 20V f = 20MHz
Output Capacitance	C _{obo}	—	—	6	pF	V _{CB} = 20V, f = 1MHz

Note: 5. For the device mounted on 15mm x 15mm x 1.6mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions.
6. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

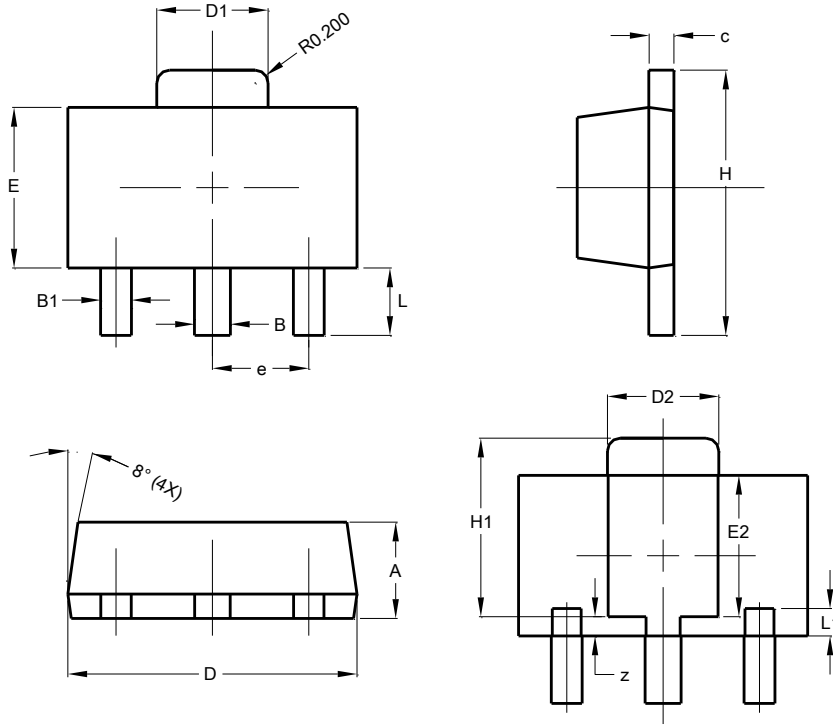
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT89

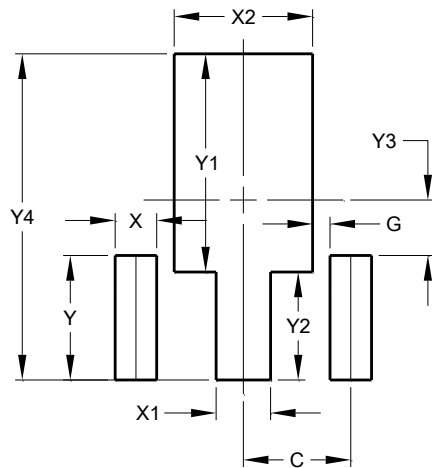


SOT89			
Dim	Min	Max	Typ
A	1.40	1.60	1.50
B	0.50	0.62	0.56
B1	0.42	0.54	0.48
c	0.35	0.43	0.38
D	4.40	4.60	4.50
D1	1.62	1.83	1.733
D2	1.61	1.81	1.71
E	2.40	2.60	2.50
E2	2.05	2.35	2.20
e	-	-	1.50
H	3.95	4.25	4.10
H1	2.63	2.93	2.78
L	0.90	1.20	1.05
L1	0.327	0.527	0.427
z	0.20	0.40	0.30
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT89



Dimensions	Value (in mm)
C	1.500
G	0.244
X	0.580
X1	0.760
X2	1.933
Y	1.730
Y1	3.030
Y2	1.500
Y3	0.770
Y4	4.530

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.

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