



CPH6123/CPH6223

Bipolar Transistor (-50V, (-)3A, Low VCE(sat), (PNP)NPN Single CPH6

ON Semiconductor®

http://onsemi.com

Applications

- DC-DC converters, relay drivers, lamp drivers, motor drivers, strobe

Features

- Adoption of MBIT process
- Large current capacity
- Low collector-to-emitter saturation voltage
- High-speed switching
- Ultrasmall package facilitates miniaturization in end products (mounting height : 0.9mm)
- High allowable power dissipation

Specifications () : CPH6123

Absolute Maximum Ratings at Ta=25°C

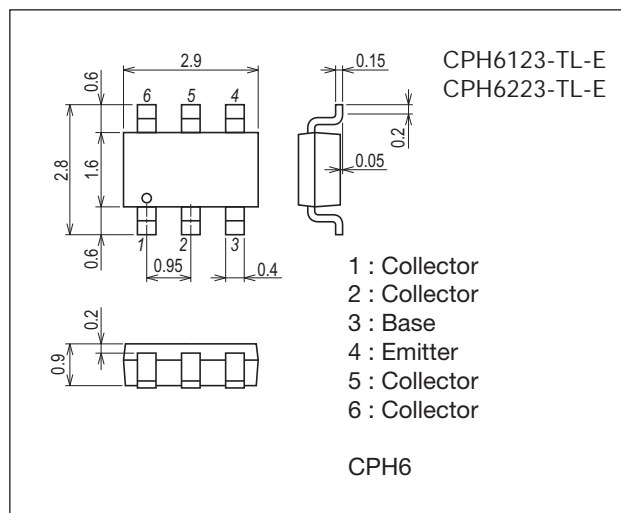
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-50)100	V
Collector-to-Emitter Voltage	V _{CES}		(-50)100	V
Collector-to-Emitter Voltage	V _{CEO}		(-50)	V
Emitter-to-Base Voltage	V _{EB0}		(-6)	V
Collector Current	I _C		(-3)	A
Collector Current (Pulse)	I _{CP}		(-6)	A
Base Current	I _B		(-600)	mA
Collector Dissipation	P _C	When mounted on ceramic substrate (600mm ² ×0.8mm)	1.3	W
Junction Temperature	T _j		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

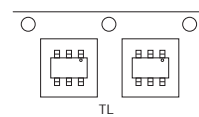
7018A-002



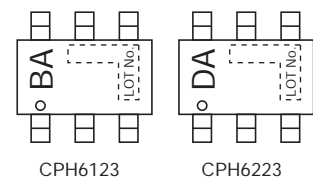
Product & Package Information

- Package : CPH6
- JEITA, JEDEC : SC-74, SOT-26, SOT-457
- Minimum Packing Quantity : 3,000 pcs./reel

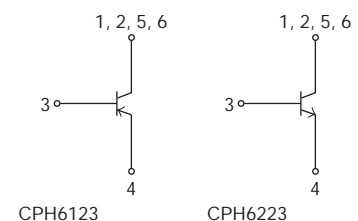
Packing Type: TL



Marking



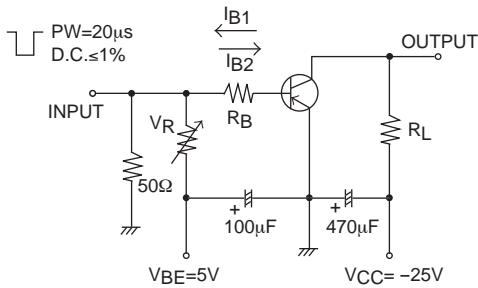
Electrical Connection



Electrical Characteristics at Ta=25°C

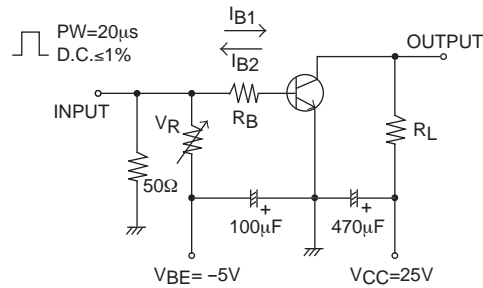
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} =(-)40V, I _E =0A			(-1)	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0A			(-1)	μA
DC Current Gain	h _{FE}	V _{CE} =(-)2V, I _C =(-)100mA	200		560	
Gain-Bandwidth Product	f _T	V _{CE} =(-)10V, I _C =(-)500mA		(390)380		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		(24)13		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)1}	I _C =(-)1A, I _B =(-)50mA		(-115)90	(-230)130	mV
	V _{CE(sat)2}	I _C =(-)2A, I _B =(-)100mA		(-240)160	(-650)240	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)2A, I _B =(-)100mA		(-0.88)	(-1.2)	V
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}	I _C =(-)10μA, I _E =0A	(-50)100			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CES}	I _C =(-)100μA, R _{BE} =0Ω	(-50)100			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C =(-)1mA, R _{BE} =∞	(-)50			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)10μA, I _C =0A	(-6)			V
Turn-On Time	t _{on}	See specified Test Circuit.		(30)35		ns
Storage Time	t _{stg}			(230)300		ns
Fall Time	t _f			(18)25		ns

Switching Time Test Circuit



$I_C = -10I_{B1} = 10I_{B2} = -1A$

CPH6123

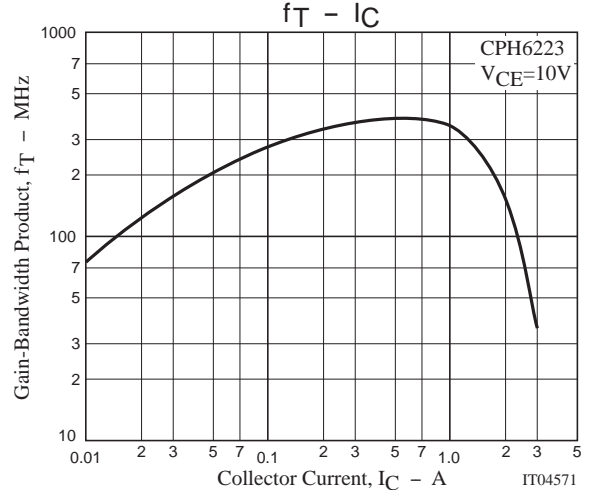
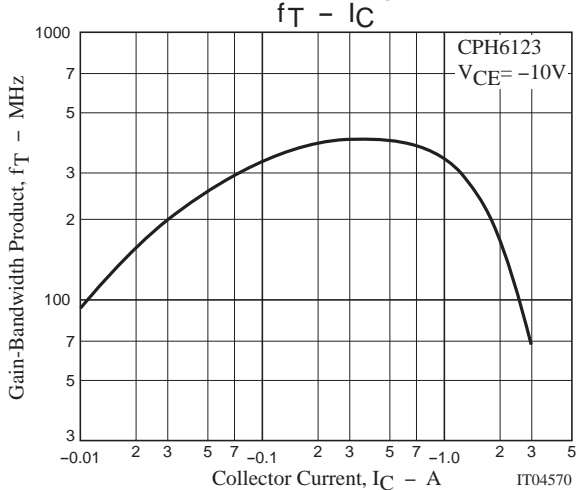
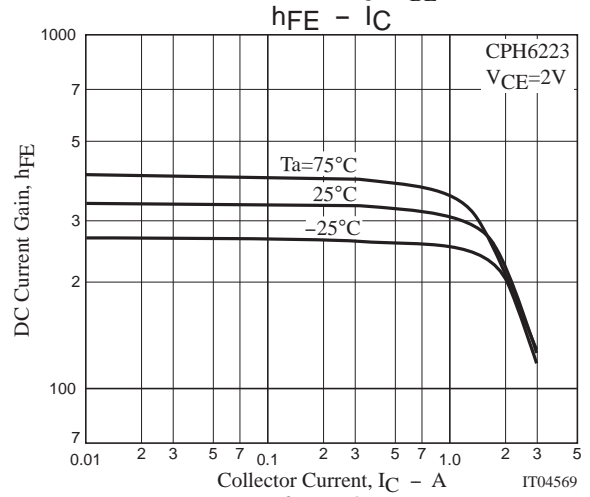
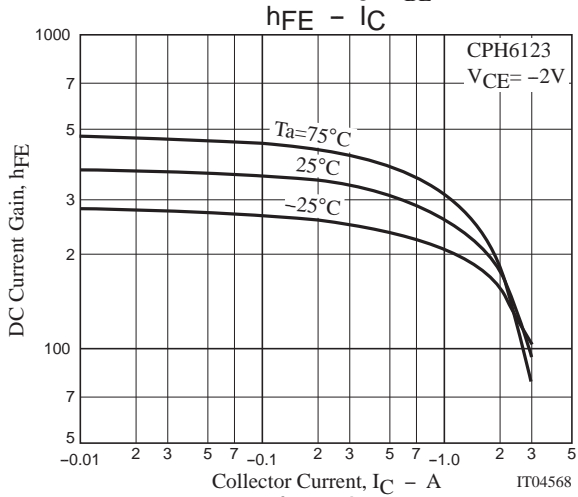
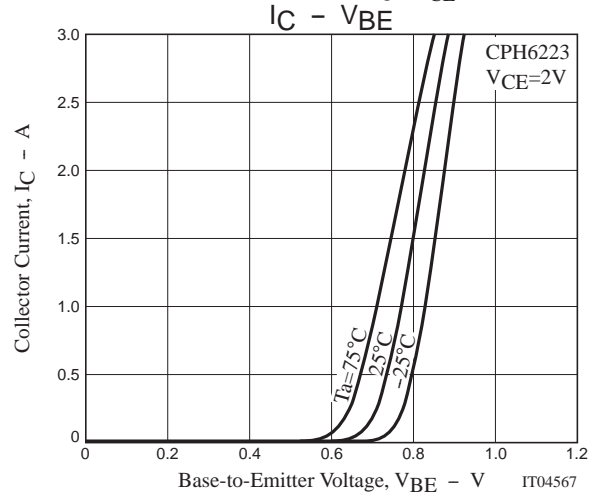
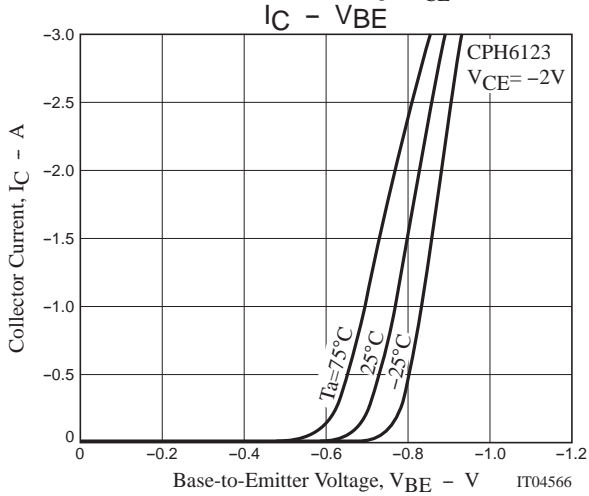
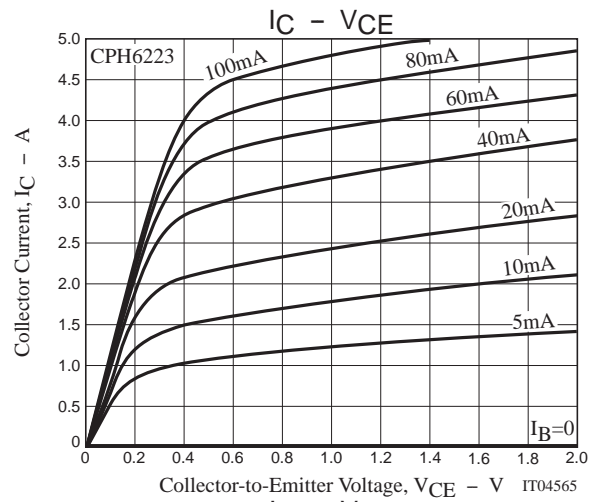
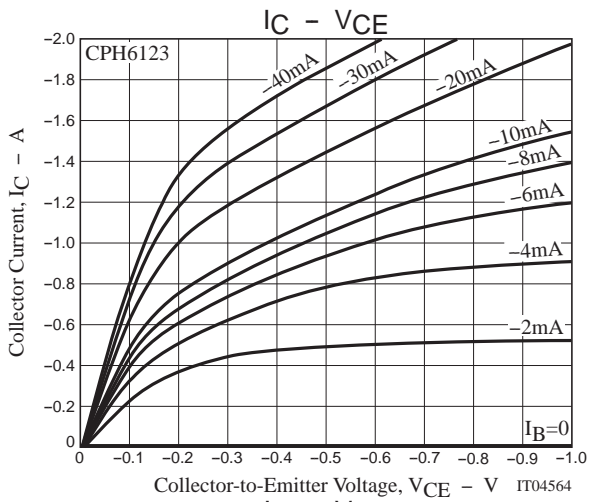


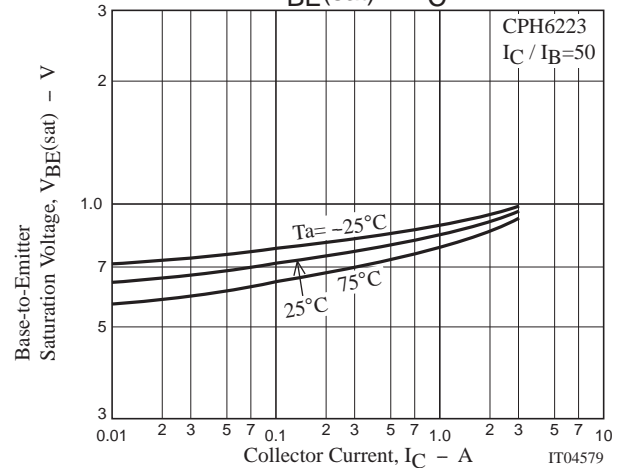
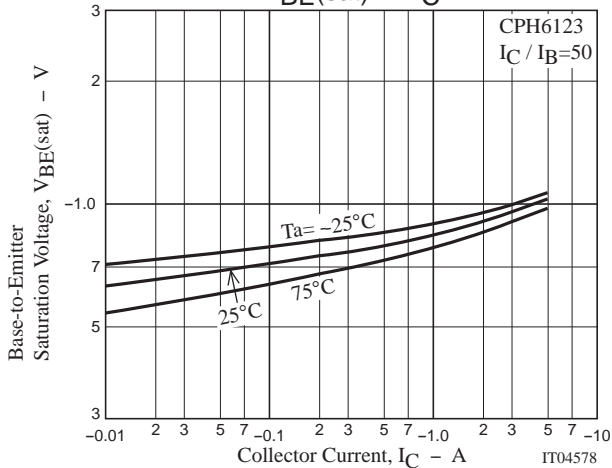
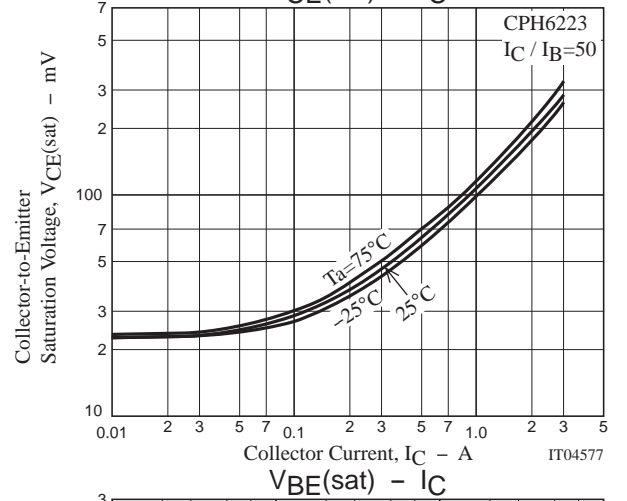
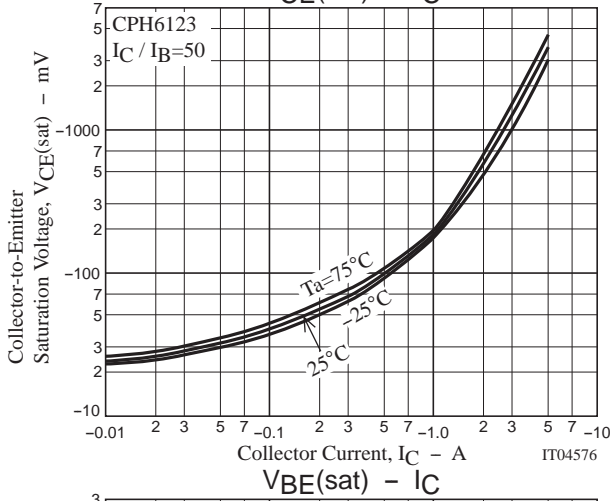
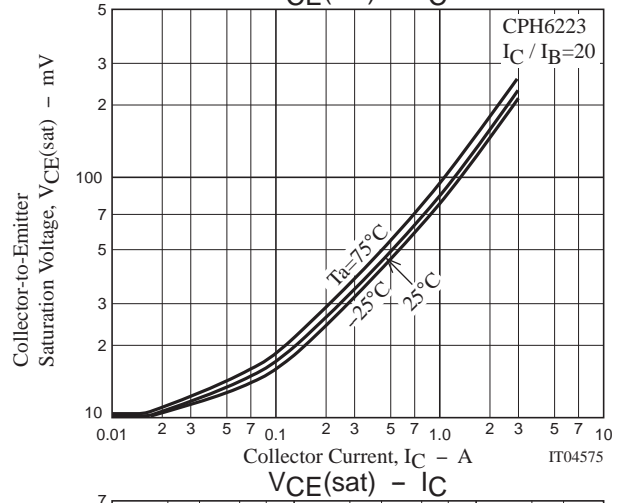
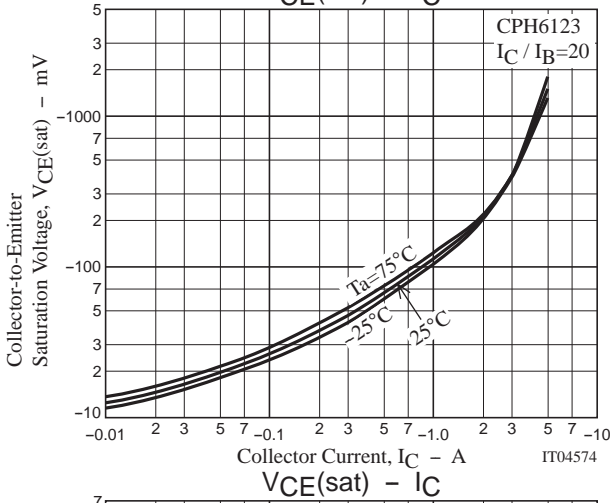
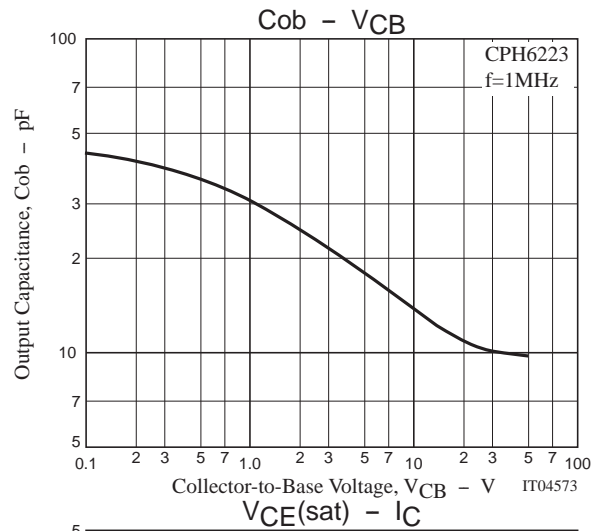
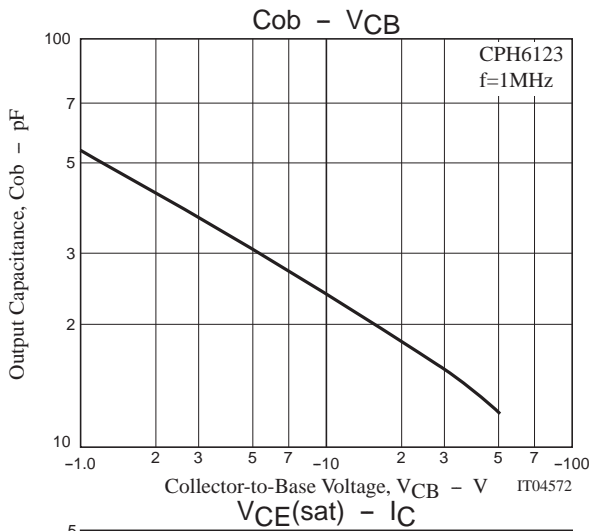
$I_C = 10I_{B1} = -10I_{B2} = 1A$

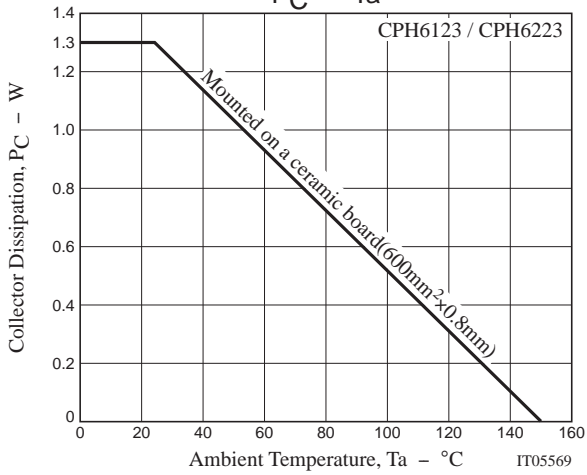
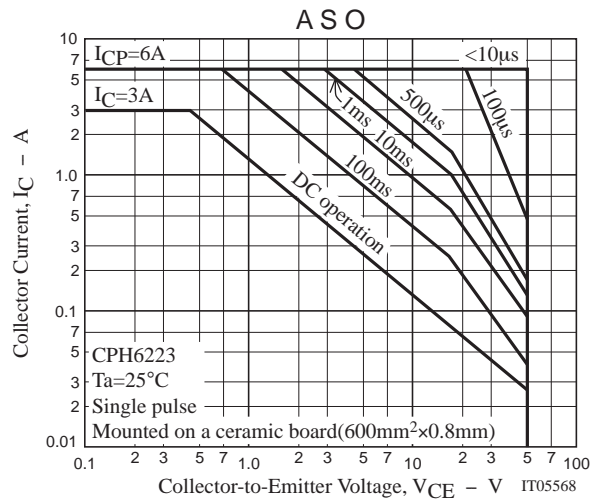
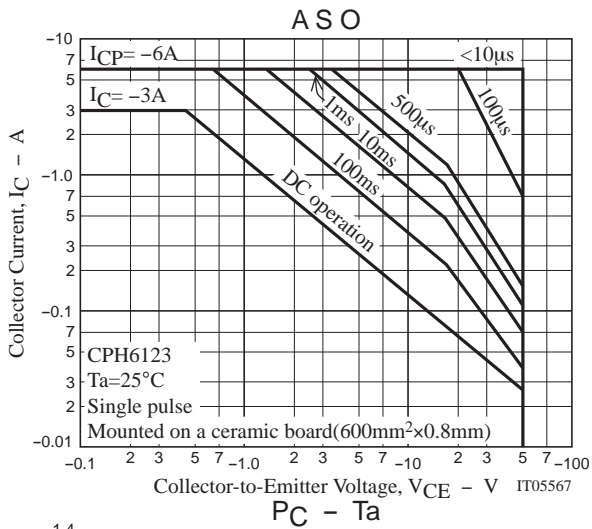
CPH6223

Ordering Information

Device	Package	Shipping	memo
CPH6123-TL-E	CPH6	3,000pcs./reel	Pb Free
CPH6223-TL-E	CPH6	3,000pcs./reel	Pb Free





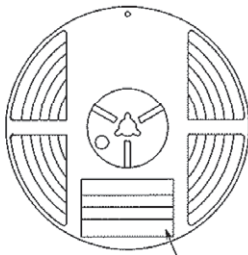


Embossed Taping Specification
CPH6123-TL-E, CPH6223-TL-E

1. Packing Format

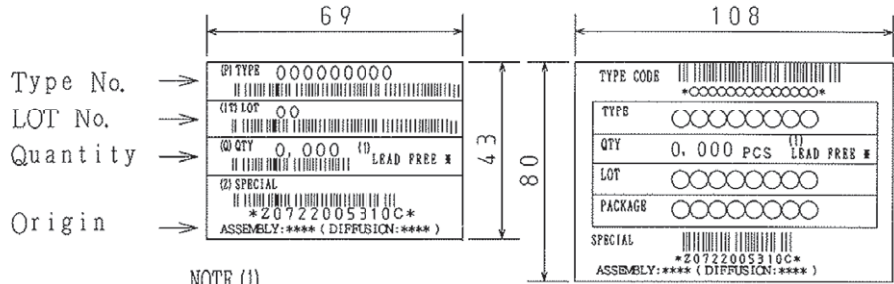
Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH6	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



Reel label

Reel label, Inner box label (unit:mm) Outer box label
It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



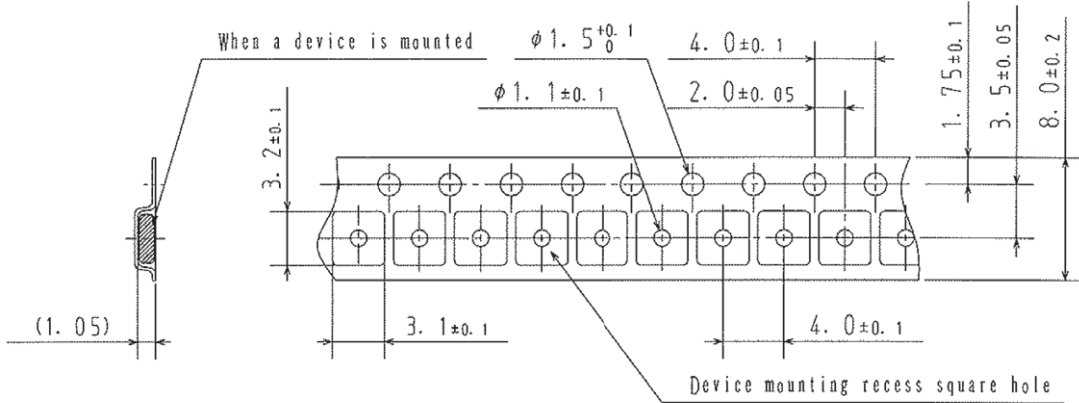
NOTE (1)

The LEAD FREE # description shows that the surface treatment of the terminal is lead free.

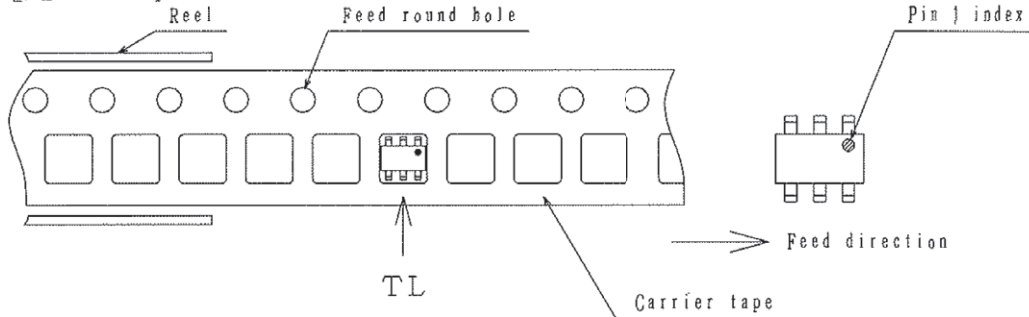
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



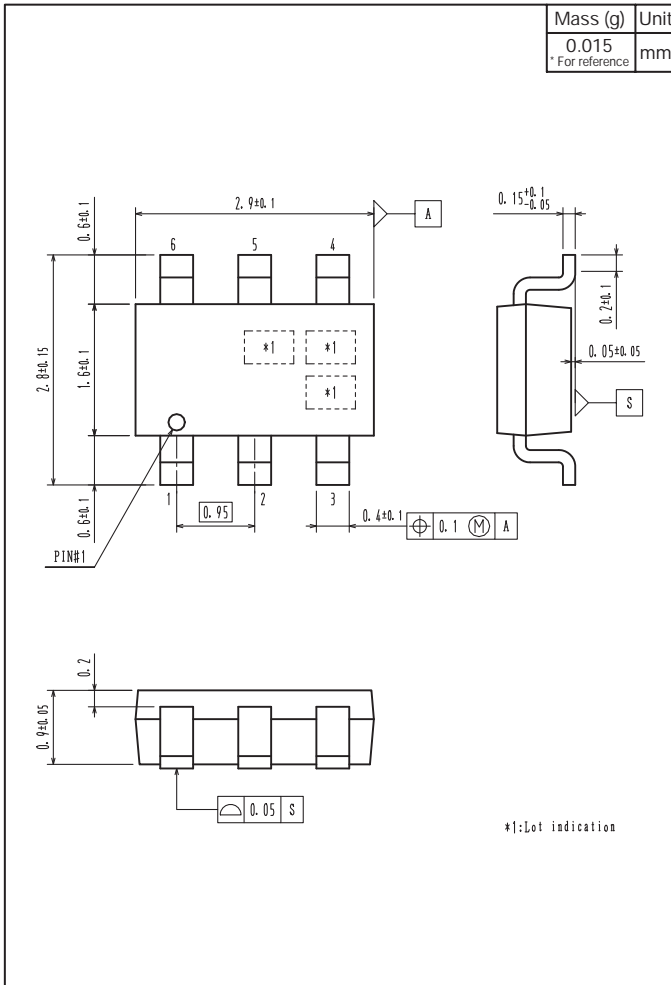
2-2. Device placement direction



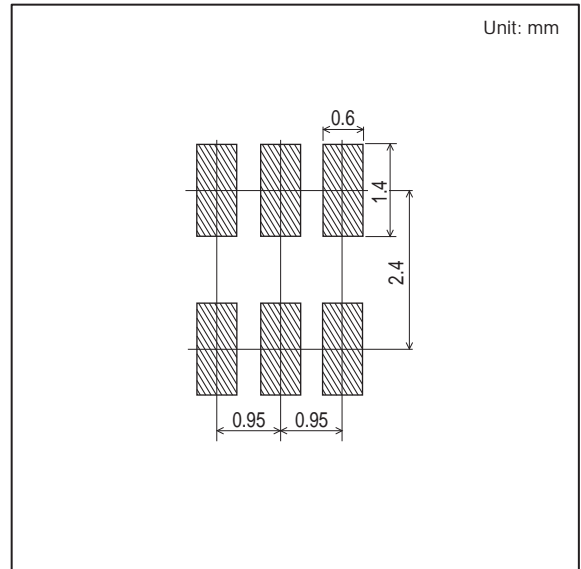
Those with pin 1 index on the feed hole side.....TL

Outline Drawing

CPH6123-TL-E, CPH6223-TL-E





Land Pattern Example



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