THE DATASHEET OF

KSD986YSTSSTU

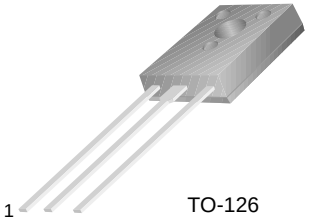
www.win-source.net 0086-755-83957316

FAIRCHILD SEMICONDUCTOR™

KSD985/986

Low Frequency Power Amplifier

- Low Speed Switching Industrial Use



TO-126

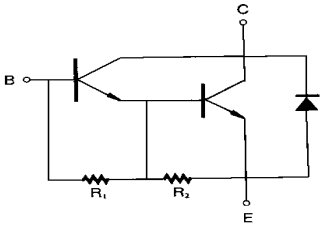
1. Emitter 2.Collector 3.Base

NPN Epitaxial Silicon Darlington Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	150	V
V_{CEO}	Collector-Emitter Volage : KSD985 : KSD986	 60 80	 V V
V_{EBO}	Emitter-Base Voltage	8.0	V
I_C	Collector Current (DC)	1.5	A
I_{CP}	*Collector Current (Pulse)	3.0	A
I_B	Base Current	0.15	A
P_C	Collector Dissipation (T_a=25°C)	1.0	W
P_C	Collector Dissipation (T_C=25°C)	10	W
T_J	Junction Temperature	150	°C
T_{STG}	Storage Temperature	- 55 ~ 150	°C

* PW≤300µs, Duty Cycle10%



R_1≒10kΩ
R_2≒500Ω

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
I_{CBO}	Collector Cut-off Current	V_{CB} = 60V, I_E = 0			10	µA
I_{CER}	Collector Cut-off Current	V_{CE} = 60V, R_{BE} = 51Ω @ T_C = 125°C			1.0	mA
I_{CEX1} I_{CEX2}	Collector Cut-off Current	V_{CE} = 60V, V_{BE}(off) = -1.5A V_{CE} = 60V, V_{BE}(off) = -1.5A @ T_C = 125°C			10 1.0	µA mA
I_{EBO}	Emitter Cut-off Current	V_{EB} = 5V, I_C = 0			1.0	mA
h_{FE1} h_{FE2}	*DC Current Gain	V_{CE} = 2V, I_C = 0.5A V_{CE} = 2V, I_C = 1A	1000 2000		 30000	
V_{CE}(sat)	*Collector-Emitter Saturation Voltage	I_C = 1A, I_B = 1mA			1.5	V
V_{BE}(sat)	*Base-Emitter Saturation Voltage	I_C = 1A, I_B = 1mA			2.0	V
t_{ON}	Turn ON Time	V_{CC} = 50V, I_C = 1A I_{B1} = - I_{B2} = 1mA R_L = 50Ω		0.5		µs
t_{STG}	Storage Time			1.0		µs
t_F	Fall Time			1.0		µs

* Pulse Test: PW≤350µs, Duty Cycle≤2%

h_{FE} Classification

Classification	R	O	Y
h_{FE2}	2000 ~ 5000	4000 ~ 10000	8000 ~ 30000

©2000 Fairchild Semiconductor International

Typical Characteristics

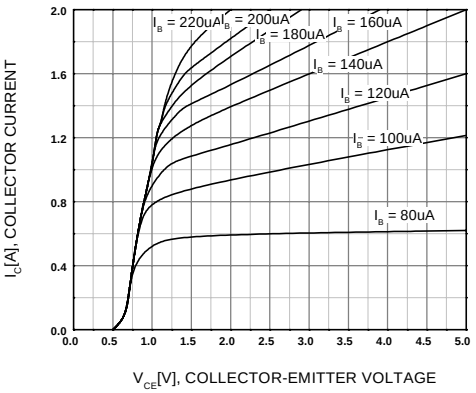


Figure 1. Static Characteristic

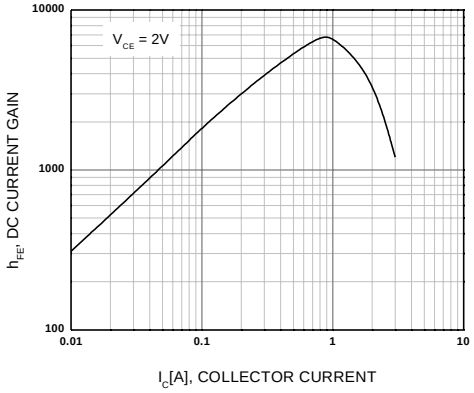


Figure 2. DC current Gain

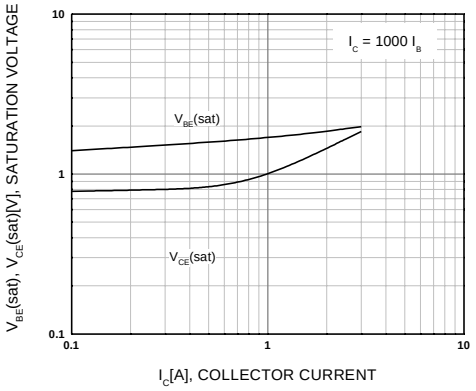


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

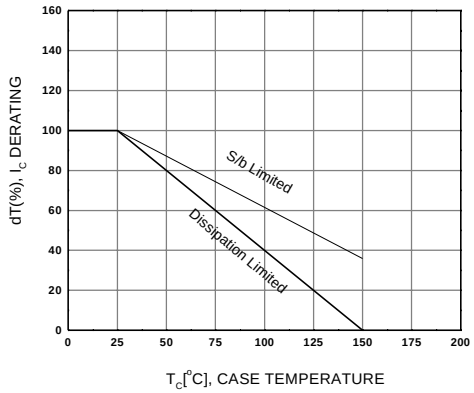


Figure 4. Derating Curve Of Safe Operating Areas

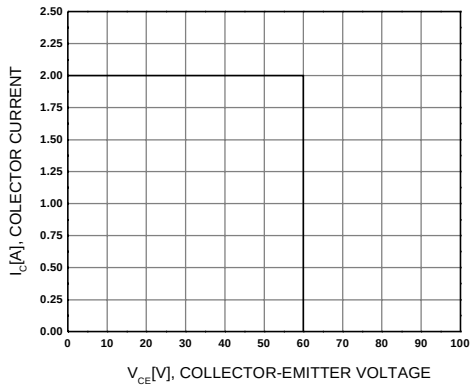


Figure 5. Reverse Bias Safe Operating Areas

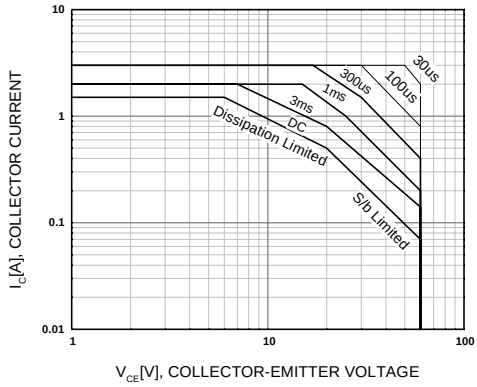


Figure 6. Safe Operating Area

©2000 Fairchild Semiconductor International

Typical Characteristics (Continued)

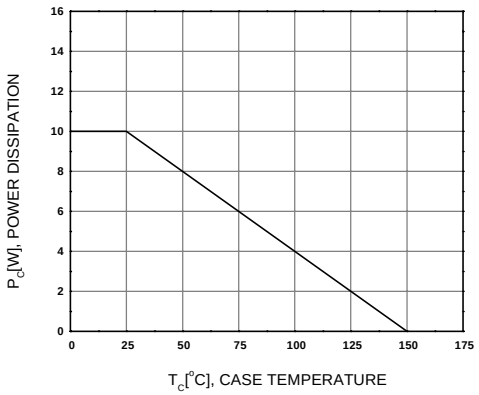
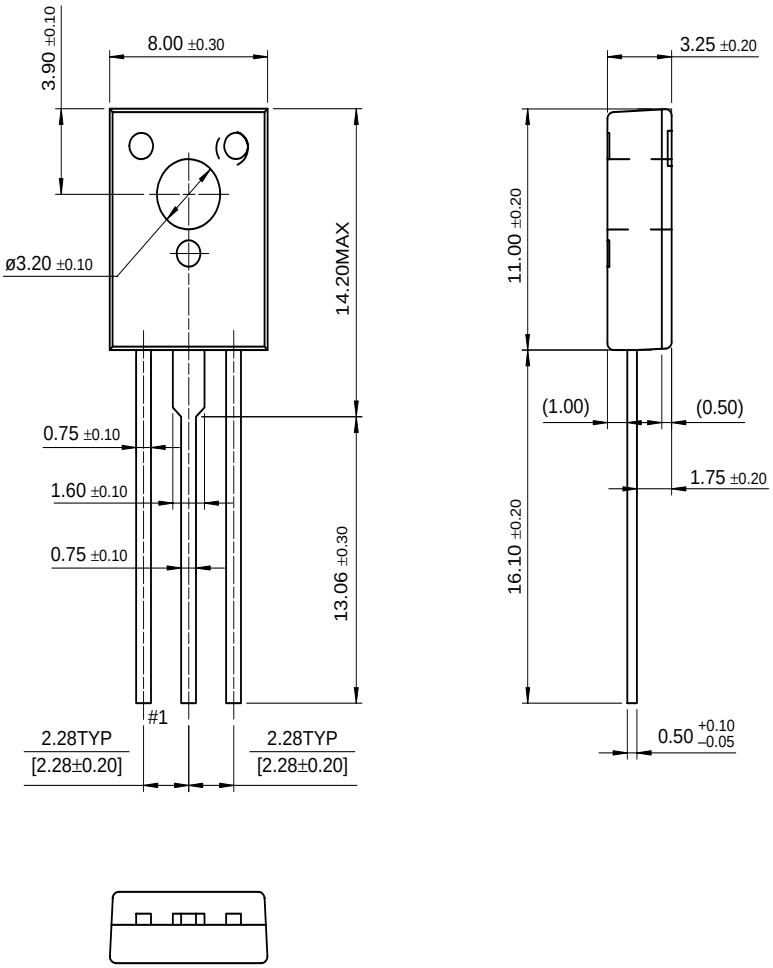


Figure 7. Power Derating

©2000 Fairchild Semiconductor International

Package Demensions

TO-126



Dimensions in Millimeters

©2000 Fairchild Semiconductor International

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™
Bottomless™
CoolFET™
CROSSVOLT™
E^2CMOS™
FACT™
FACT Quiet Series™
FAST®
FASTr™
GTO™
HiSeC™
ISOPLANAR™
MICROWIRE™
POP™
PowerTrench®
QFET™
QS™
Quiet Series™
SuperSOT™-3
SuperSOT™-6
SuperSOT™-8
SyncFET™
TinyLogic™
UHC™
VCX™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2000 Fairchild Semiconductor International

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

- View KSD986YSTSSTU on WIN SOURCE
- Fairchild/ON Semiconductor Information

Optimize Your Supply Chain with WIN SOURCE Solutions

- Global Sourcing Solution
- Obsolete Management
- Cost Control Management
- Shortage Management
- Alternative Solution
- Excess Inventory Management