

# CPH3360

## Power MOSFET –30V, 303mΩ, –1.6A, Single P-Channel

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

### Features

- High Speed Switching
- 4V drive
- Pb-Free, Halogen Free and RoHS compliance

### Typical Applications

- DC/DC Converter

### SPECIFICATIONS

**ABSOLUTE MAXIMUM RATING** at Ta = 25°C (Note 1, 2)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V <sub>DSS</sub>	–30	V
Gate to Source Voltage	V <sub>GSS</sub>	±20	V
Drain Current (DC)	I <sub>D</sub>	–1.6	A
Drain Current (Pulse) PW ≤ 10μs, duty cycle ≤ 1%	I <sub>DP</sub>	–6.4	A
Power Dissipation When mounted on ceramic substrate (900mm <sup>2</sup> × 0.8mm)	P <sub>D</sub>	0.9	W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	–55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2 : This product is designed to "ESD immunity < 200V\*", so please take care when handling.

\*Machine Model

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm <sup>2</sup> × 0.8mm)	R <sub>θJA</sub>	138.8	°C/W

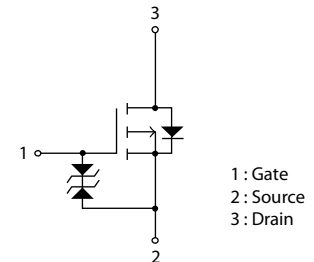


**ON Semiconductor**®

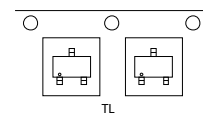
www.onsemi.com

V <sub>DSS</sub>	R <sub>DS(on)</sub> Max	I <sub>D</sub> Max
–30V	303mΩ@ –10V	–1.6A
	532mΩ@ –4.5V	
	617mΩ@ –4V	

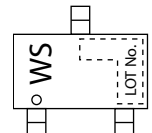
### ELECTRICAL CONNECTION P-Channel



### PACKING TYPE : TL



### MARKING



### ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

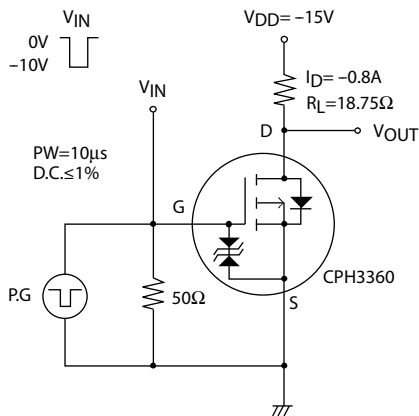
# CPH3360

## ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 3)

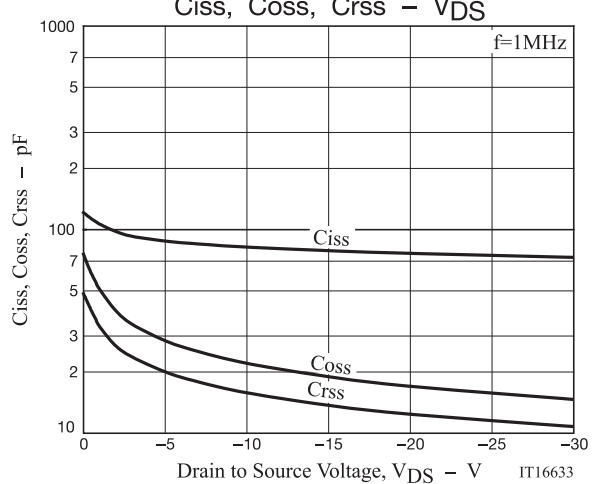
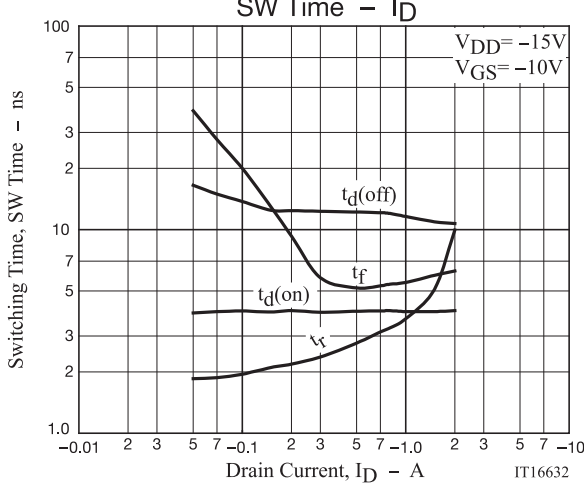
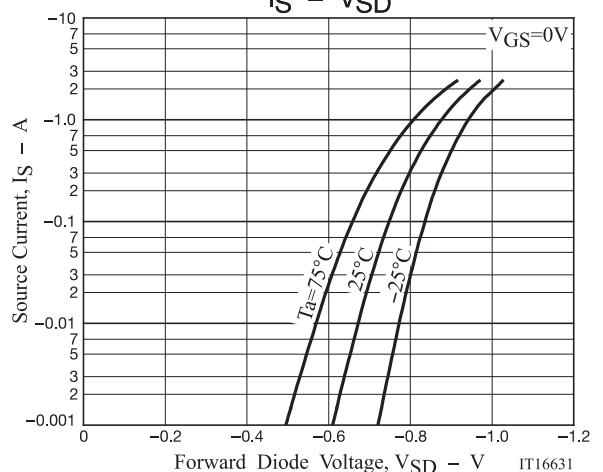
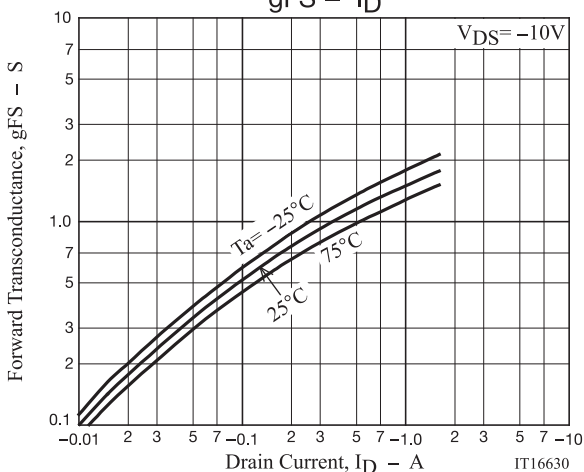
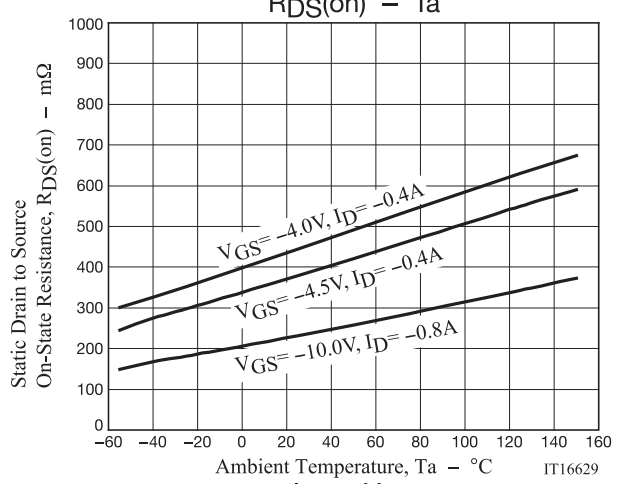
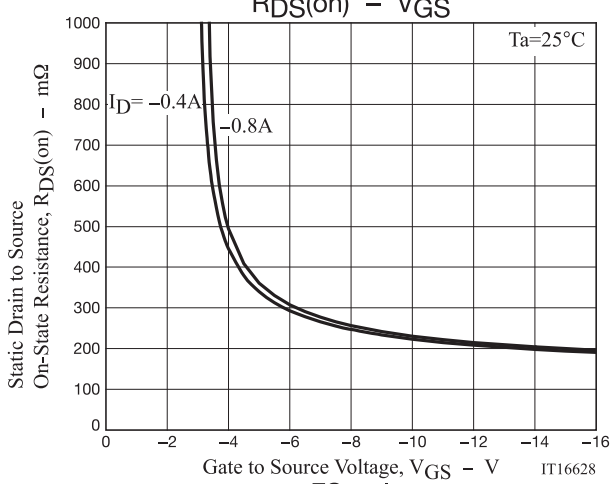
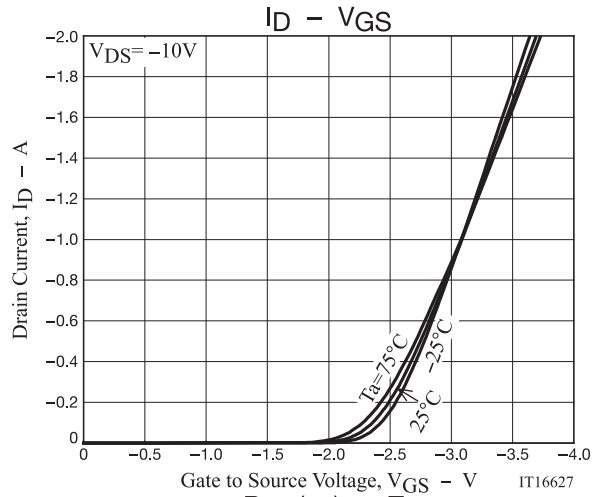
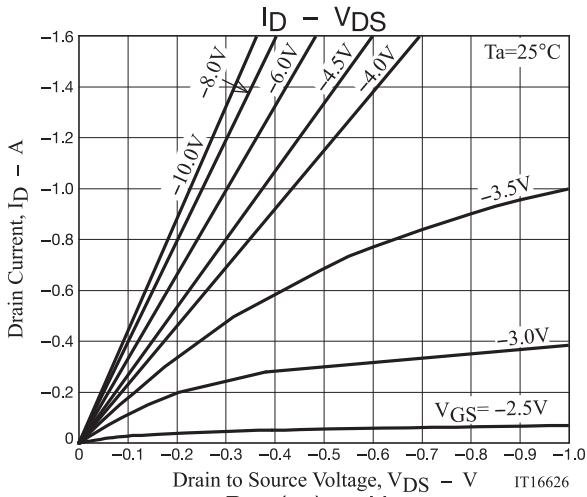
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	-30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-1	μA
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.2		-2.6	V
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-0.8A		1.3		S
Static Drain to Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =-0.8A, V <sub>GS</sub> =-10V		233	303	mΩ
	R <sub>DS(on)2</sub>	I <sub>D</sub> =-0.4A, V <sub>GS</sub> =-4.5V		380	532	mΩ
	R <sub>DS(on)3</sub>	I <sub>D</sub> =-0.4A, V <sub>GS</sub> =-4V		441	617	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-10V, f=1MHz		82		pF
Output Capacitance	C <sub>oss</sub>			22		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			16		pF
Turn-ON Delay Time	t <sub>d(on)</sub>		See specified Test Circuit		4.0	
Rise Time	t <sub>r</sub>			3.3		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>			12		ns
Fall Time	t <sub>f</sub>			5.4		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.6A			2.2	
Gate to Source Charge	Q <sub>gs</sub>			0.36		nC
Gate to Drain "Miller" Charge	Q <sub>gd</sub>			0.49		nC
Forward Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1.6A, V <sub>GS</sub> =0V		-0.9	-1.5	V

Note 3 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

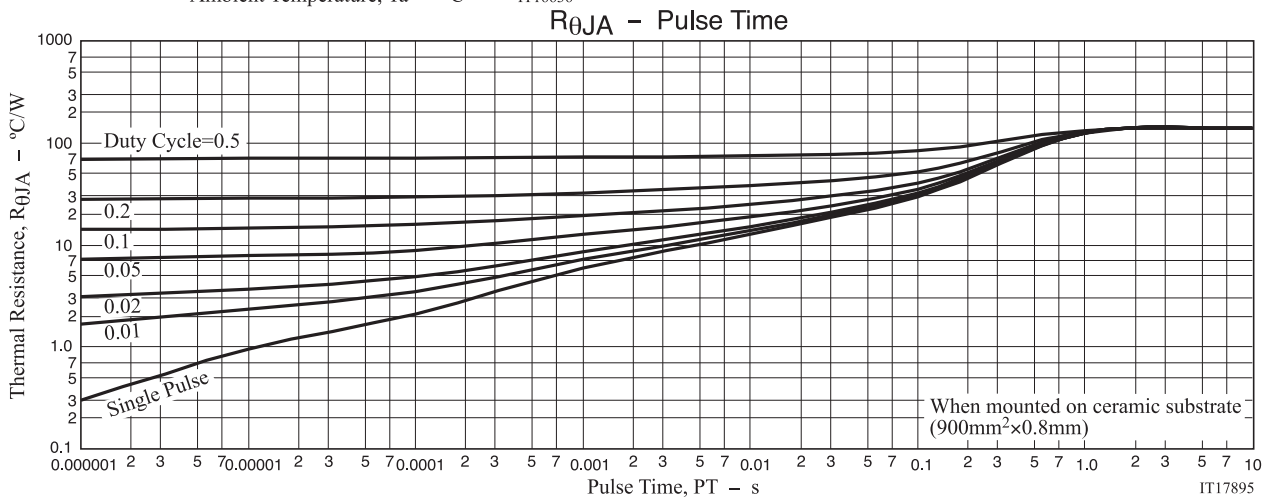
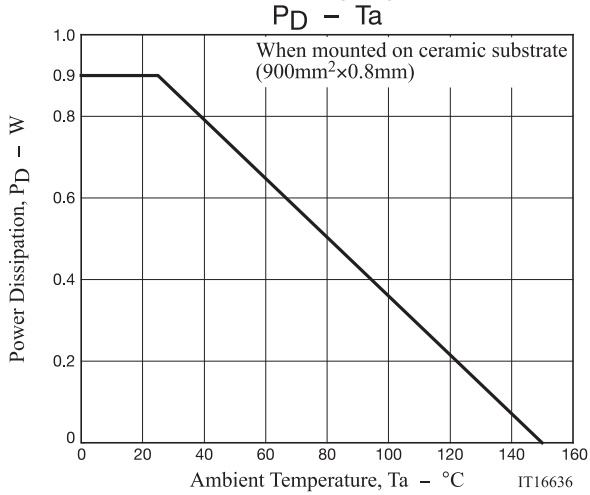
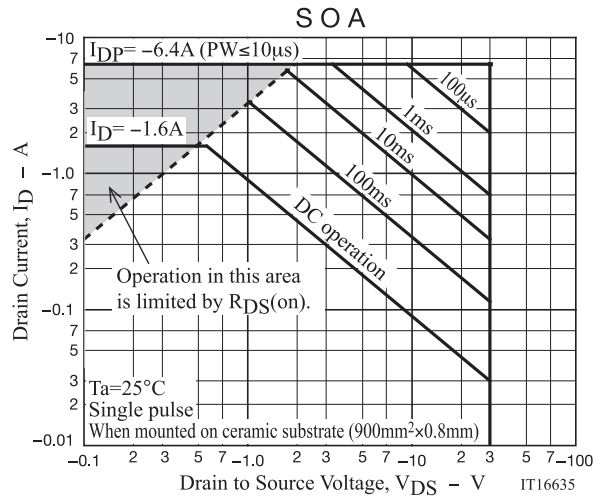
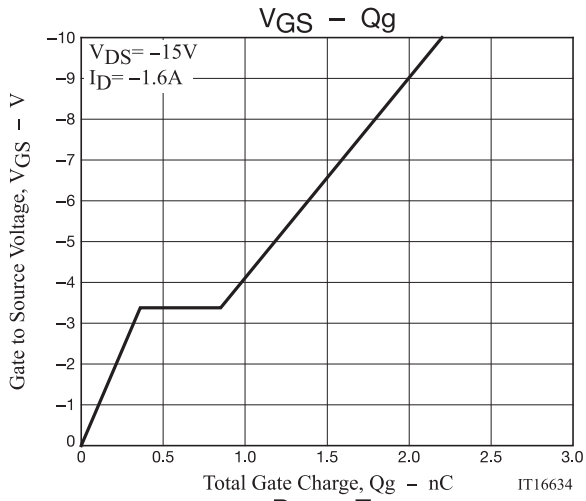
### Switching Time Test Circuit



CPH3360



# CPH3360

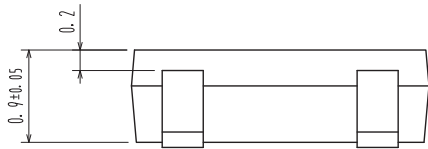
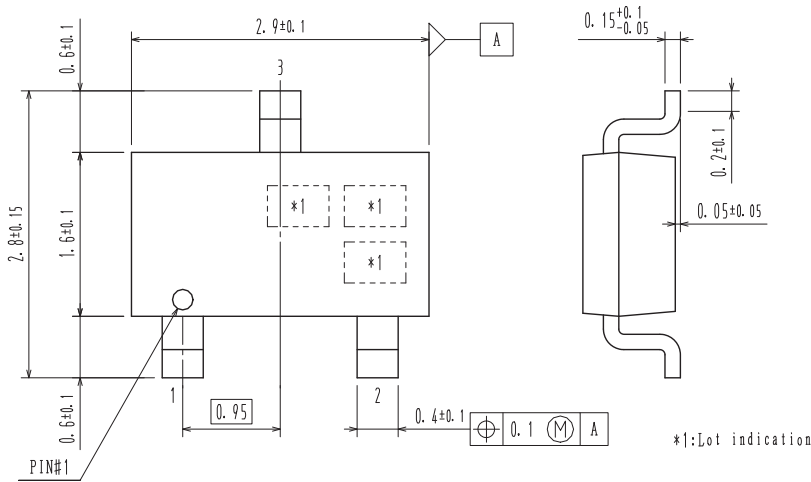


# CPH3360

## PACKAGE DIMENSIONS

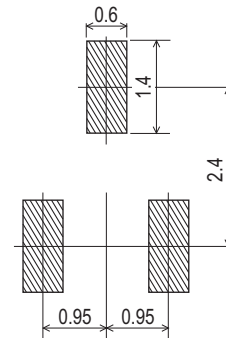
unit : mm

CPH3  
CASE 318BA  
ISSUE O



- 1 : Gate
- 2 : Source
- 3 : Drain

## Recommended Soldering Footprint



## ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
CPH3360-TL-H	WS	CPH3 SC-59, SOT-23, TO-236 (Pb-Free / Halogen Free)	3,000 / Tape & Reel
CPH3360-TL-W			

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. [http://www.onsemi.com/pub\\_link/Collateral/BRD8011-D.PDF](http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF)

Note on usage : Since the CPH3360 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

- ⊖ [View CPH3360-TL-H on WIN SOURCE](#)
- ⊖ [ON Semiconductor](#) Information

## Optimize Your Supply Chain with WIN SOURCE Solutions

- ✓ Global Sourcing Solution
- ✓ Obsolete Management
- ✓ Cost Control Management
- ✓ Shortage Management
- ✓ Alternative Solution
- ✓ Excess Inventory Management