



THE DATASHEET OF IRFU3707



Applications

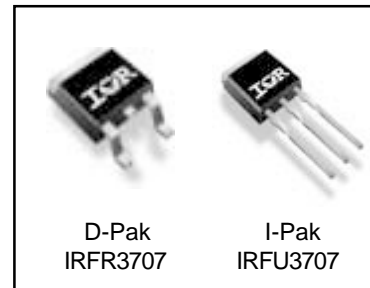
- High Frequency DC-DC Isolated Converters with Synchronous Rectification for Telecom and Industrial use
- High Frequency Buck Converters for Computer Processor Power

HEXFET® Power MOSFET

V_{DSS}	R_{DS(on)} max	I_D
30V	13mΩ	61A^④

Benefits

- Ultra-Low R_{DS(on)}
- Very Low Gate Impedance
- Fully Characterized Avalanche Voltage and Current



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-to-Source Voltage	± 20	V
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	61 ^④	A
I _D @ T _C = 70°C	Continuous Drain Current, V _{GS} @ 10V	51 ^④	
I _{DM}	Pulsed Drain Current ^①	244	
P _D @ T _C = 25°C	Maximum Power Dissipation ^③	87	W
P _D @ T _C = 70°C	Maximum Power Dissipation ^③	61	W
	Linear Derating Factor	0.59	mW/°C
T _J , T _{STG}	Junction and Storage Temperature Range	-55 to + 175	°C

Thermal Resistance

	Parameter	Typ.	Max.	Units
R _{θJC}	Junction-to-Case	—	1.73	°C/W
R _{θJA}	Junction-to-Ambient (PCB mount)*	—	50	
R _{θJA}	Junction-to-Ambient	—	110	

* When mounted on 1" square PCB (FR-4 or G-10 Material) .
 For recommended footprint and soldering techniques refer to application note #AN-994

Notes ① through ④ are on page 9

Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	0.027	—	V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	9.7	13	mΩ	V _{GS} = 10V, I _D = 15A ③
		—	13.2	17.5		V _{GS} = 4.5V, I _D = 12A ③
V _{GS(th)}	Gate Threshold Voltage	1.0	—	3.0	V	V _{DS} = V _{GS} , I _D = 250μA
I _{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	V _{DS} = 24V, V _{GS} = 0V
		—	—	100		V _{DS} = 24V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	200	nA	V _{GS} = 16V
	Gate-to-Source Reverse Leakage	—	—	-200		V _{GS} = -16V

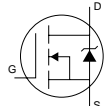
Dynamic @ T_J = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
g _{fs}	Forward Transconductance	37	—	—	S	V _{DS} = 15V, I _D = 49.6A
Q _g	Total Gate Charge	—	19	—	nC	I _D = 24.8A
Q _{gs}	Gate-to-Source Charge	—	8.2	—		V _{DS} = 15V
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	6.3	—		V _{GS} = 4.5V ③
Q _{oss}	Output Gate Charge	—	18	27		V _{GS} = 0V, V _{DS} = 15V
t _{d(on)}	Turn-On Delay Time	—	8.5	—	ns	V _{DD} = 15V
t _r	Rise Time	—	78	—		I _D = 24.8A
t _{d(off)}	Turn-Off Delay Time	—	11.8	—		R _G = 1.8Ω
t _f	Fall Time	—	3.3	—		V _{GS} = 4.5V ③
C _{iss}	Input Capacitance	—	1990	—	pF	V _{GS} = 0V
C _{oss}	Output Capacitance	—	707	—		V _{DS} = 15V
C _{rss}	Reverse Transfer Capacitance	—	50	—		f = 1.0MHz

Avalanche Characteristics

Symbol	Parameter	Typ.	Max.	Units
E _{AS}	Single Pulse Avalanche Energy②	—	213	mJ
I _{AR}	Avalanche Current①	—	61	A

Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	61④	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	244		
V _{SD}	Diode Forward Voltage	—	0.88	1.3	V	T _J = 25°C, I _S = 31A, V _{GS} = 0V ③
		—	0.8	—		T _J = 125°C, I _S = 31A, V _{GS} = 0V ③
t _{rr}	Reverse Recovery Time	—	39	59	ns	T _J = 25°C, I _F = 31A, V _R = 20V
Q _{rr}	Reverse Recovery Charge	—	49	74	nC	di/dt = 100A/μs ③
t _{rr}	Reverse Recovery Time	—	42	63	ns	T _J = 125°C, I _F = 31A, V _R = 20V
Q _{rr}	Reverse Recovery Charge	—	62	93	nC	di/dt = 100A/μs ③

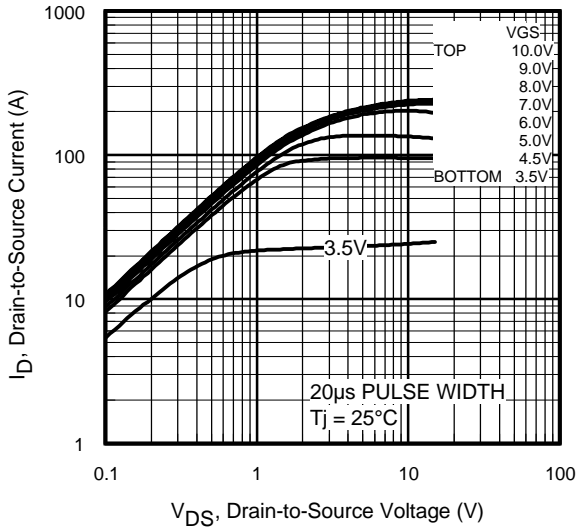


Fig 1. Typical Output Characteristics

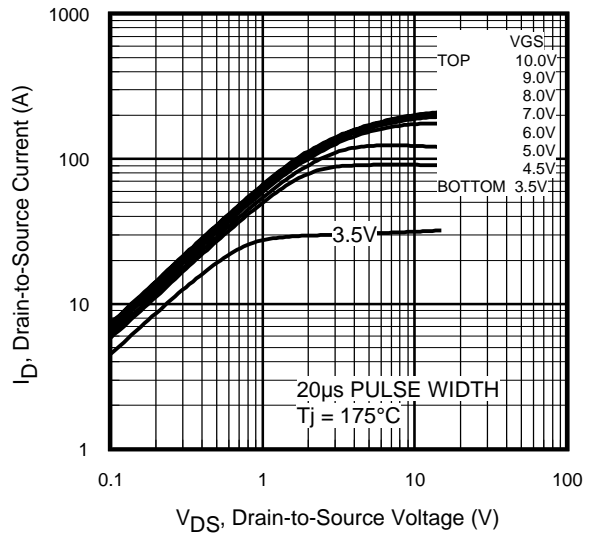


Fig 2. Typical Output Characteristics

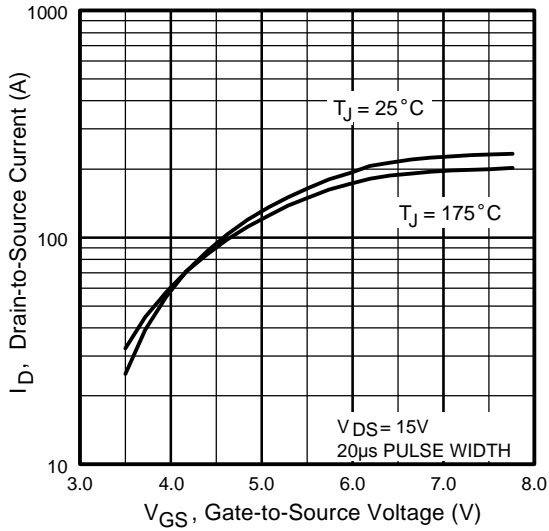


Fig 3. Typical Transfer Characteristics

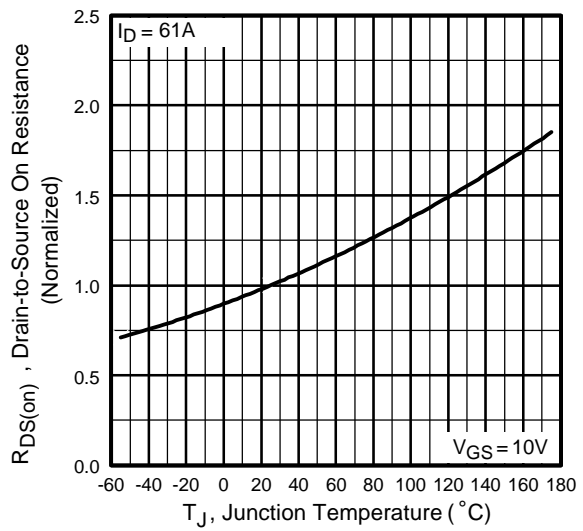


Fig 4. Normalized On-Resistance Vs. Temperature

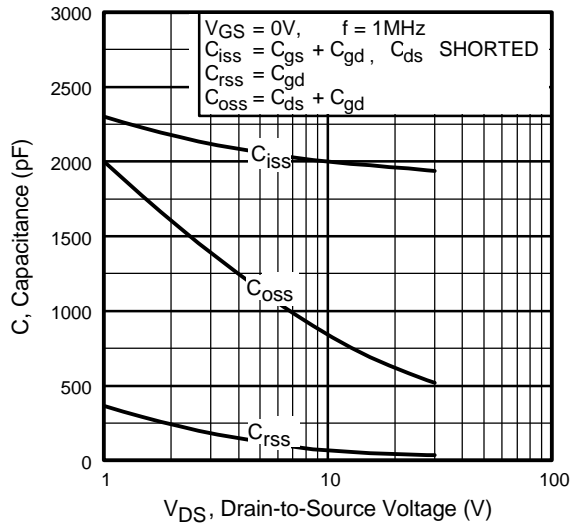


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

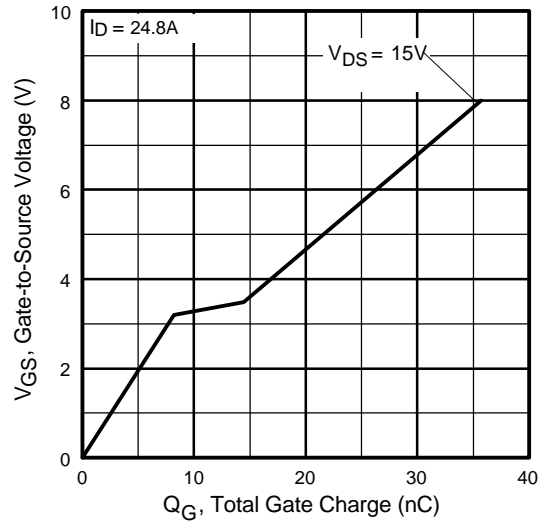


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

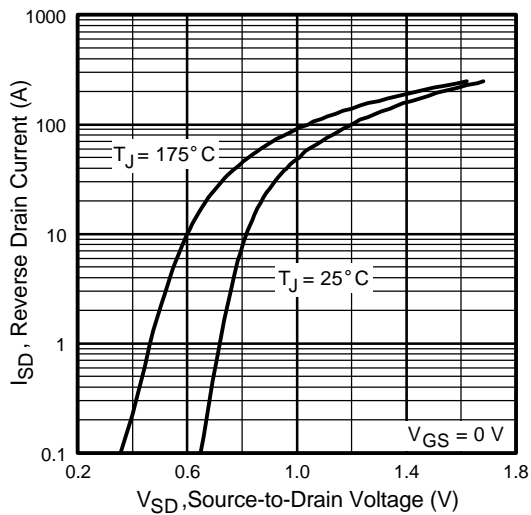


Fig 7. Typical Source-Drain Diode Forward Voltage

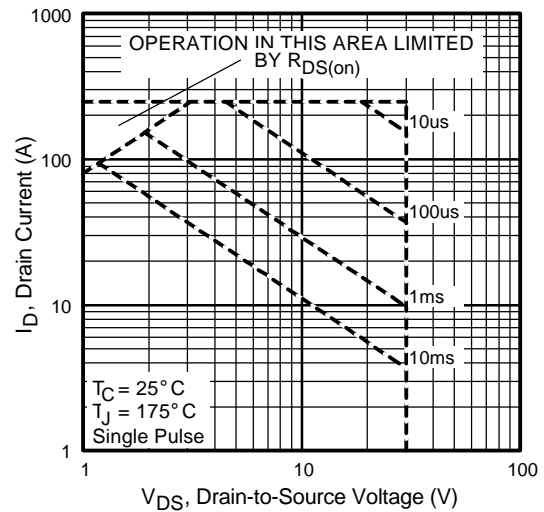


Fig 8. Maximum Safe Operating Area

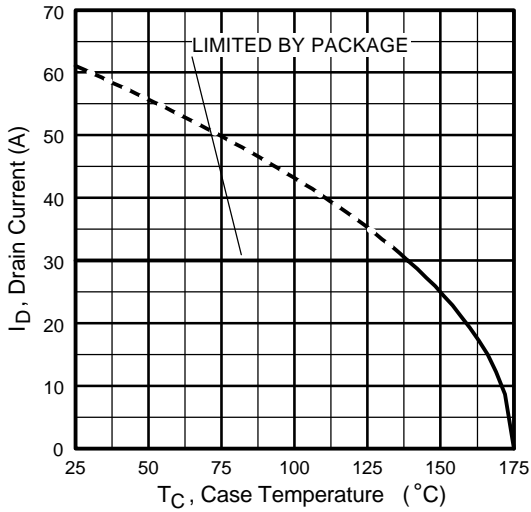


Fig 9. Maximum Drain Current Vs. Case Temperature

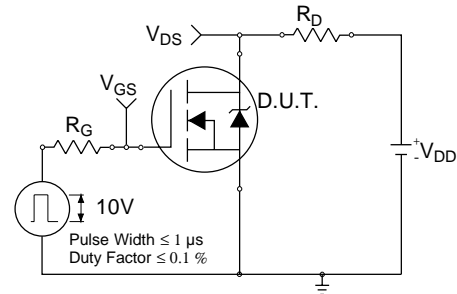


Fig 10a. Switching Time Test Circuit

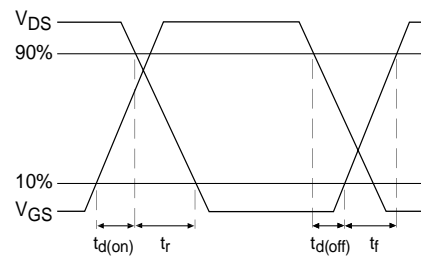


Fig 10b. Switching Time Waveforms

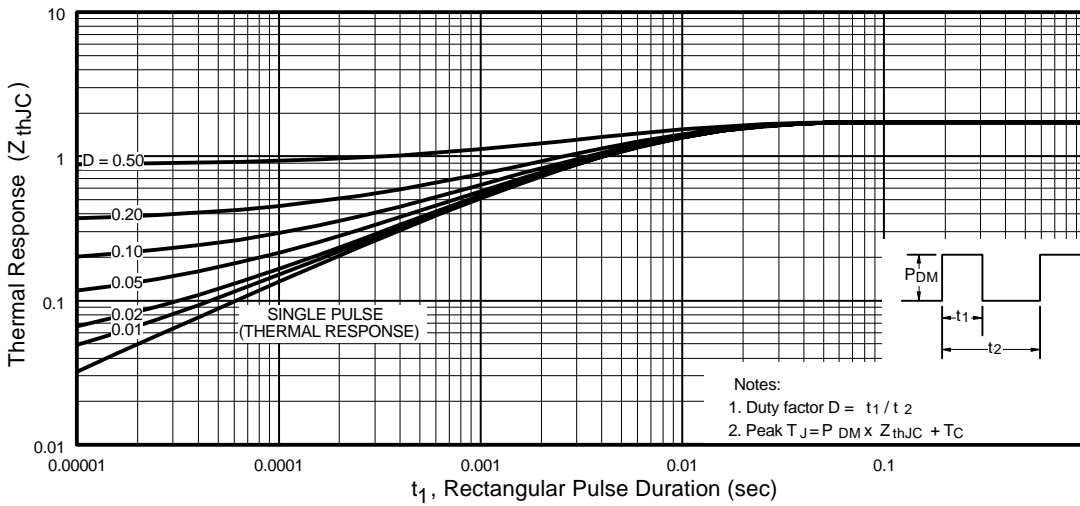


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

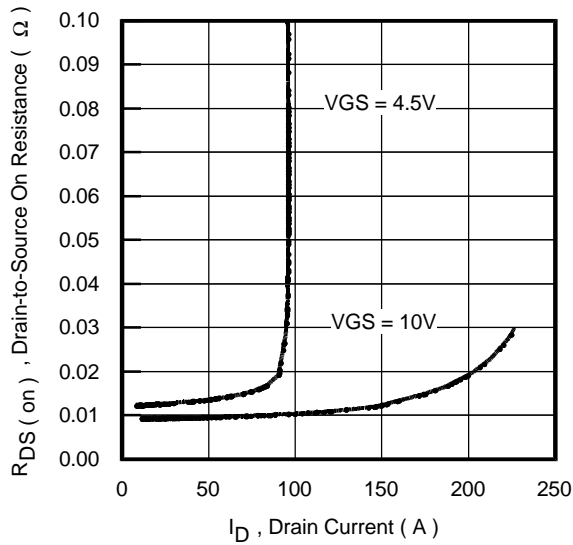


Fig 12. On-Resistance Vs. Drain Current

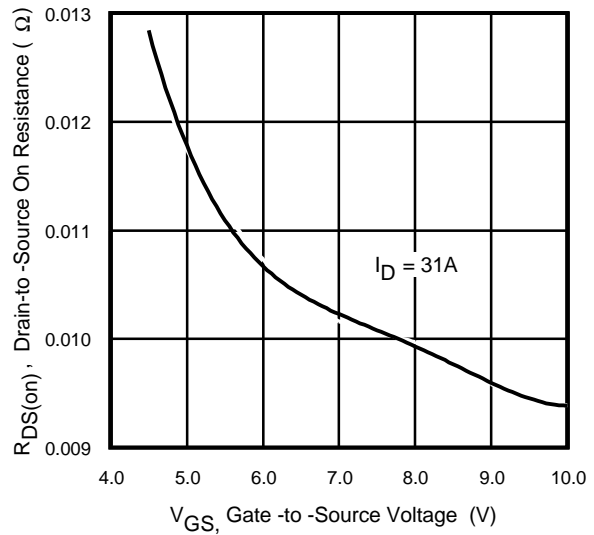


Fig 13. On-Resistance Vs. Gate Voltage

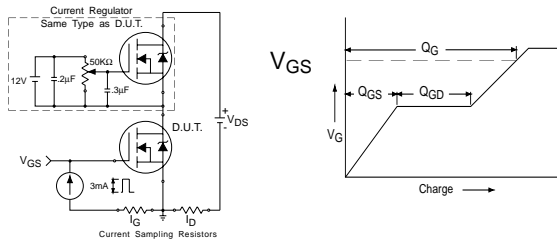


Fig 14a&b. Basic Gate Charge Test Circuit and Waveforms

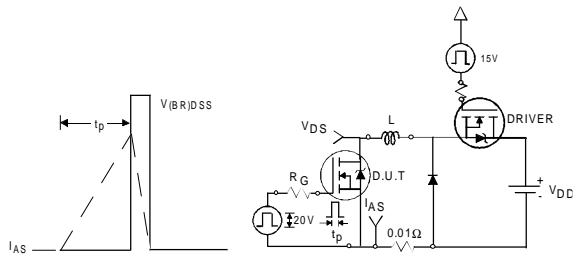


Fig 15a&b. Unclamped Inductive Test Circuit and Waveforms

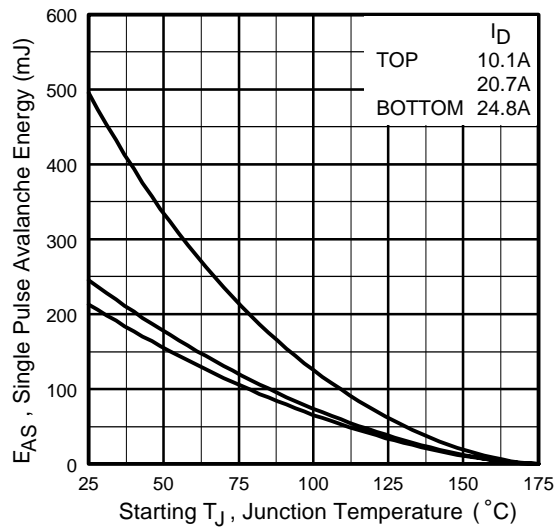
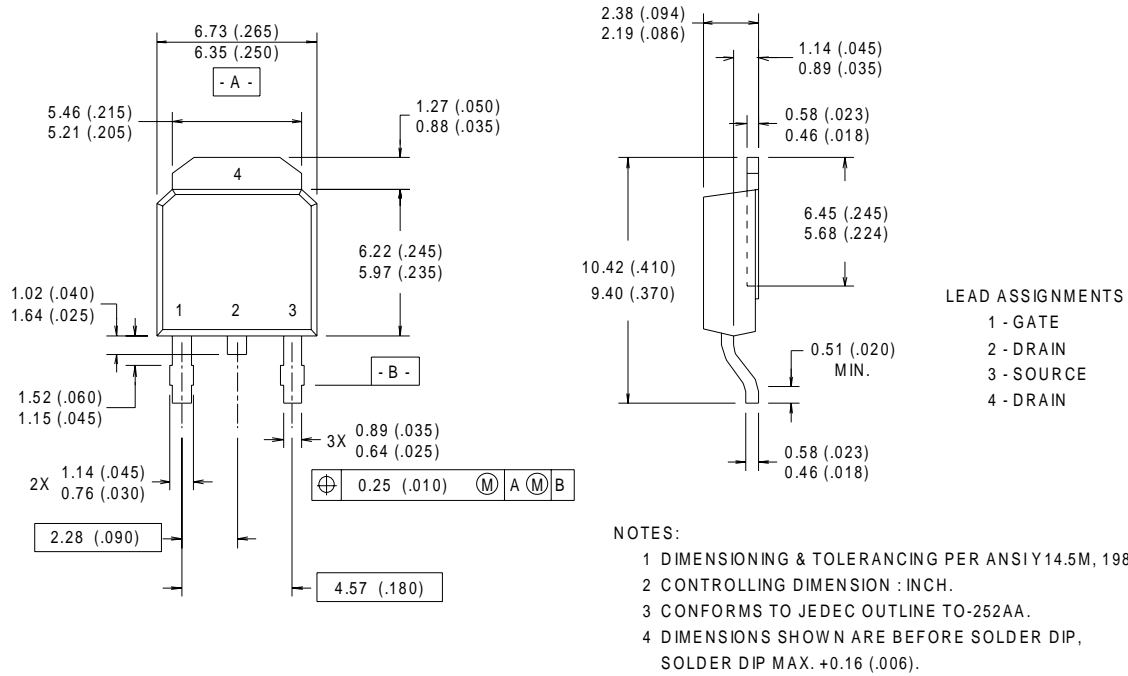


Fig 15c. Maximum Avalanche Energy Vs. Drain Current

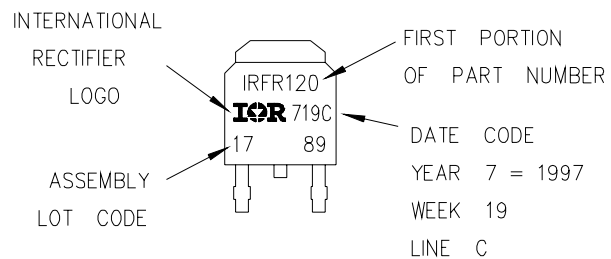
D-Pak (TO-252AA) Package Outline

Dimensions are shown in millimeters (inches)



D-Pak (TO-252AA) Part Marking Information

EXAMPLE: THIS IS AN IRFR120
 LOT CODE 1789
 ASSEMBLED ON WW 19, 1997
 IN THE ASSEMBLY LINE "C"

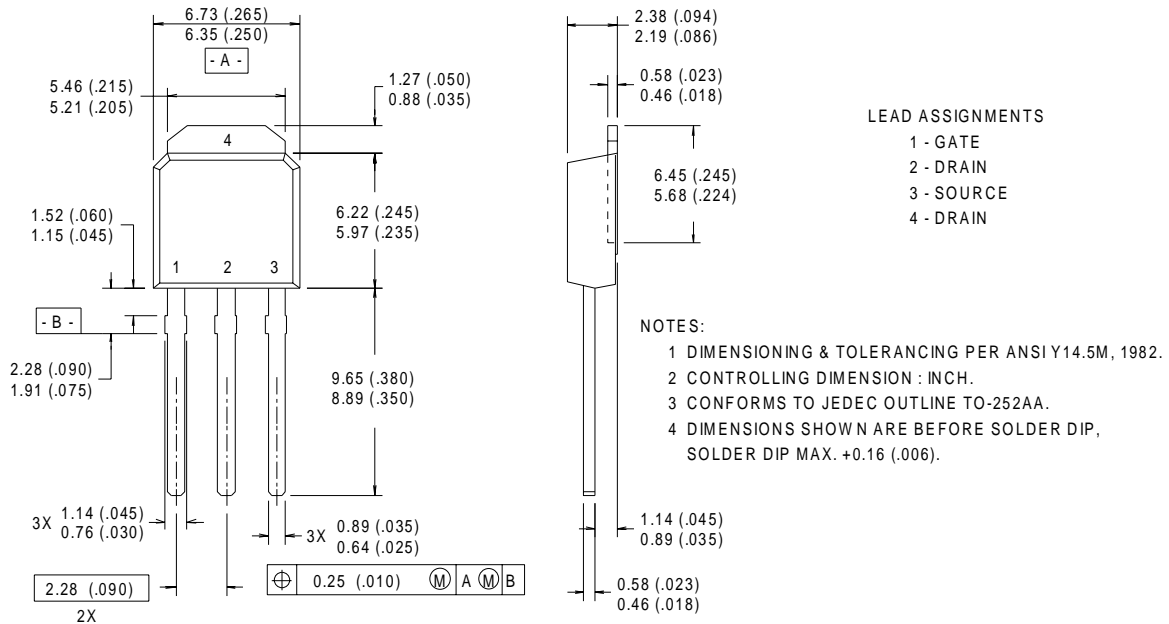


IRFR/U3707

International
IR Rectifier

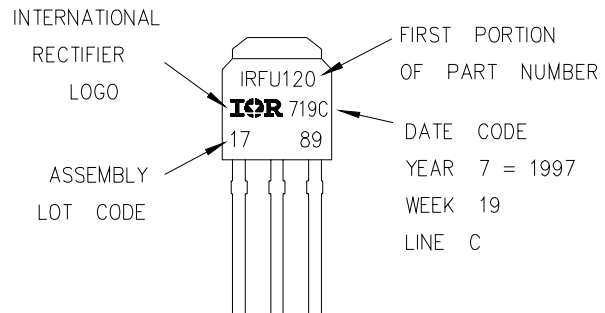
I-Pak (TO-251AA) Package Outline

Dimensions are shown in millimeters (inches)



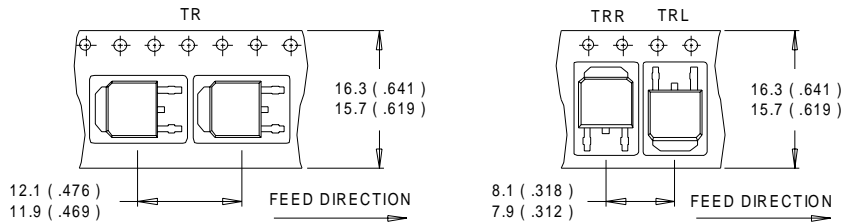
I-Pak (TO-251AA) Part Marking Information

EXAMPLE: THIS IS AN IRFU120
LOT CODE 1789
ASSEMBLED ON WW 19, 1997
IN THE ASSEMBLY LINE "C"

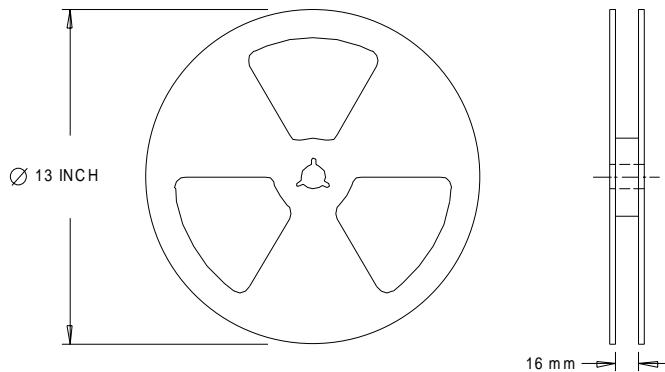


D-Pak (TO-252AA) Tape & Reel Information

Dimensions are shown in millimeters (inches)



- NOTES :
1. CONTROLLING DIMENSION : MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES :
1. OUTLINE CONFORMS TO EIA-481.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 0.7 \text{ mH}$
 $R_G = 25\Omega$, $I_{AS} = 24.8 \text{ A}$.
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 30A

Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>

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