



**THE DATASHEET OF  
IXFY4N60P3**

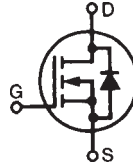


# Polar3™ HiPerFET™ Power MOSFET

**IXFY4N60P3**  
**IXFA4N60P3**  
**IXFP4N60P3**

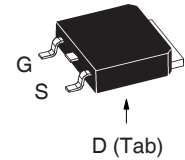
**V<sub>DSS</sub> = 600V**  
**I<sub>D25</sub> = 4A**  
**R<sub>DS(on)</sub> ≤ 2.4Ω**

N-Channel Enhancement Mode  
Avalanche Rated  
Fast Intrinsic Rectifier

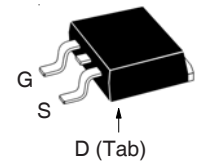


Symbol	Test Conditions	Maximum Ratings	
V <sub>DSS</sub>	T <sub>J</sub> = 25°C to 150°C	600	V
V <sub>DGR</sub>	T <sub>J</sub> = 25°C to 150°C, R <sub>GS</sub> = 1MΩ	600	V
V <sub>GSS</sub>	Continuous	±30	V
V <sub>GSM</sub>	Transient	±40	V
I <sub>D25</sub>	T <sub>C</sub> = 25°C	4	A
I <sub>DM</sub>	T <sub>C</sub> = 25°C, Pulse Width Limited by T <sub>JM</sub>	8	A
I <sub>A</sub>	T <sub>C</sub> = 25°C	2	A
E <sub>AS</sub>	T <sub>C</sub> = 25°C	200	mJ
dv/dt	I <sub>S</sub> ≤ I <sub>DM</sub> , V <sub>DD</sub> ≤ V <sub>DSS</sub> , T <sub>J</sub> ≤ 150°C	35	V/ns
P <sub>D</sub>	T <sub>C</sub> = 25°C	114	W
T <sub>J</sub>		-55 ... +150	°C
T <sub>JM</sub>		150	°C
T <sub>stg</sub>		-55 ... +150	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering	300	°C
T <sub>SOLD</sub>	Plastic Body for 10s	260	°C
M <sub>d</sub>	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in.
Weight	TO-252	0.35	g
	TO-263	2.50	g
	TO-220	3.00	g

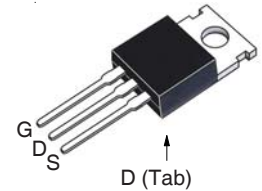
TO-252 (IXFY)



TO-263 (IXFA)



TO-220 (IXFP)



G = Gate      D = Drain  
S = Source    Tab = Drain

## Features

- International Standard Packages
- Fast Intrinsic Rectifier
- Avalanche Rated
- Low R<sub>DS(ON)</sub> and Q<sub>G</sub>
- Low Package Inductance

## Advantages

- High Power Density
- Easy to Mount
- Space Savings

## Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- Laser Drivers
- AC and DC Motor Drives
- Robotics and Servo Controls

Symbol	Test Conditions (T <sub>J</sub> = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	600		V
V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	3.0		5.0 V
I <sub>GSS</sub>	V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V			±100 nA
I <sub>DSS</sub>	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0V T <sub>J</sub> = 125°C			10 μA 100 μA
R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.5 • I <sub>D25</sub> , Note 1			2.4 Ω

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
$g_{fs}$	$V_{DS} = 20\text{V}$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1	2.2	3.7	S
$R_{Gi}$	Gate Input Resistance		6.0	$\Omega$
$C_{iss}$	} $V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$		365	pF
$C_{oss}$			46	pF
$C_{rss}$			3	pF
$t_{d(on)}$	} <b>Resistive Switching Times</b> $V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$ $R_G = 30\Omega$ (External)		15	ns
$t_r$			24	ns
$t_{d(off)}$			24	ns
$t_f$			23	ns
$Q_{g(on)}$	} $V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$		6.9	nC
$Q_{gs}$			1.7	nC
$Q_{gd}$			2.8	nC
$R_{thJC}$	TO-220			1.10 $^\circ\text{C/W}$
$R_{thCS}$		0.50		$^\circ\text{C/W}$

**Source-Drain Diode**

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
$I_S$	$V_{GS} = 0\text{V}$ , Note 1			4 A
$I_{SM}$	Repetitive, Pulse Width Limited by $T_{JM}$			16 A
$V_{SD}$	$I_F = I_S$ , $V_{GS} = 0\text{V}$ , Note 1			1.4 V
$t_{rr}$	} $I_F = 4\text{A}$ , $-di/dt = 25\text{A}/\mu\text{s}$ $V_R = 100\text{V}$			250 ns
$Q_{RM}$			0.35	$\mu\text{C}$
$I_{RM}$			2.15	A

Note 1. Pulse test,  $t \leq 300\mu\text{s}$ , duty cycle,  $d \leq 2\%$ .

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065B1	6,683,344	6,727,585	7,005,734B2	7,157,338B2
	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123B1	6,534,343	6,710,405B2	6,759,692	7,063,975B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728B1	6,583,505	6,710,463	6,771,478B2	7,071,537	

Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$

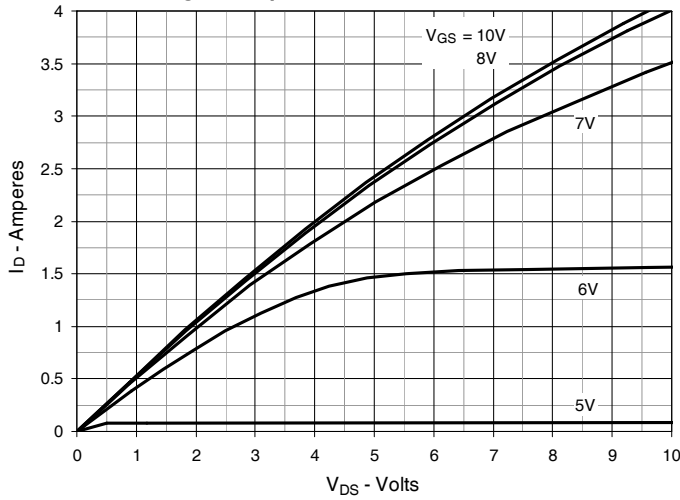


Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$

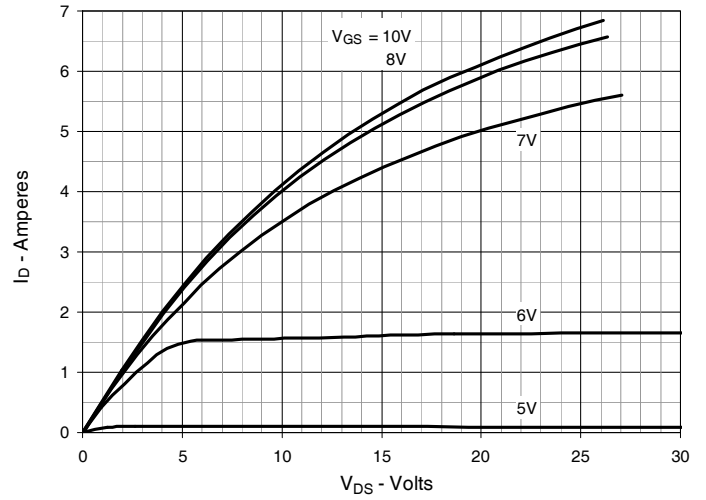


Fig. 3. Output Characteristics @  $T_J = 125^\circ\text{C}$

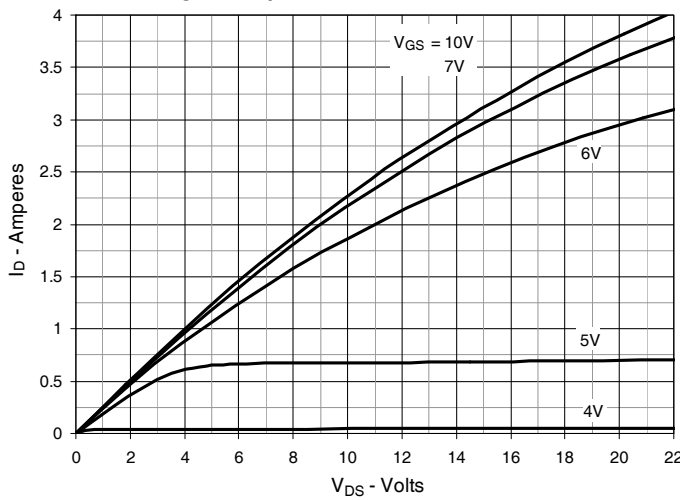


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 2\text{A}$  Value vs. Junction Temperature

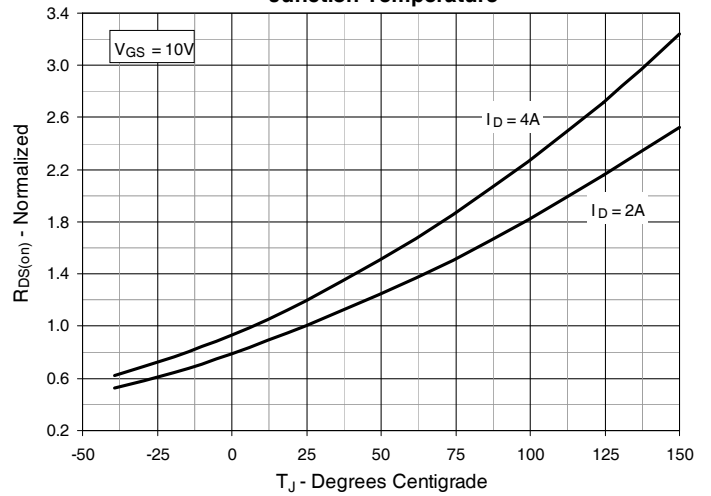


Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 2\text{A}$  Value vs. Drain Current

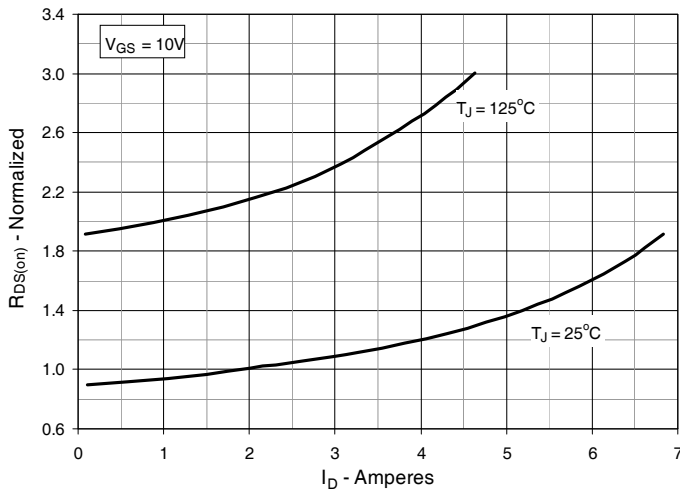


Fig. 6. Maximum Drain Current vs. Case Temperature

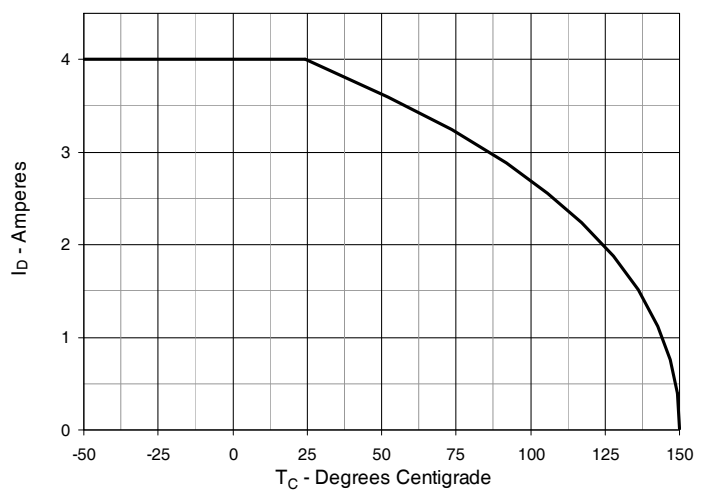


Fig. 7. Input Admittance

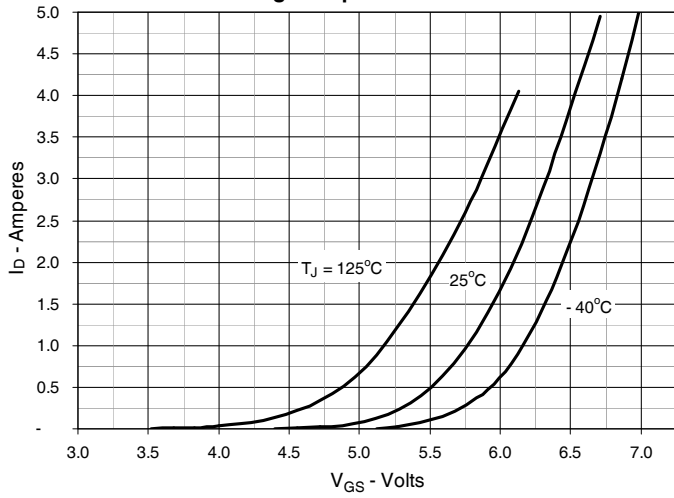


Fig. 8. Transconductance

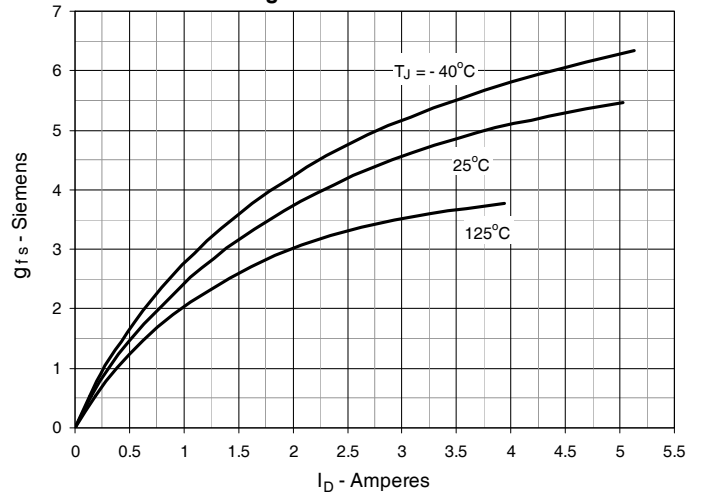


Fig. 9. Forward Voltage Drop of Intrinsic Diode

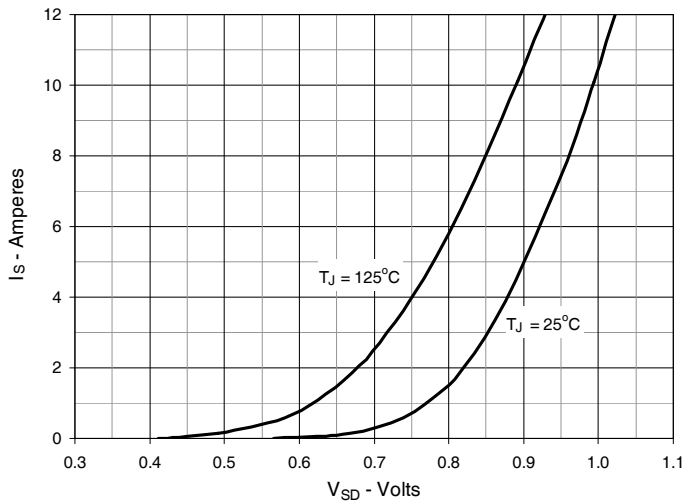


Fig. 10. Gate Charge

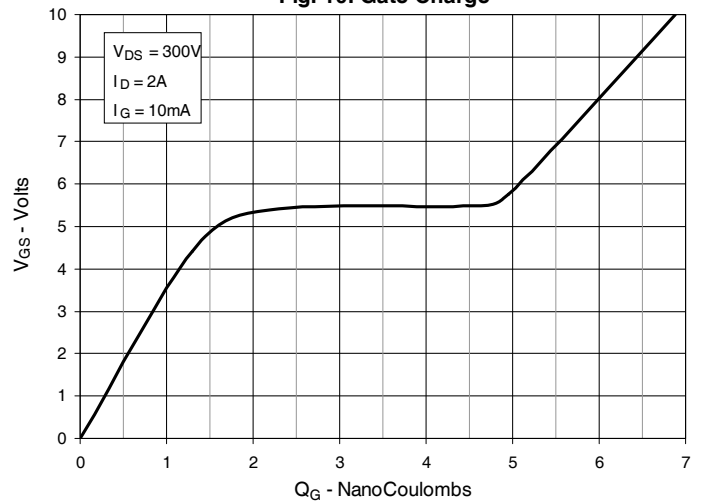


Fig. 11. Capacitance

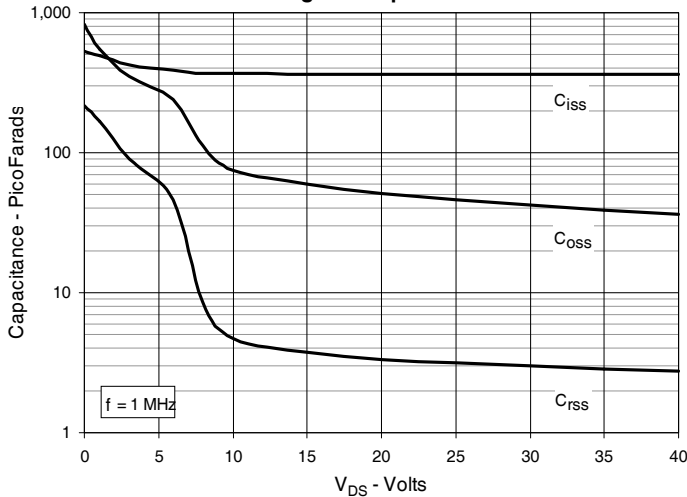


Fig. 12. Forward-Bias Safe Operating Area

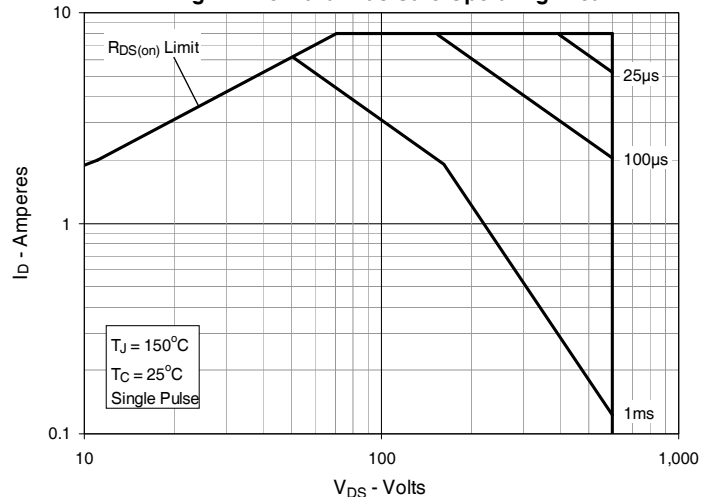
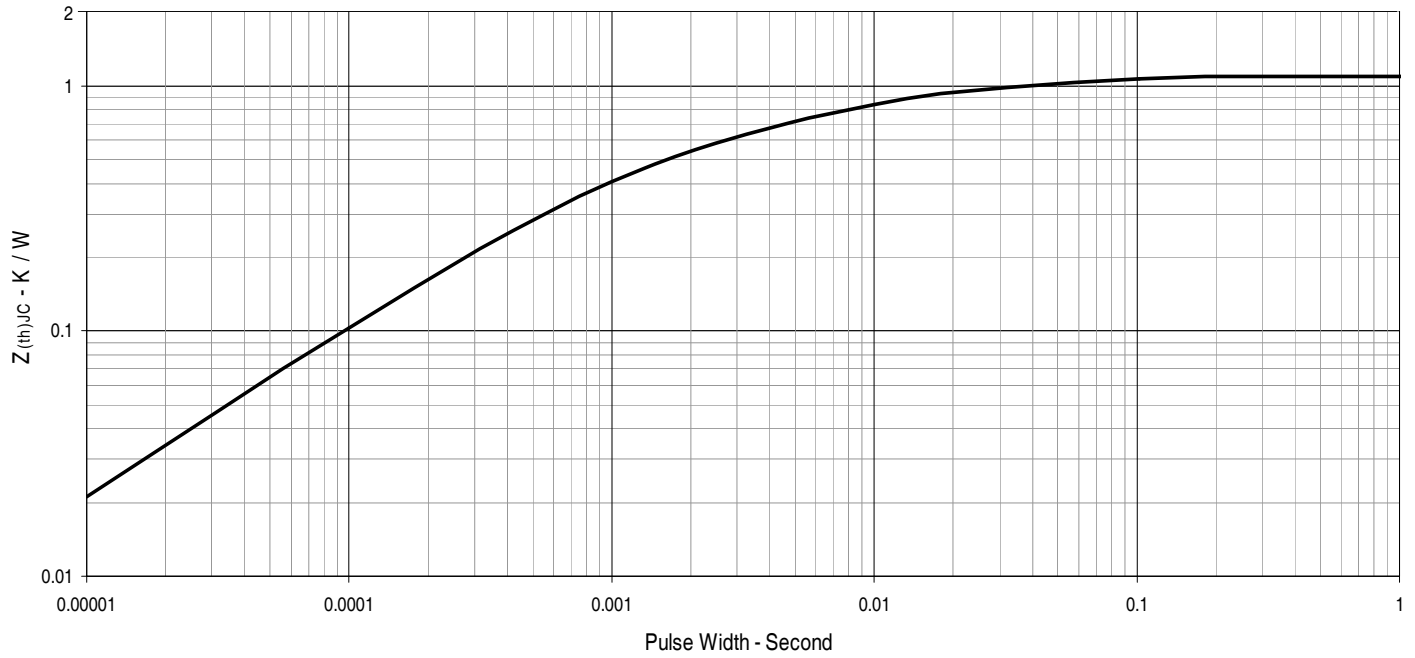


Fig. 13. Maximum Transient Thermal Impedance







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