



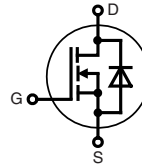
**THE DATASHEET OF
IXKP10N60C5**



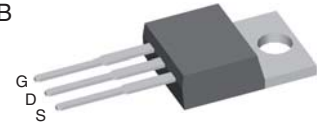
CoolMOS™ 1, Power MOSFET

N-Channel Enhancement Mode
 Low $R_{DS(on)}$, High V_{DSS} MOSFET
 Ultra low gate charge

I_{D25} = 10 A
 V_{DSS} = 600 V
 $R_{DS(on) \text{ max}}$ = 0.385 Ω



TO-220 AB



MOSFET			
Symbol	Conditions	Maximum Ratings	
V_{DSS}	$T_{VJ} = 25^{\circ}\text{C}$	600	V
V_{GS}		± 20	V
I_{D25}	$T_C = 25^{\circ}\text{C}$	10	A
I_{D90}	$T_C = 90^{\circ}\text{C}$	7	A
E_{AS} E_{AR}	single pulse repetitive } $I_D = 3.4 \text{ A}; T_C = 25^{\circ}\text{C}$	225 0.3	mJ mJ
dV/dt	MOSFET dV/dt ruggedness $V_{DS} = 0 \dots 480 \text{ V}$	50	V/ns

Features

- fast CoolMOS™ 1) power MOSFET 4th generation
- High blocking capability
- Lowest resistance
- Avalanche rated for unclamped inductive switching (UIS)
- Low thermal resistance due to reduced chip thickness
- Enhanced total power density

Applications

- Switched mode power supplies (SMPS)
- Uninterruptible power supplies (UPS)
- Power factor correction (PFC)
- Welding
- Inductive heating
- PDP and LCD adapter

Symbol	Conditions	Characteristic Values			
		(T _{VJ} = 25°C, unless otherwise specified)			
		min.	typ.	max.	
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}; I_D = 5.2 \text{ A}$		350	385	m Ω
$V_{GS(th)}$	$V_{DS} = V_{GS}; I_D = 0.34 \text{ mA}$	2.5	3	3.5	V
I_{DSS}	$V_{DS} = 600 \text{ V}; V_{GS} = 0 \text{ V}$			1	μA
				10	μA
I_{GSS}	$V_{GS} = \pm 20 \text{ V}; V_{DS} = 0 \text{ V}$			100	nA
C_{iss} C_{oss}	} $V_{GS} = 0 \text{ V}; V_{DS} = 100 \text{ V}$ } $f = 1 \text{ MHz}$		790 38		pF pF
Q_g Q_{gs} Q_{gd}	} $V_{GS} = 0 \text{ to } 10 \text{ V}; V_{DS} = 400 \text{ V}; I_D = 5.2 \text{ A}$		17 4 6	22	ns ns ns
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	} $V_{GS} = 10 \text{ V}; V_{DS} = 400 \text{ V}$ } $I_D = 5.2 \text{ A}; R_G = 3.3 \Omega$		10 5 40 5		ns ns ns ns
R_{thJC}				1.15	K/W

¹⁾ CoolMOS™ is a trademark of Infineon Technologies AG.

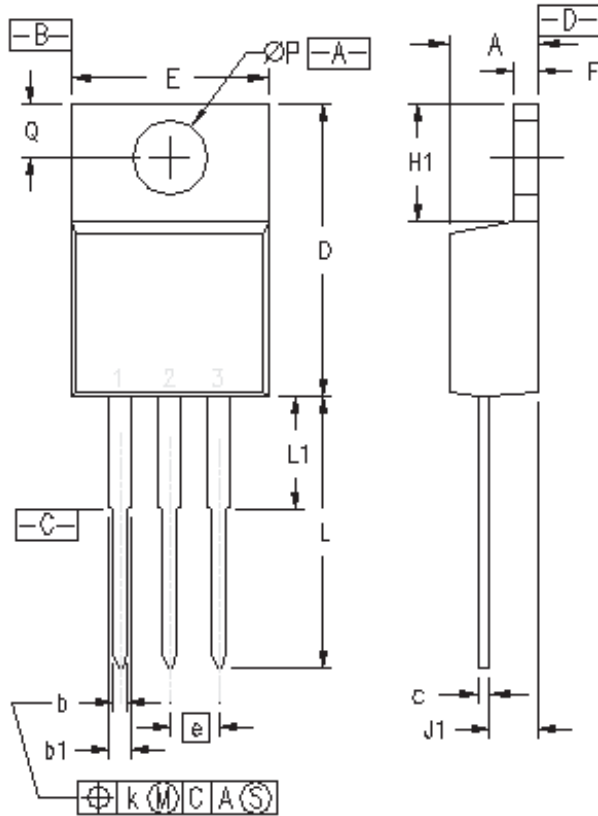
Source-Drain Diode

Symbol	Conditions	Characteristic Values			
		min.	typ.	max.	
(T _{VJ} = 25°C, unless otherwise specified)					
I _S	V _{GS} = 0 V			5.2	A
V _{SD}	I _F = 5.2 A; V _{GS} = 0 V		0.9	1.2	V
t _{rr}	I _F = 5.2 A; -di _F /dt = 100 A/μs; V _R = 400 V		260		ns
Q _{RM}			3.1		μC
I _{RM}			24		A

Component

Symbol	Conditions	Maximum Ratings			
		min.	typ.	max.	
T _{VJ}	operating		-55...+150		°C
T _{stg}			-55...+150		°C
M _d	mounting torque		0.4 ... 0.6		Nm
Symbol	Conditions	Characteristic Values			
		min.	typ.	max.	
R _{thCH}	with heatsink compound		0.50		K/W
Weight			2		g

TO-220 AB Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100 BSC		2.54 BSC	
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
$\varnothing P$.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

NOTE: This drawing will meet all dimensions requirement of JEDEC outline TO-220 AB.

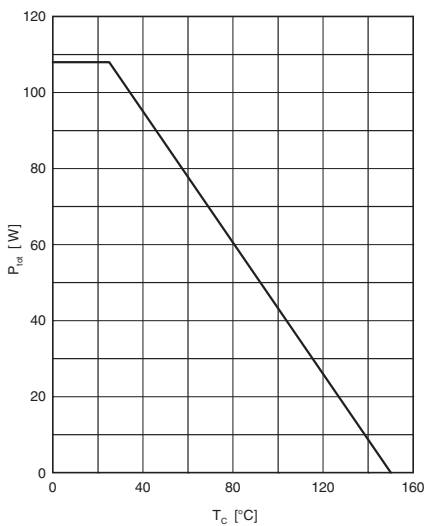


Fig. 1 Power dissipation

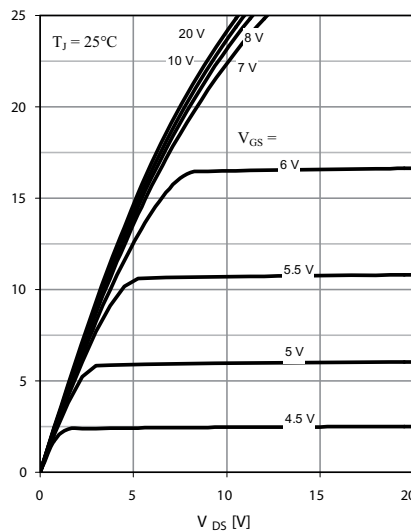


Fig. 2 Typ. output characteristics

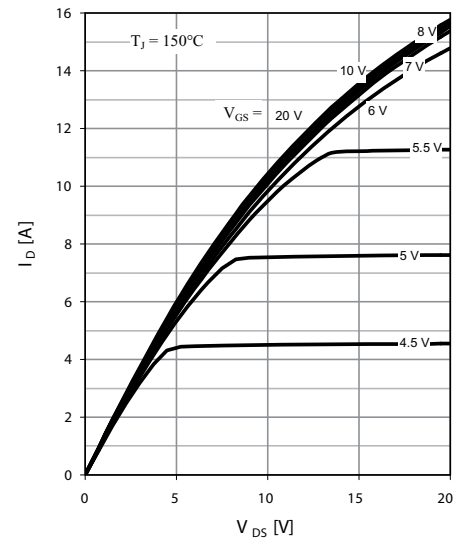


Fig. 3 Typ. output characteristics

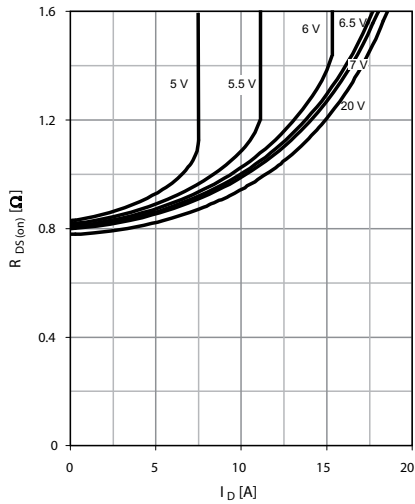


Fig. 4 Typ. drain-source on-state resistance characteristics of IGBT

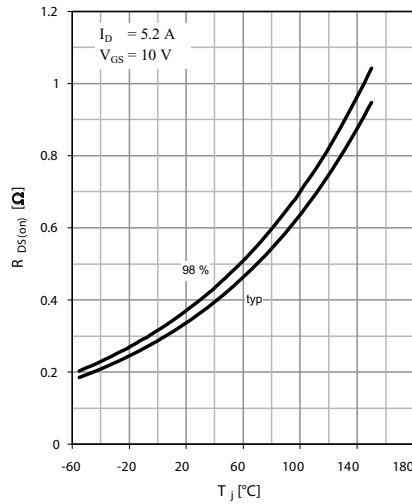


Fig. 5 Drain-source on-state resistance

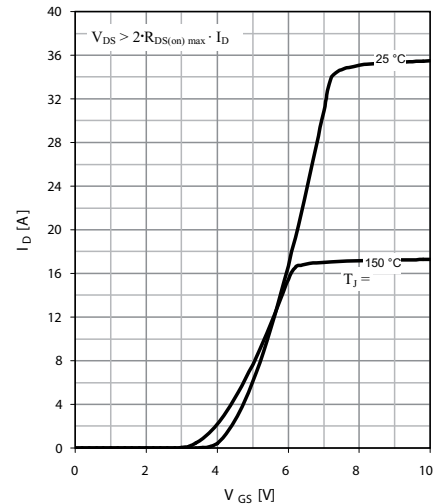


Fig. 6 Typ. transfer characteristics

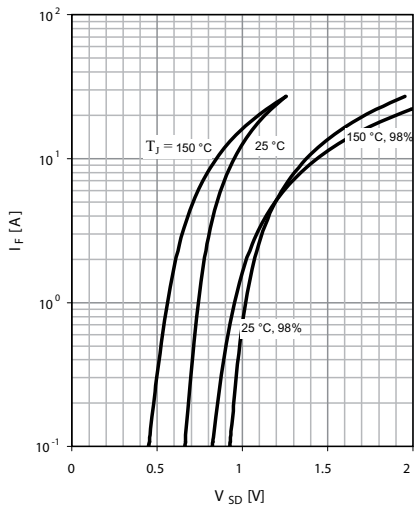


Fig. 7 Forward characteristic of reverse diode

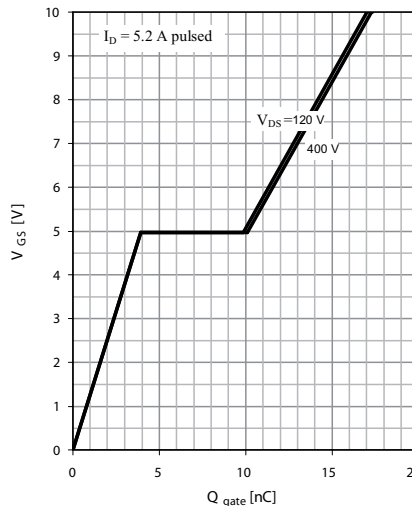


Fig. 8 Typ. gate charge

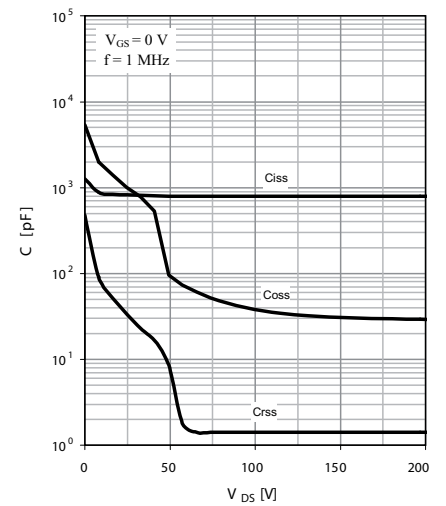


Fig. 9 Typ. capacitances

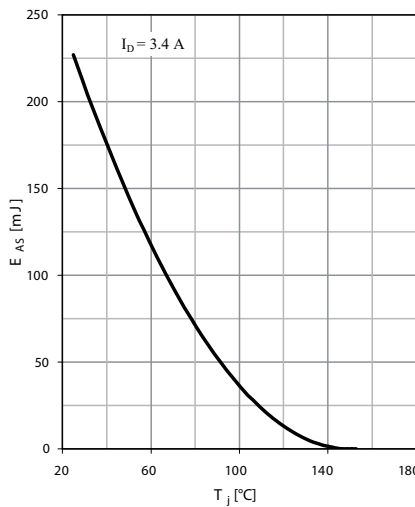


Fig. 10 Avalanche energy

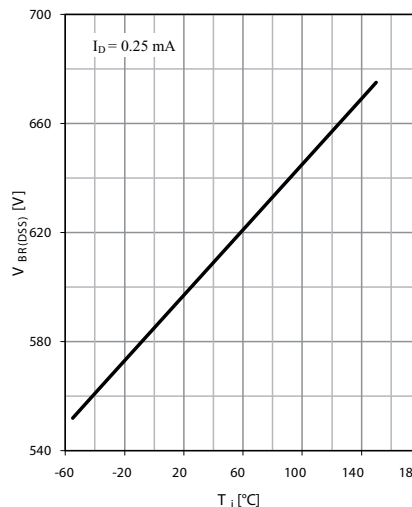


Fig. 11 Drain-source breakdown voltage

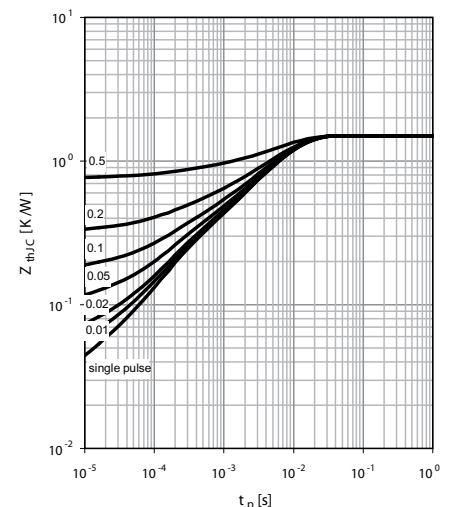


Fig. 12 Max. transient thermal impedance

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