

4V Drive Nch MOSFET

RSJ10HN06

● Structure

Silicon N-channel MOSFET

● Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) High power Package

● Application

Switching

● Packaging specifications

Type	Package	Taping
	Code	TL
	Basic ordering unit (pieces)	1000
RSJ10HN06		○

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	V _{DSS}	60	V	
Gate-source voltage	V _{GSS}	±20	V	
Drain current	Continuous	I _D *3	±100	A
	Pulsed	I _{DP} *1	±200	A
Source current (Body Diode)	Continuous	I _S *3	100	A
	Pulsed	I _{SP} *1	200	A
Power dissipation	P _D *2	100	W	
Channel temperature	T _{ch}	150	°C	
Range of storage temperature	T _{stg}	-55 to +150	°C	

*1 P_w≤10μs, Duty cycle≤1%

*2 T_c=25°C

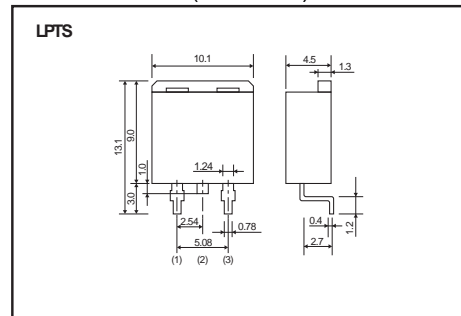
*3 Limited only by maximum channel temperature allowed.

● Thermal resistance

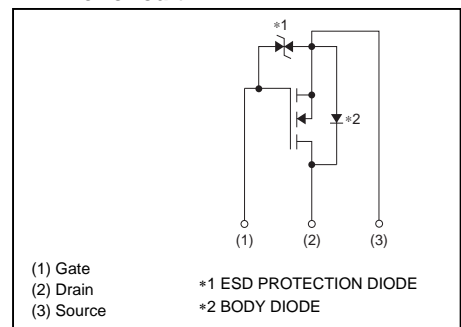
Parameter	Symbol	Limits	Unit
Channel to Case	R _{th (ch-c)} *	1.25	°C / W

* T_c=25°C

● Dimensions (Unit : mm)



● Inner circuit



●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR)DSS}	60	-	-	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	-	1	μA	V _{DS} =60V, V _{GS} =0V
Gate threshold voltage	V _{GS(th)}	1.0	-	2.5	V	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS(on)} *	-	3.0	4.2	mΩ	I _D =50A, V _{GS} =10V
		-	3.5	4.9		I _D =50A, V _{GS} =4V
Forward transfer admittance	Y _{fs} *	60	-	-	S	V _{DS} =10V, I _D =50A
Input capacitance	C _{iss}	-	11000	-	pF	V _{DS} =10V
Output capacitance	C _{oss}	-	2000	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	-	1050	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	-	50	-	ns	V _{DD} ≒30V, I _D =50A
Rise time	t _r *	-	470	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	-	420	-	ns	R _L =0.6Ω
Fall time	t _f *	-	710	-	ns	R _G =10Ω
Total gate charge	Q _g *	-	202	-	nC	V _{DD} ≒30V, I _D =50A
Gate-source charge	Q _{gs} *	-	35	-	nC	V _{GS} =10V
Gate-drain charge	Q _{gd} *	-	42	-	nC	

*Pulsed

●Body diode characteristics (Source-Drain)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.5	V	I _s =100A, V _{GS} =0V

*Pulsed

●Electrical characteristic curves (Ta=25°C)

Fig.1 Typical Output Characteristics (I)

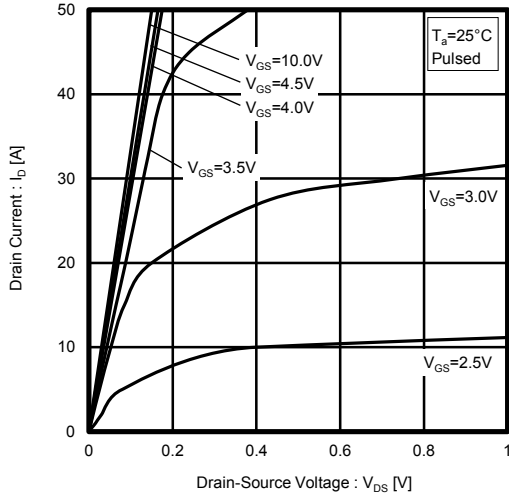


Fig.2 Typical Output Characteristics (II)

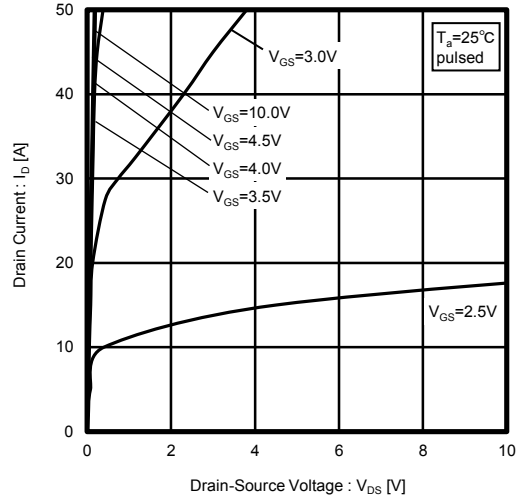


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

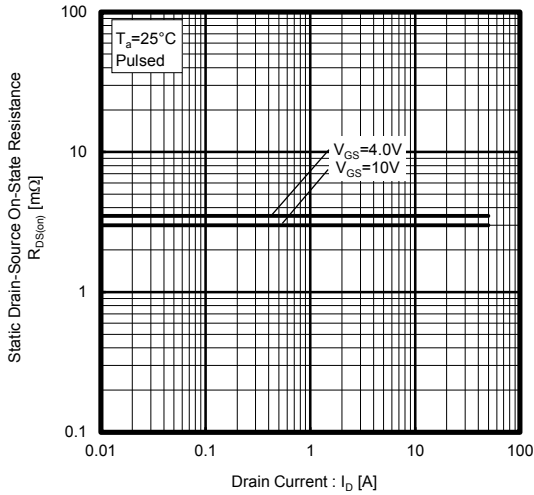


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

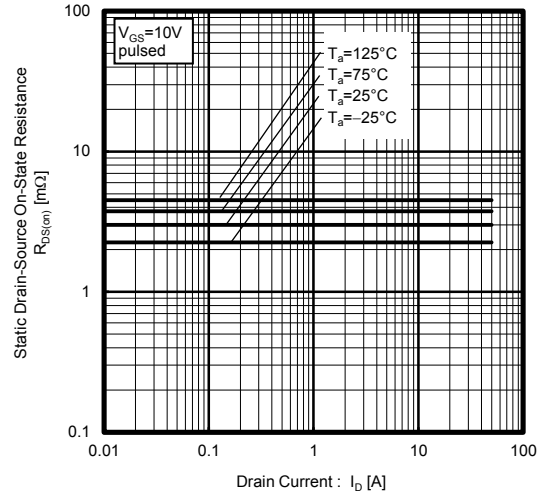


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

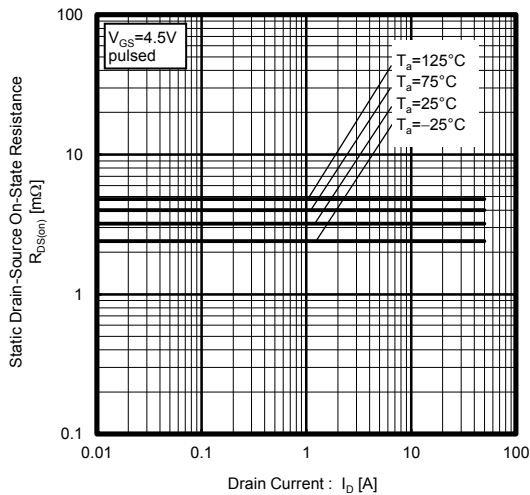


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current

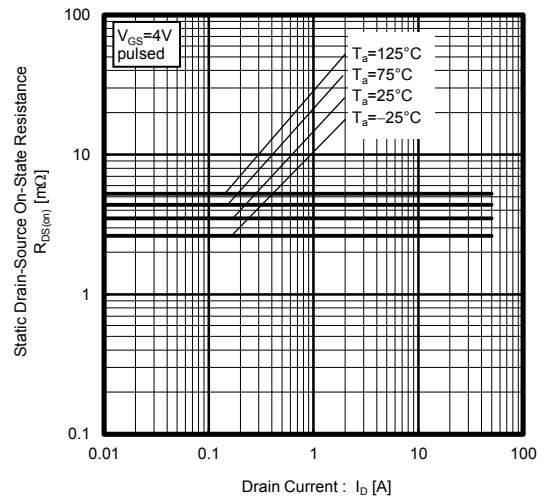


Fig.7 Forward Transfer Admittance vs. Drain Current

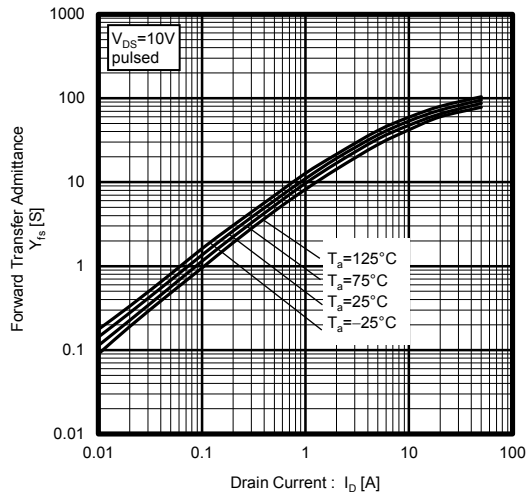


Fig.8 Typical Transfer Characteristics

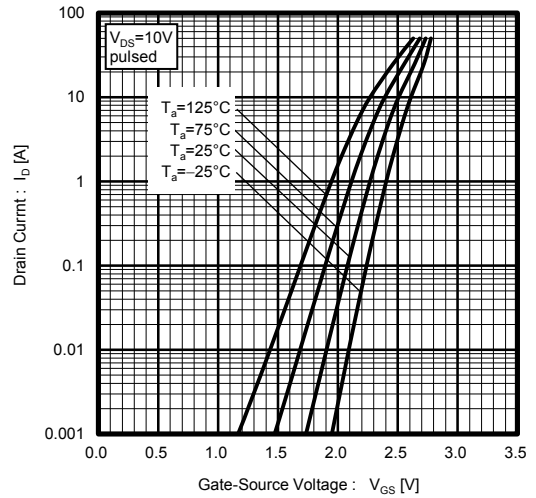


Fig.9 Source Current vs. Source-Drain Voltage

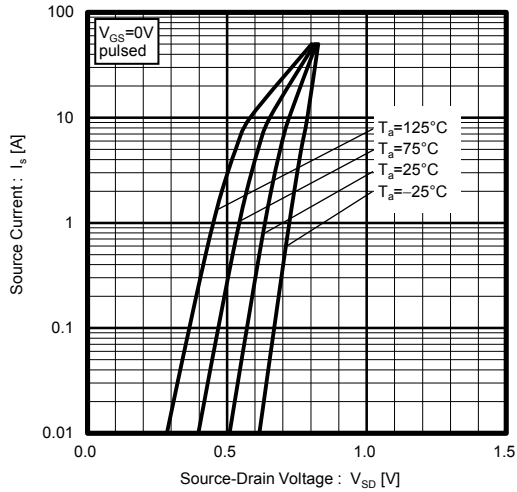


Fig.10 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

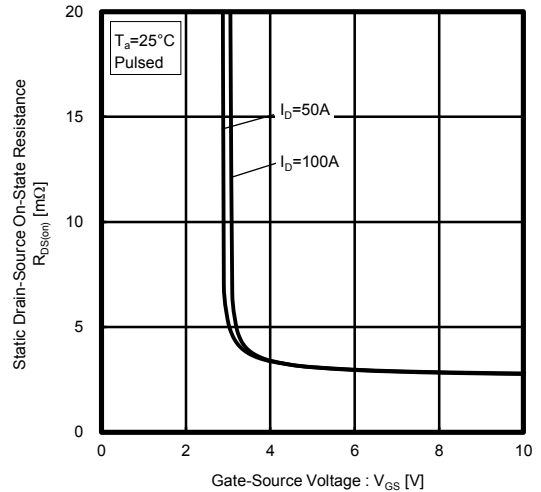


Fig.11 Switching Characteristics

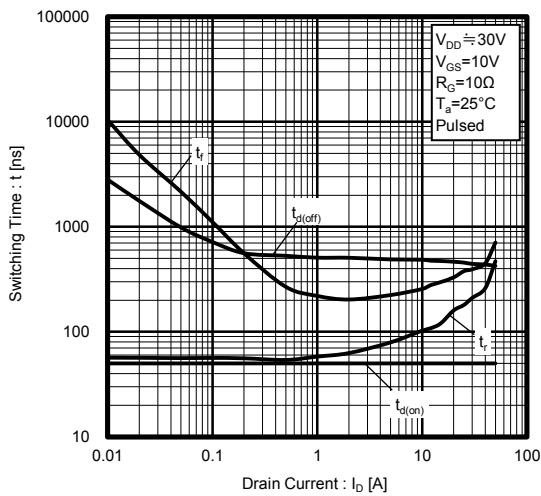


Fig.12 Dynamic Input Characteristics

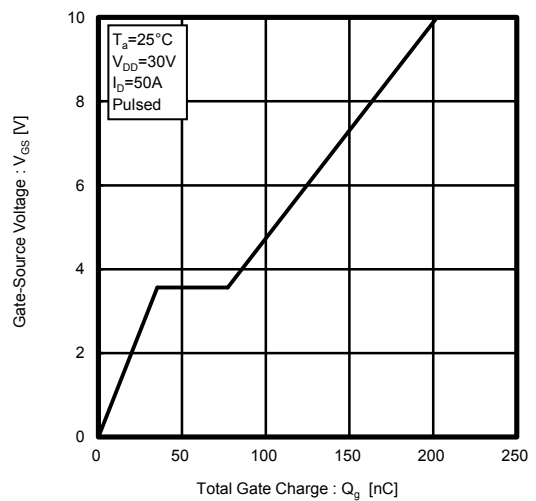


Fig.13 Typical Capacitance vs. Drain-Source Voltage

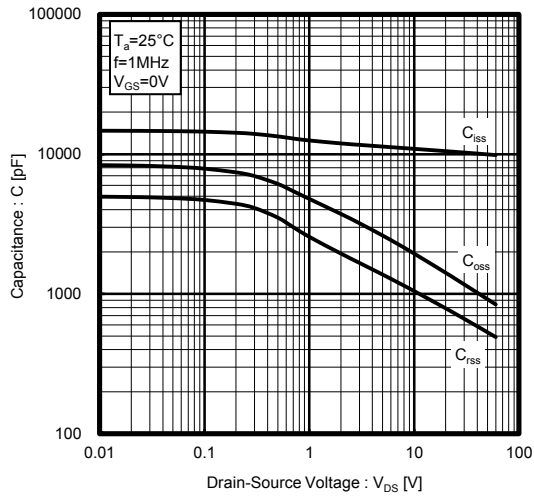
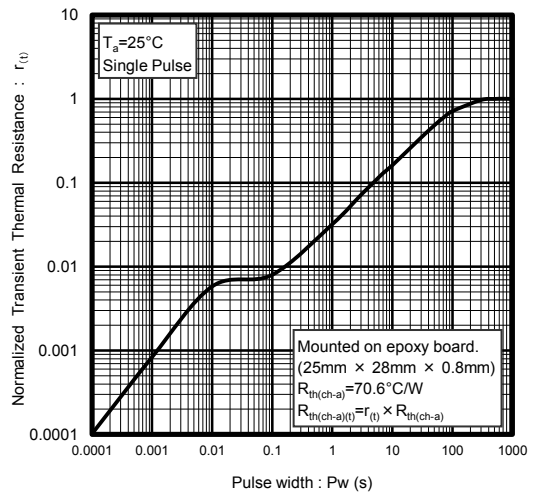


Fig.14 Normalized Transient Thermal Resistance v.s. Pulse Width



●Electrical characteristics ($T_a = 25^\circ\text{C}$)

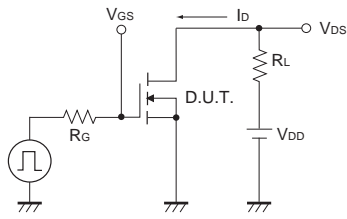


Fig.1-1 Switching Time Measurement Circuit

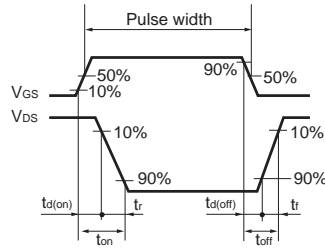


Fig.1-2 Switching Waveforms

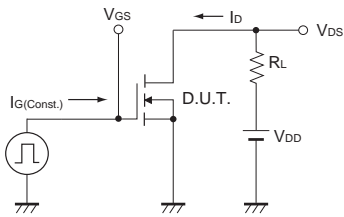


Fig.2-1 Gate Charge Measurement Circuit

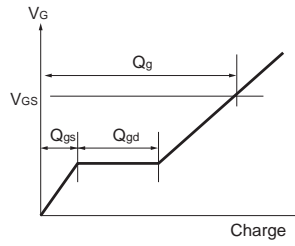


Fig.2-2 Gate Charge Waveform

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