



**THE DATASHEET OF  
DMN13H750S-7**



## Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$ $T_A = +25^\circ\text{C}$
130V	0.75Ω @ $V_{GS} = 10\text{V}$	1.0A
	0.85Ω @ $V_{GS} = 6.0\text{V}$	0.9A

## Description

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

## Features and Benefits

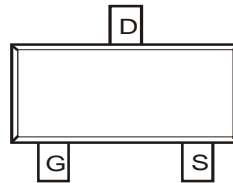
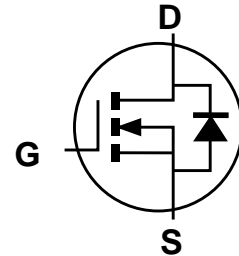
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208 (E3)
- Lead Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe)
- Terminal Connections: See Diagram
- Weight: 0.009 grams (Approximate)



Top View


 Top View  
Pin Configuration


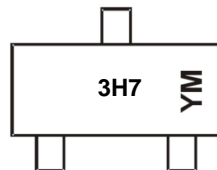
Equivalent Circuit

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN13H750S-7	SOT23	3,000/Tape & Reel
DMN13H750S-13	SOT23	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



3H7 = Product Type Marking Code  
 YM = Date Code Marking  
 Y or Y = Year (ex: C = 2015)  
 M = Month (ex: 9 = September)

Date Code Key

Year	2014	2015	2016	2017	2018	2019	2020	2021				
Code	B	C	D	E	F	G	H	I				
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	130	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	1.0 0.8	A
	t < 10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	1.2 1.0	A
Pulsed Drain Current (10µs Pulse, Duty Cycle ≤ 1%)			I <sub>DM</sub>	3.3	A
Maximum Body Diode Continuous Current (Note 6)			I <sub>S</sub>	1.0	A

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation	(Note 5)	P <sub>D</sub>	0.77	W
	(Note 6)		1.26	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R <sub>θJA</sub>	163	°C/W
	t < 10s		115	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	R <sub>θJA</sub>	99	
	t < 10s		70	
Thermal Resistance, Junction to Case	(Note 6)	R <sub>θJC</sub>	17.3	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	130	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	100	nA	V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V
Gate-Body Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.0	2.7	4.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	0.41	0.75	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.0A
		—	0.43	0.85		V <sub>GS</sub> = 6.0V, I <sub>D</sub> = 2.0A
Diode Forward Voltage	V <sub>SD</sub>	—	0.8	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.0A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iSS</sub>	—	231	—	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	19	—		
Reverse Transfer Capacitance	C <sub>rSS</sub>	—	11	—		
Gate Resistance	R <sub>G</sub>	—	2.3	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge	Q <sub>g</sub>	—	5.6	—	nC	V <sub>DS</sub> = 104V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.0A
Gate-Source Charge	Q <sub>gs</sub>	—	0.8	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	2.0	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	2.3	—	ns	V <sub>DS</sub> = 65V, I <sub>D</sub> = 2.0A, V <sub>GS</sub> = 10V, R <sub>G</sub> = 6.0Ω
Turn-On Rise Time	t <sub>R</sub>	—	1.7	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	6.6	—		
Turn-Off Fall Time	t <sub>F</sub>	—	1.7	—		
Reverse Recovery Time	t <sub>RR</sub>	—	26	—	ns	V <sub>R</sub> = 100V, I <sub>F</sub> = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	—	21	—		

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

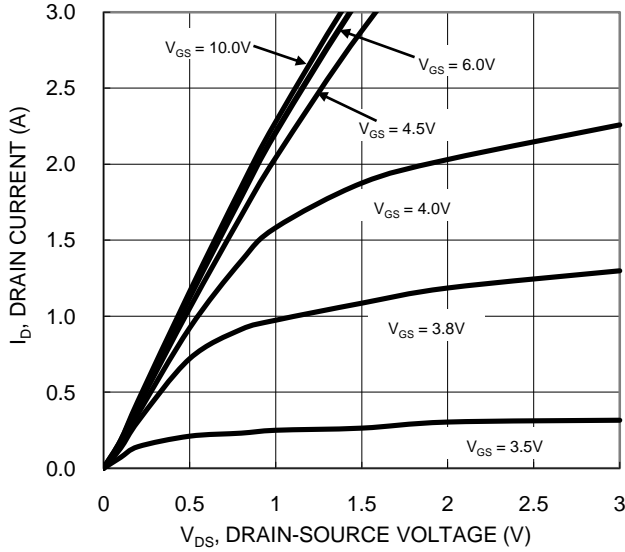


Figure 1. Typical Output Characteristic

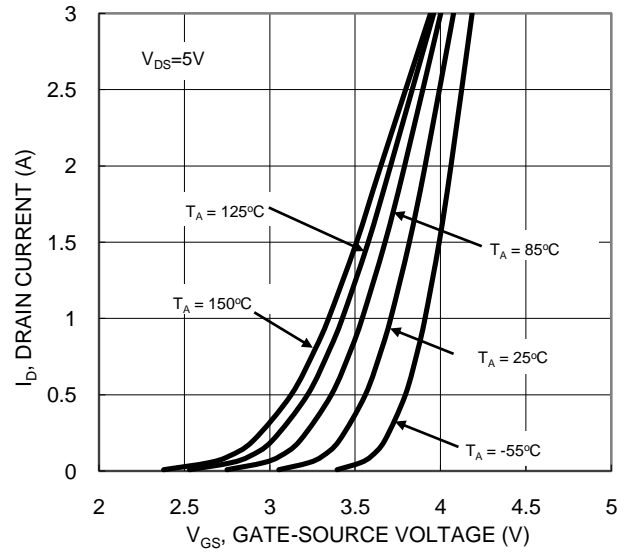


Figure 2. Typical Transfer Characteristic

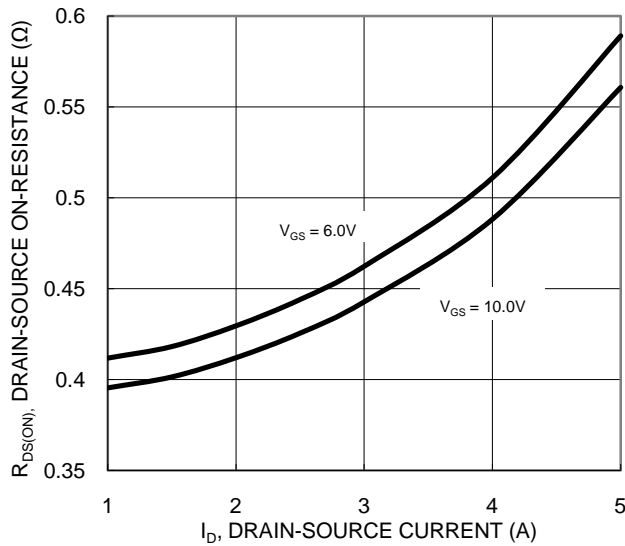


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

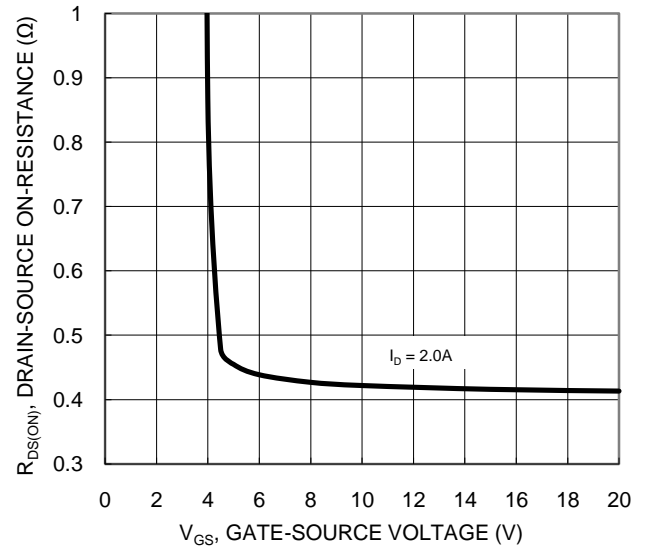


Figure 4. Typical Transfer Characteristic

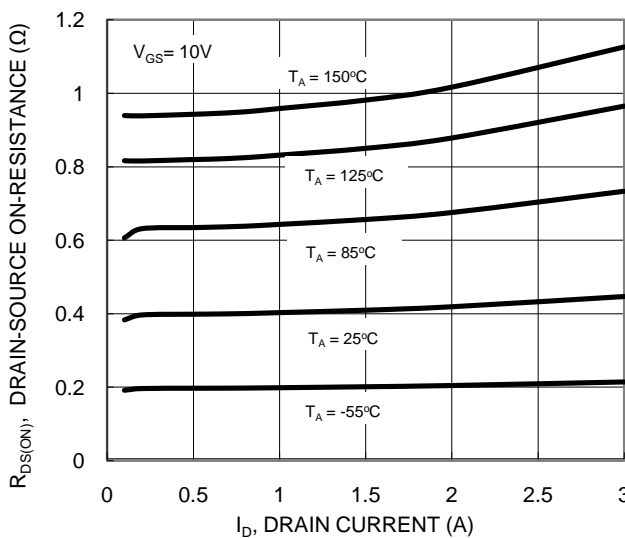


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

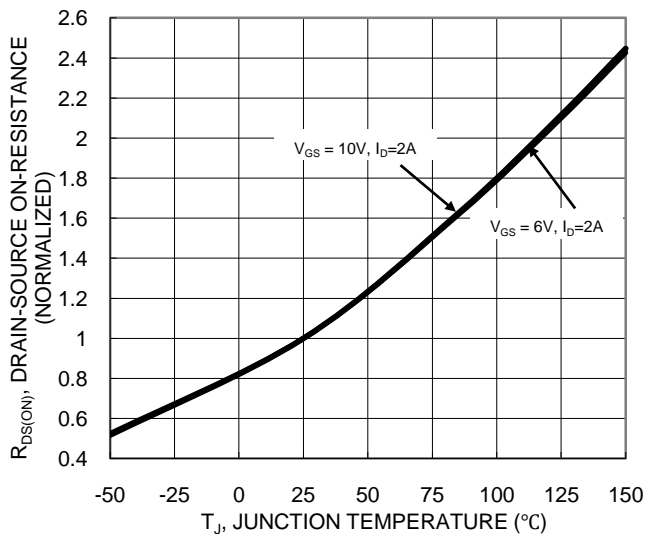


Figure 6. On-Resistance Variation with Temperature

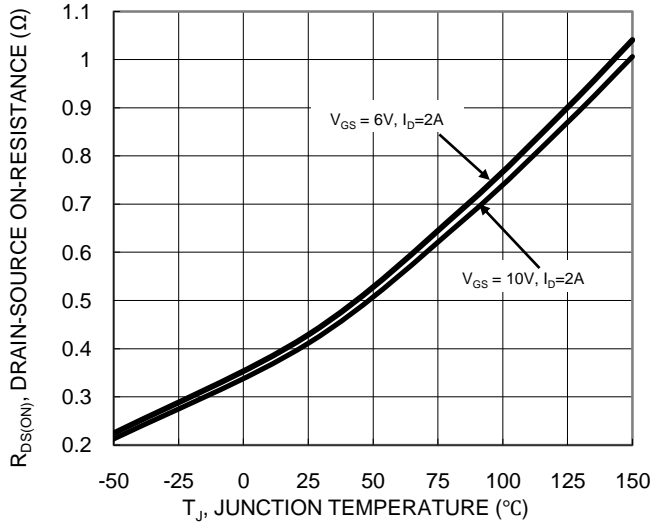


Figure 7. On-Resistance Variation with Temperature

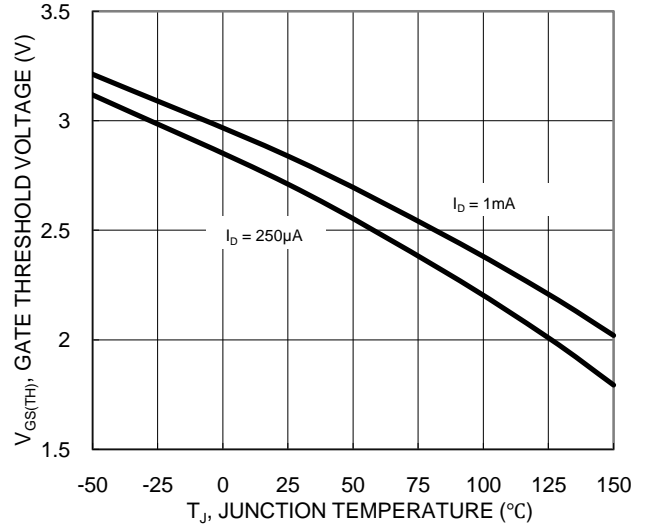


Figure 8. Gate Threshold Variation vs. Junction Temperature

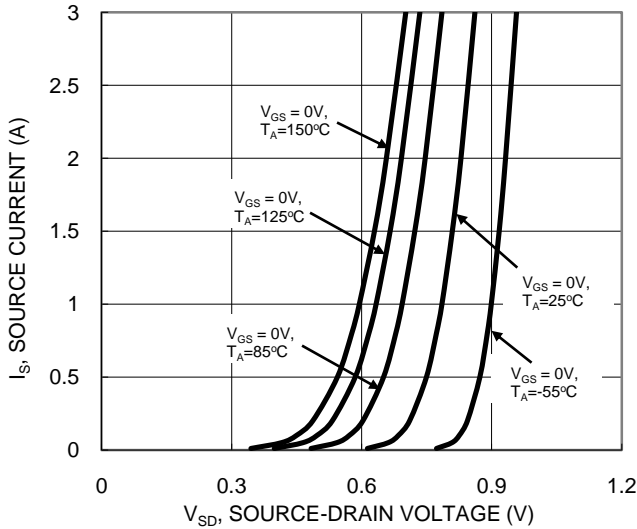


Figure 9. Diode Forward Voltage vs. Current

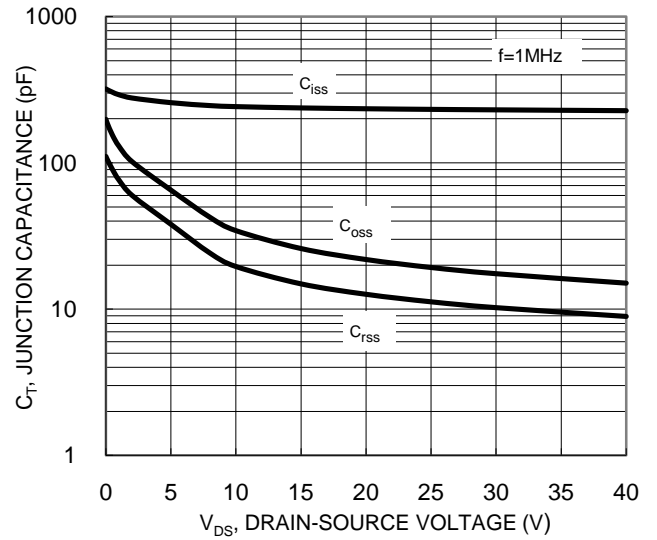


Figure 10. Typical Junction Capacitance

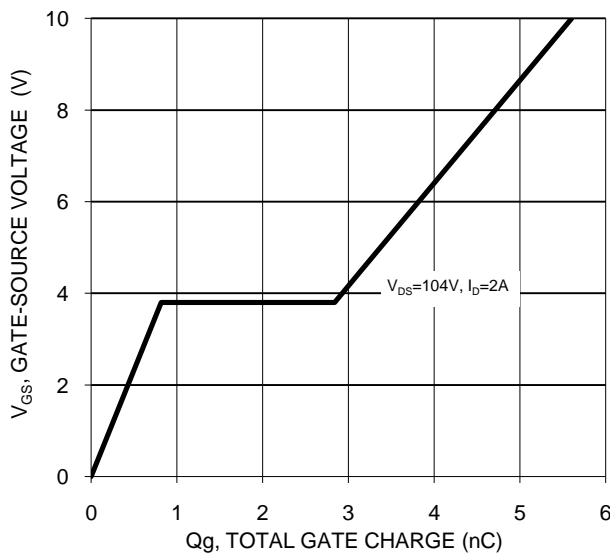


Figure 11. Gate Charge

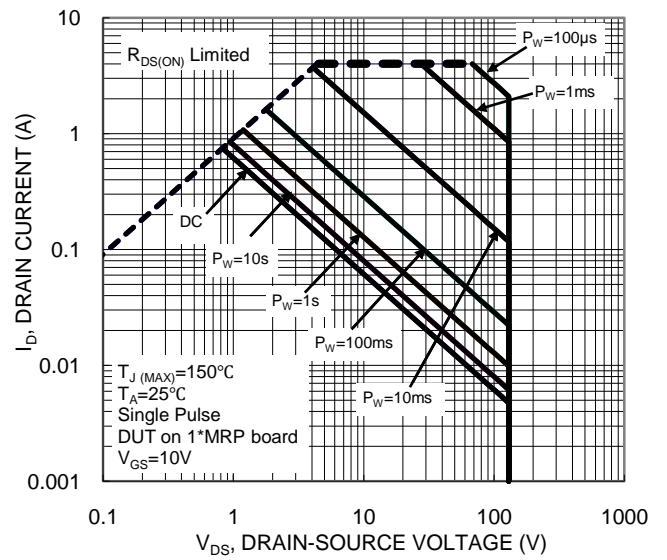


Figure 12. SOA, Safe Operation Area

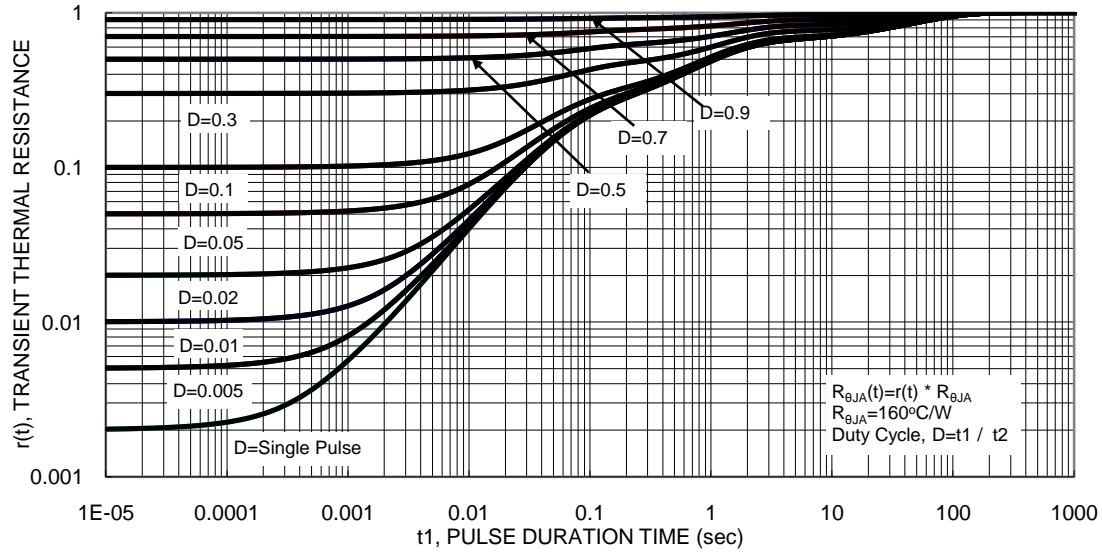
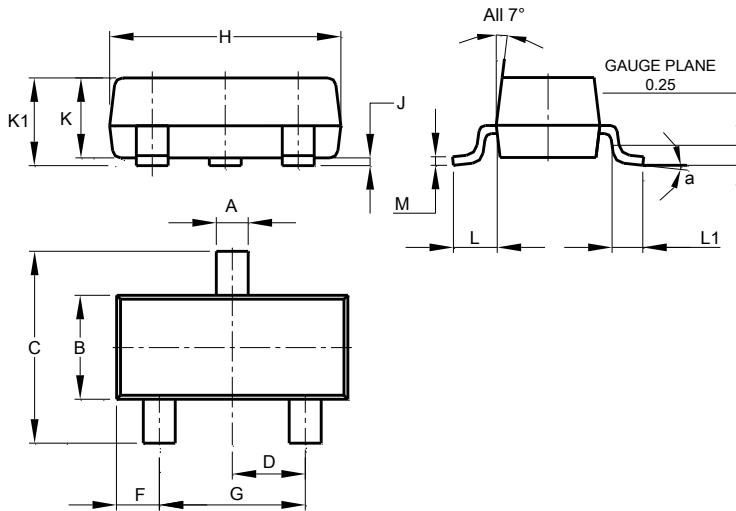


Figure 13. Transient Thermal Resistance

### Package Outline Dimensions

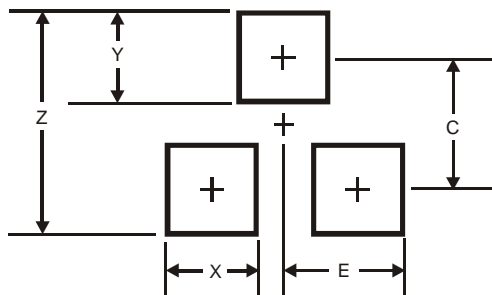
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	8°		
All Dimensions in mm			

### Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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