



**THE DATASHEET OF
DMP3068L-7**



Product Summary

V _{(BR)DSS}	R _{DS(on)} max	Package	I _D max T _A = +25°C
-30V	72mΩ @ V _{GS} = -10V	SOT-23	-3.9A
	85mΩ @ V _{GS} = -4.5V		-3.6A

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative.**

<https://www.diodes.com/quality/product-definitions/>

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

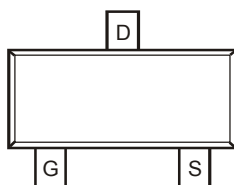
Mechanical Data

- Case: SOT23 (Standard)
- Case Material: Molded Plastic.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead-Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe).
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

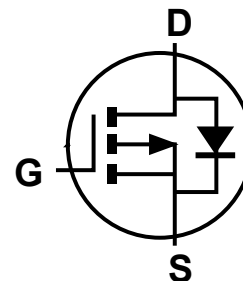


Top View

SOT23



Top View
Pin Configuration



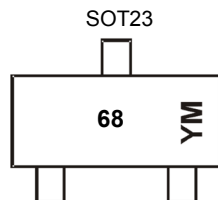
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3068L-7	SOT23 (Standard)	3,000/Tape & Reel
DMP3068L-13	SOT23 (Standard)	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



68 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: B = 2014)
 M or \bar{M} = Month (ex: 9 = September)

Date Code Key

Year	2014	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	B	I	J	K	L	M	N	O	P	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$ unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	± 12	V
Drain Current (Note 6) $V_{GS} = -10\text{V}$	Steady State $T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-3.3 -2.6	A
	$t < 10\text{s}$ $T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-3.9 -3.2	A
Pulsed Drain Current (Pulse width $\leq 10\mu\text{s}$, Duty Cycle $\leq 1\%$)		I_{DM}	-18	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	0.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	182	$^\circ\text{C/W}$
	$t < 10\text{s}$		133	
Total Power Dissipation (Note 6)		P_D	1.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	103	$^\circ\text{C/W}$
	$t < 10\text{s}$		75	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1in. square copper plate.

Electrical Characteristics (@ T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-0.5	—	-1.3	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	57 64 80 107	72 85 120 165	mΩ	V _{GS} = -10V, I _D = -4.2A V _{GS} = -4.5V, I _D = -4.0A V _{GS} = -2.5V, I _D = -2.0A V _{GS} = -1.8V, I _D = -1.0A
Diode Forward Voltage	V _{SD}	—	—	-1.2	V	V _{GS} = 0V, I _S = -1.0A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	708	—	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	57	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	47	—	pF	
Gate Resistance	R _G	—	14	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = -4.5V)	Q _G	—	7.3	—	nC	V _{DS} = -15V, I _D = -4A
Total Gate Charge (V _{GS} = -10V)	Q _G	—	15.9	—	nC	V _{DS} = -15V, I _D = -4A
Gate-Source Charge	Q _{GS}	—	1.2	—		
Gate-Drain Charge	Q _{GD}	—	1.7	—		
Turn-On Delay Time	t _{d(on)}	—	3.5	—	ns	V _{DS} = -15V, V _{GS} = -10V, I _D = -4A, R _G = 6.0Ω
Rise Time	t _r	—	15.8	—		
Turn-Off Delay Time	t _{d(off)}	—	70.3	—		
Fall Time	t _f	—	33.9	—		

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

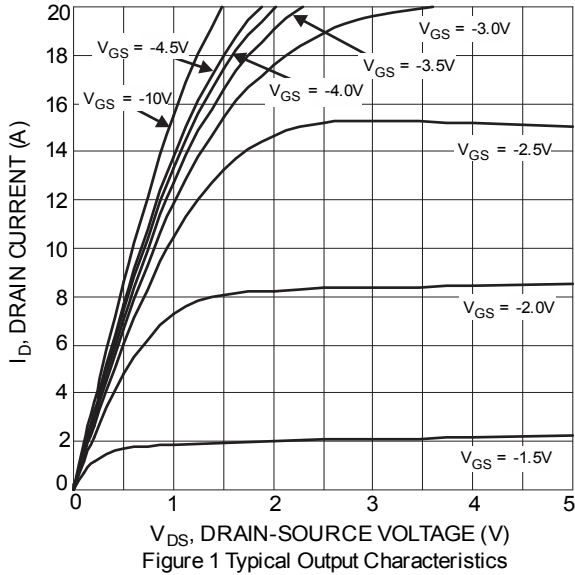


Figure 1 Typical Output Characteristics

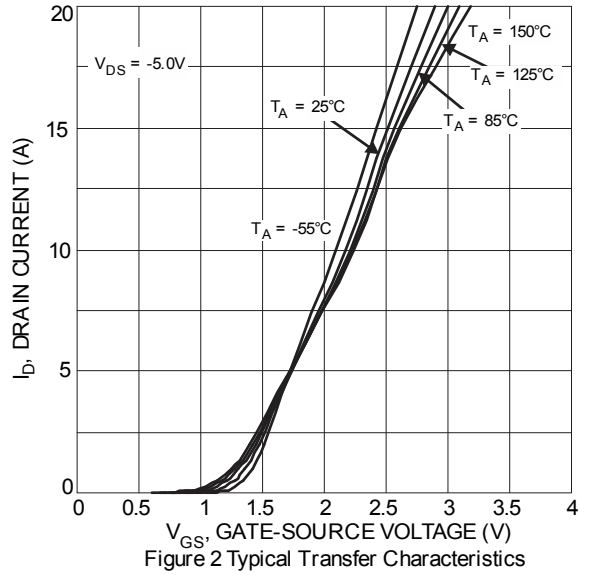


Figure 2 Typical Transfer Characteristics

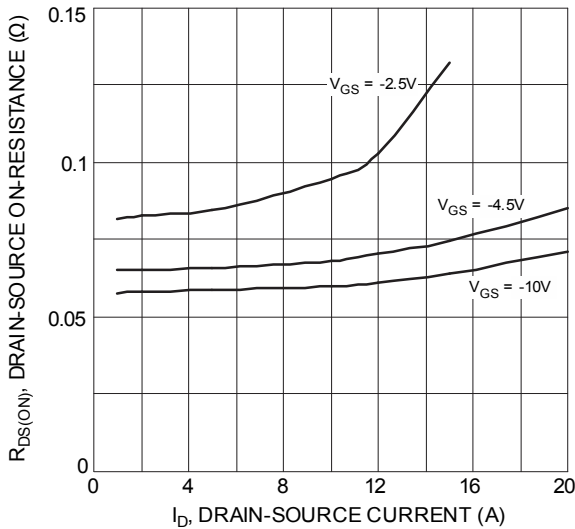


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

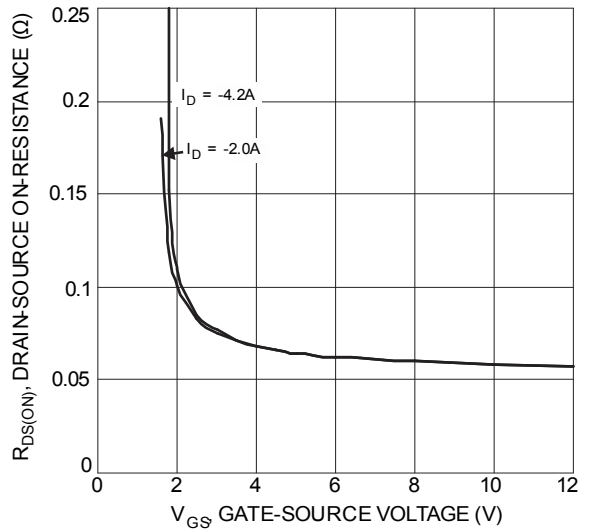


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

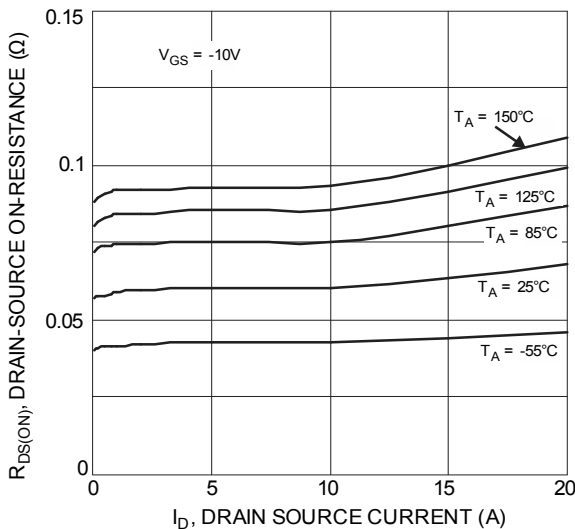


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

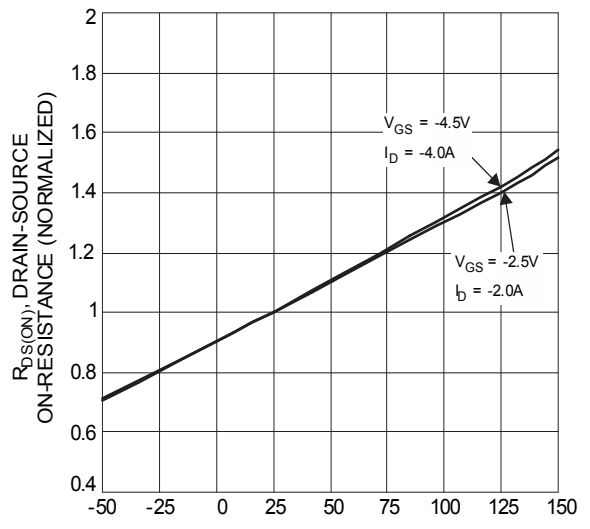


Figure 6 On-Resistance Variation with Temperature

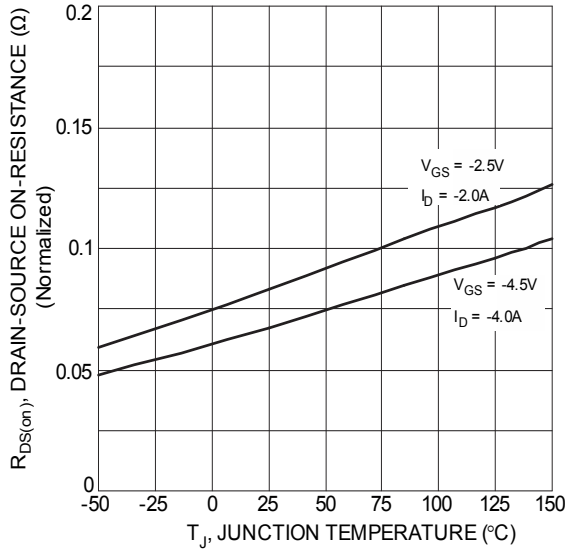


Figure 7 On-Resistance Variation with Temperature

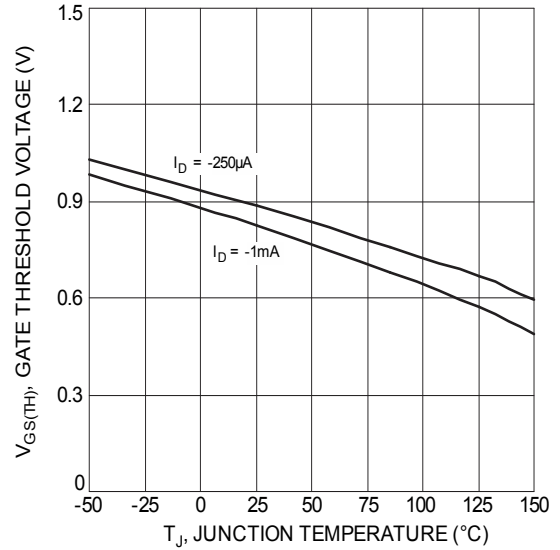


Figure 8 Gate Threshold Variation vs. Ambient Temperature

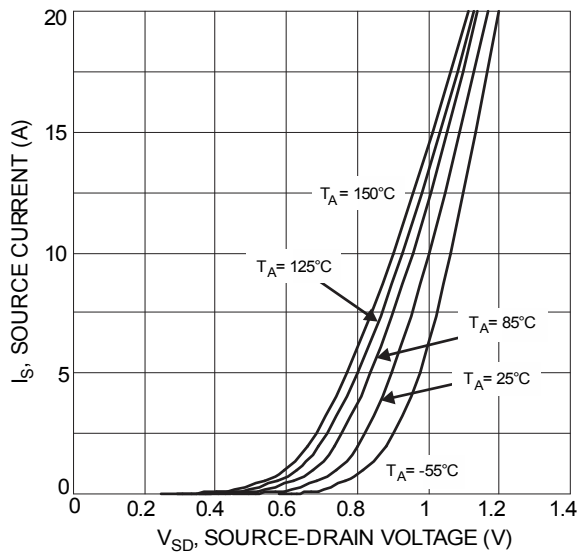


Figure 9 Diode Forward Voltage vs. Current

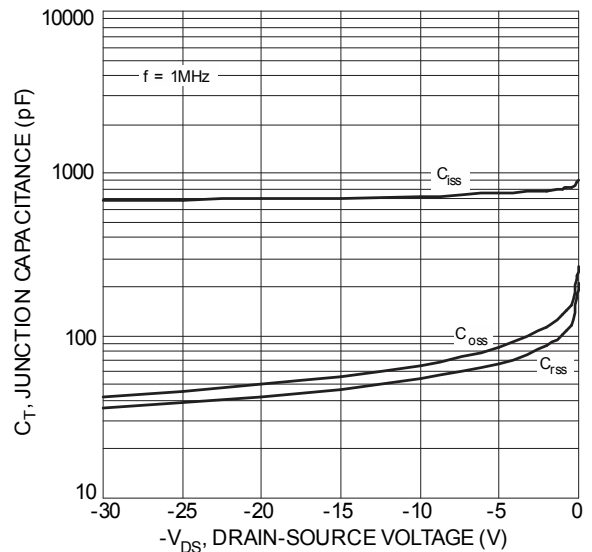


Figure 10 Typical Junction Capacitance

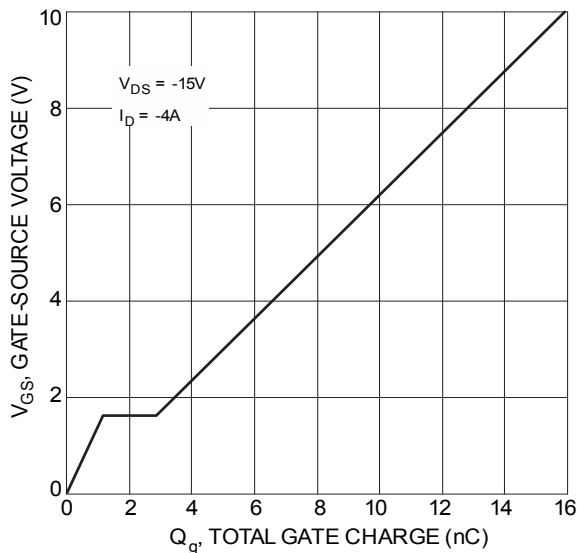


Figure 11 Gate-Charge Characteristics

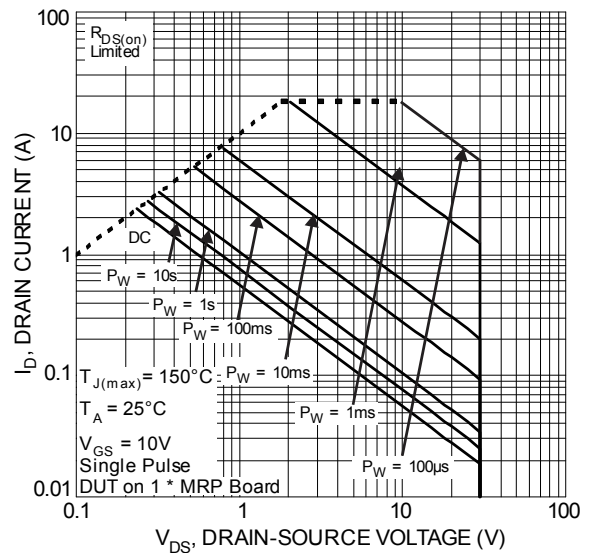


Figure 12 SOA, Safe Operation Area

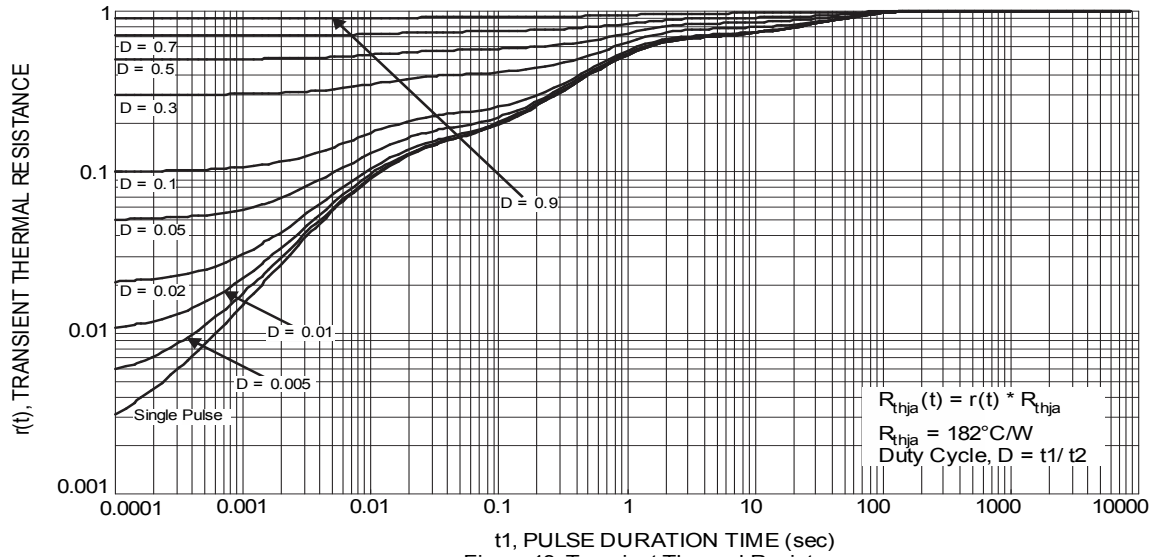
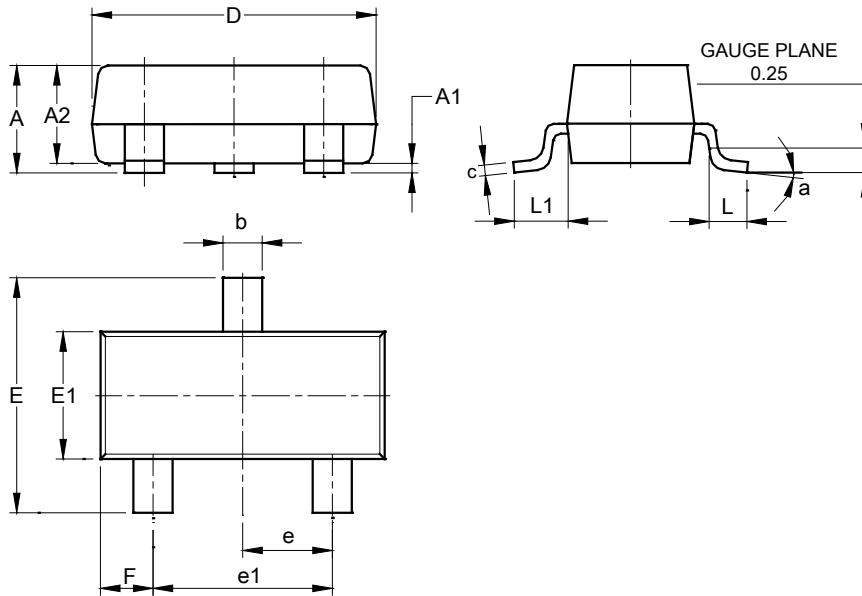


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23 (Standard)

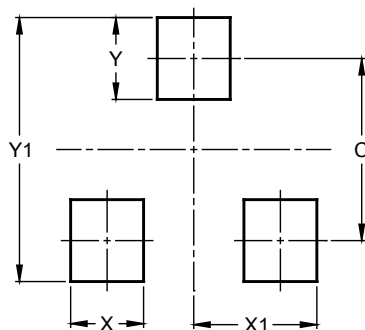


SOT23 (Standard)			
Dim	Min	Max	Typ
A	0.90	1.15	1.025
A1	0.00	0.10	0.05
A2	0.85	1.10	0.975
b	0.30	0.51	0.40
c	0.080	0.202	0.11
D	2.80	3.00	2.90
E	2.25	2.55	2.40
E1	1.20	1.40	1.30
e	0.89	1.03	0.915
e1	1.78	2.05	1.83
F	0.40	0.60	0.535
L1	0.45	0.61	0.55
L	0.25	0.55	0.40
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23 (Standard)



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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