



**THE DATASHEET OF  
SQ3418EEV-T1-GE3**



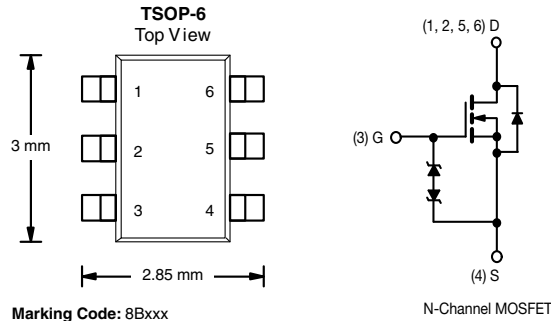
## Automotive N-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
$V_{DS}$ (V)	40
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 10$ V	0.032
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 4.5$ V	0.048
$I_D$ (A)	8
Configuration	Single

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Typical ESD Protection 800 V
- AEC-Q101 Qualified<sup>d</sup>
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

AUTOMOTIVE GRADE


**RoHS**  
 COMPLIANT  
 HALOGEN  
**FREE**


ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and Halogen-free	SQ3418EEV-T1-GE3

ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	$T_C = 25$ °C <sup>a</sup>	8
		$T_C = 125$ °C	5
Continuous Source Current (Diode Conduction)	$I_S$	6	A
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	32	
Single Pulse Avalanche Current	$I_{AS}$	5	
Single Pulse Avalanche Energy	$E_{AS}$	1.2	mJ
Maximum Power Dissipation <sup>b</sup>	$P_D$	$T_C = 25$ °C	
		$T_C = 125$ °C	1.6
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	$R_{thJA}$	110	°C/W
Junction-to-Foot (Drain)	$R_{thJF}$	30	

### Notes

- Package limited.
- Pulse test; pulse width  $\leq 300$   $\mu$ s, duty cycle  $\leq 2$  %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.



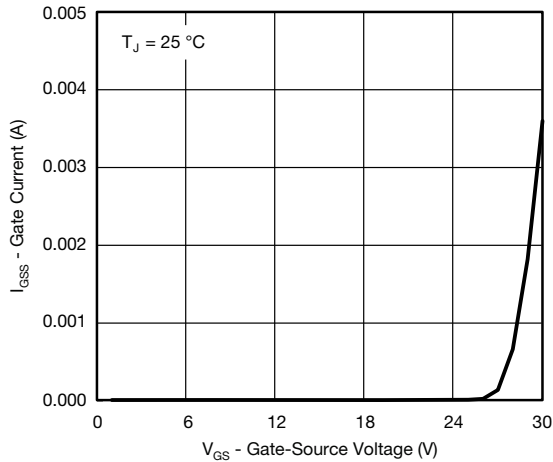
<b>SPECIFICATIONS</b> ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$	40	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.5	2.0	2.5	
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$	-	-	$\pm 500$	nA
		$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	$\pm 1$	mA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{GS} = 0\text{ V}, V_{DS} = 40\text{ V}$	-	-	1	$\mu\text{A}$
		$V_{GS} = 0\text{ V}, V_{DS} = 40\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	50	
		$V_{GS} = 0\text{ V}, V_{DS} = 40\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	150	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{GS} = 10\text{ V}, V_{DS} \geq 5\text{ V}$	10	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 5\text{ A}$	-	0.026	0.032	$\Omega$
		$V_{GS} = 10\text{ V}, I_D = 5\text{ A}, T_J = 125\text{ }^\circ\text{C}$	-	-	0.050	
		$V_{GS} = 10\text{ V}, I_D = 5\text{ A}, T_J = 175\text{ }^\circ\text{C}$	-	-	0.061	
		$V_{GS} = 4.5\text{ V}, I_D = 4\text{ A}$	-	0.040	0.048	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 4\text{ A}$	-	13	-	S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	528	660	$\mu\text{F}$
Output Capacitance	$C_{oss}$		-	112	140	
Reverse Transfer Capacitance	$C_{rss}$		-	76	95	
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{GS} = 4.5\text{ V}, V_{DS} = 20\text{ V}, I_D = 4\text{ A}$	-	7.1	11	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$		-	1.7	-	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	3.7	-	
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	1.2	2.4	3.6	$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = 20\text{ V}, R_L = 4\text{ }\Omega, I_D \cong 5\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$	-	8	12	ns
Rise Time <sup>c</sup>	$t_r$		-	8	12	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$		-	15	23	
Fall Time <sup>c</sup>	$t_f$		-	7	11	
<b>Source-Drain Diode Ratings and Characteristics <math>T_C = 25\text{ }^\circ\text{C}</math><sup>b</sup></b>						
Pulsed Current <sup>a</sup>	$I_{SM}$		-	-	32	A
Forward Voltage	$V_{SD}$	$I_F = 3\text{ A}, V_{GS} = 0$	-	0.8	1.2	V

**Notes**

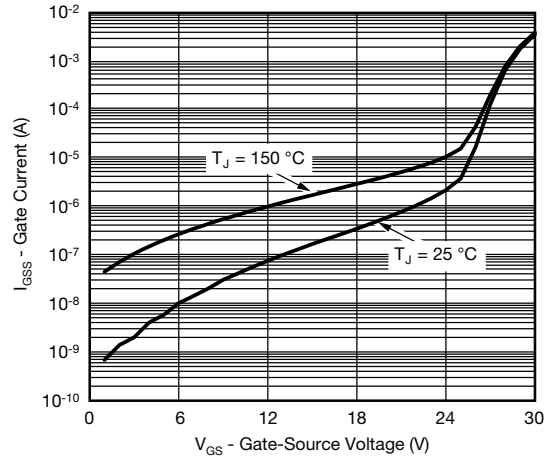
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

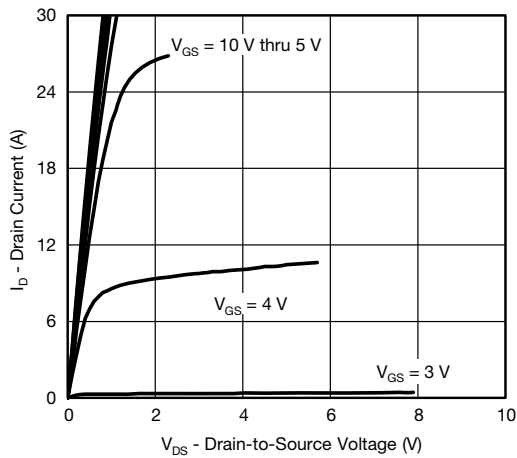
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



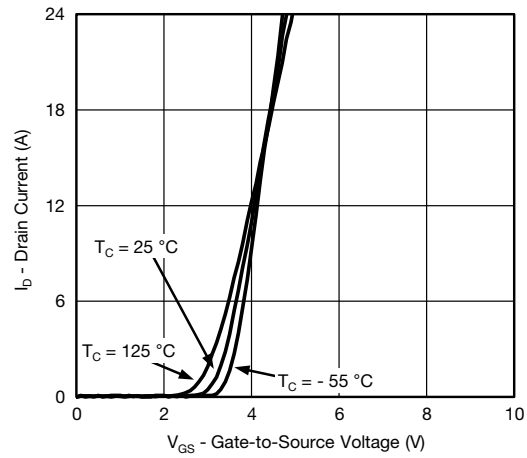
**Gate Current vs. Gate-Source Voltage**



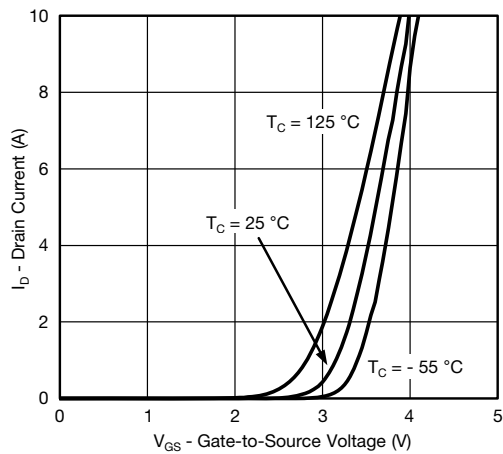
**Gate Current vs. Gate-Source Voltage**



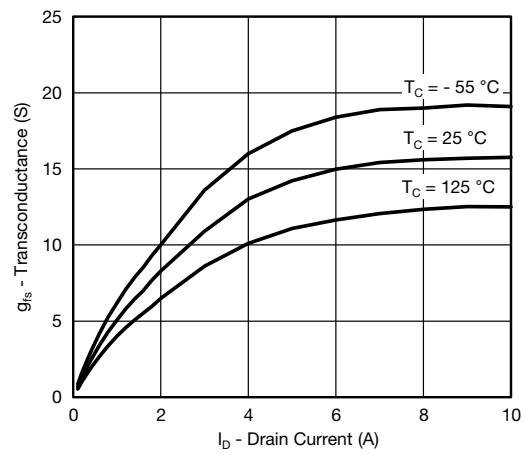
**Output Characteristics**



**Transfer Characteristics**



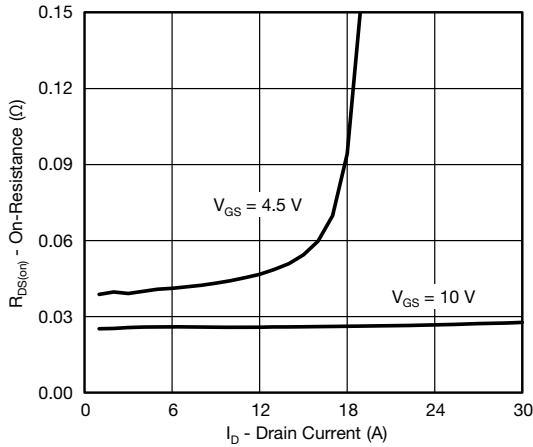
**Transfer Characteristics**



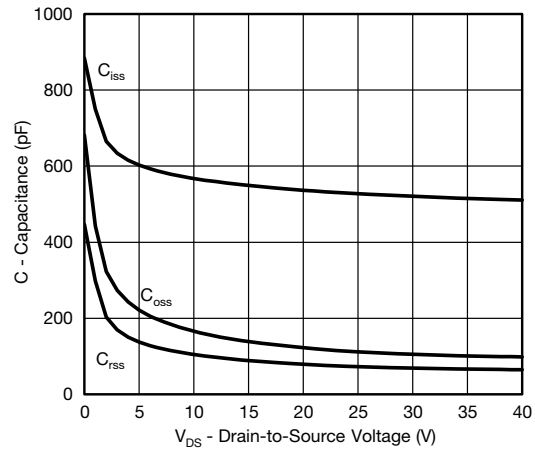
**Transconductance**



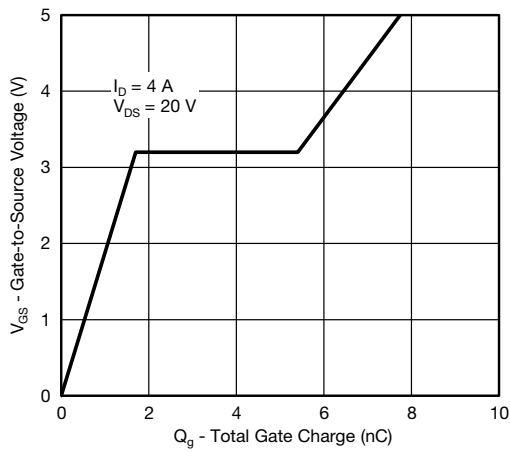
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



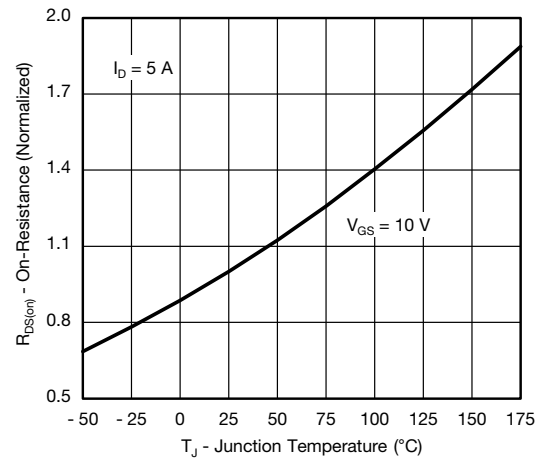
**On-Resistance vs. Drain Current**



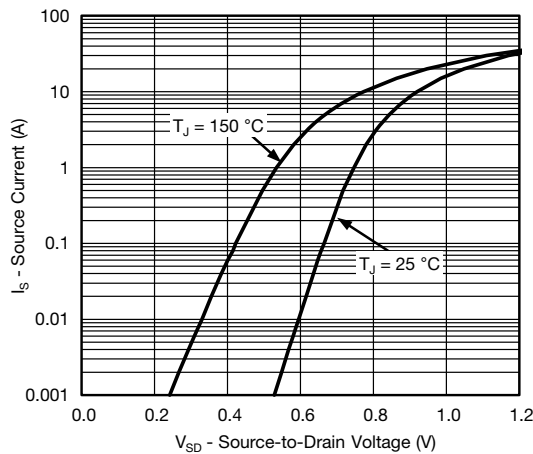
**Capacitance**



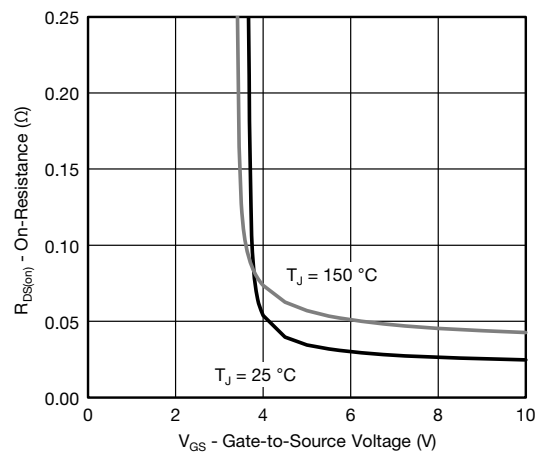
**Gate Charge**



**On-Resistance vs. Junction Temperature**



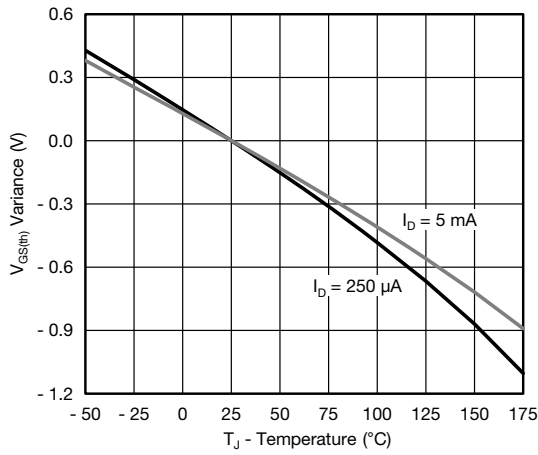
**Source-Drain Diode Forward Voltage**



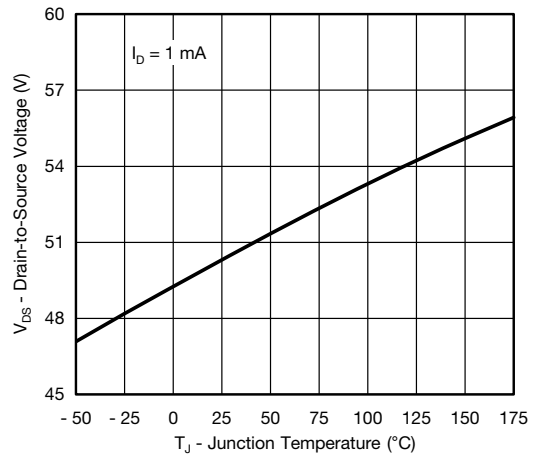
**On-Resistance vs. Gate-Source Voltage**



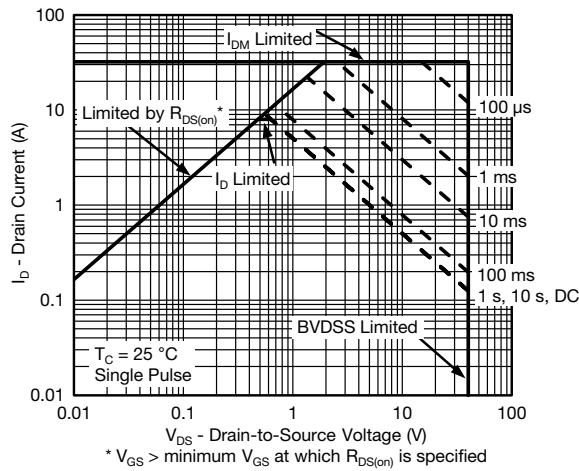
TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Threshold Voltage



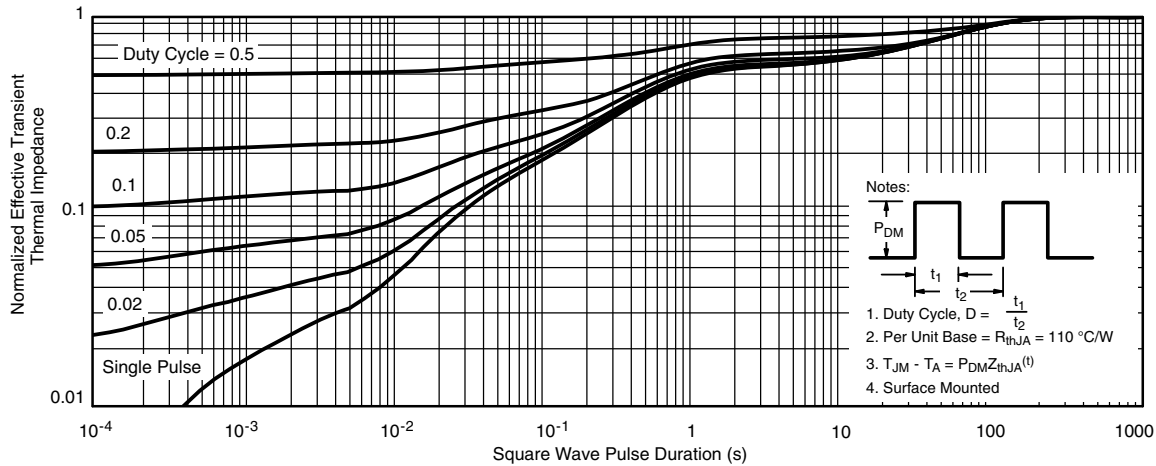
Drain-Source Breakdown vs. Junction Temperature



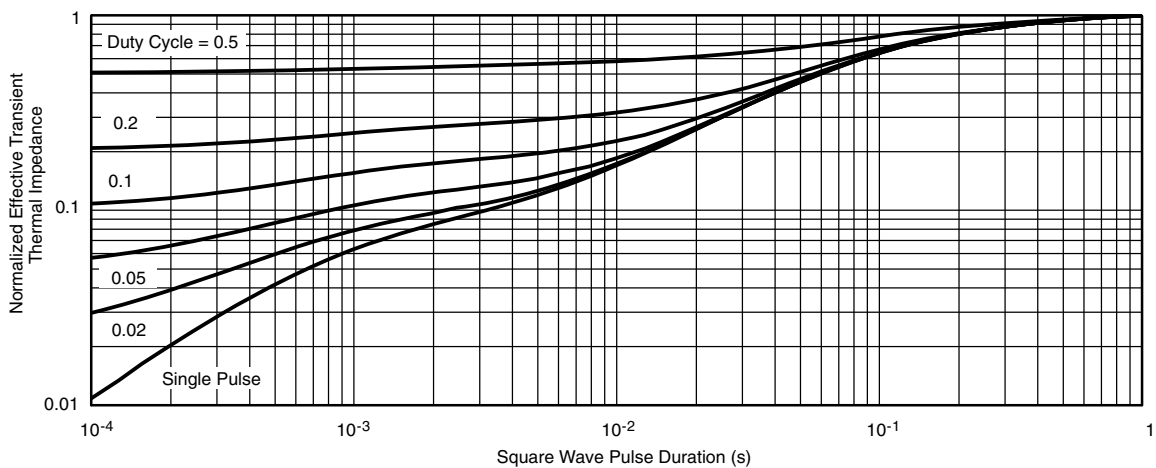
Safe Operating Area



**THERMAL RATINGS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Foot**

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient ( $25\text{ }^\circ\text{C}$ )
  - Normalized Transient Thermal Impedance Junction-to-Foot ( $25\text{ }^\circ\text{C}$ )
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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