



**THE DATASHEET OF
74LVC2G07GM,115**



74LVC2G07

Buffers with open-drain outputs

Rev. 10 — 21 August 2017

Product data sheet

1 General description

The 74LVC2G07 provides two non-inverting buffers.

The output of this device is an open drain and can be connected to other open-drain outputs to implement active-LOW wired-OR or active-HIGH wired-AND functions.

Inputs can be driven from either 3.3 V or 5 V devices. This feature allows the use of this device in a mixed 3.3 V and 5 V environment.

Schmitt trigger action at all inputs makes the circuit tolerant for slower input rise and fall time.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2 Features and benefits

- Wide supply voltage range from 1.65 V to 5.5 V
- 5 V tolerant input/output for interfacing with 5 V logic
- High noise immunity
- Complies with JEDEC standard:
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8-B/JESD36 (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F exceeds 2 000 V
 - MM JESD22-A115-A exceeds 200 V
- -24 mA output drive ($V_{CC} = 3.0$ V)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3 Ordering information

Table 1. Ordering information

| Type number | Package | | | Version |
|-------------|-------------------|--------|---|---------|
| | Temperature range | Name | Description | |
| 74LVC2G07GW | -40 °C to +125 °C | SC-88 | plastic surface-mounted package; 6 leads | SOT363 |
| 74LVC2G07GV | -40 °C to +125 °C | TSOP6 | plastic surface-mounted package (TSOP6); 6 leads | SOT457 |
| 74LVC2G07GM | -40 °C to +125 °C | XSON6 | plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm | SOT886 |
| 74LVC2G07GF | -40 °C to +125 °C | XSON6 | plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm | SOT891 |
| 74LVC2G07GN | -40 °C to +125 °C | XSON6 | extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm | SOT1115 |
| 74LVC2G07GS | -40 °C to +125 °C | XSON6 | extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm | SOT1202 |
| 74LVC2G07GX | -40 °C to +125 °C | X2SON6 | plastic thermal extremely thin small outline package; no leads; 6 terminals; body 1 x 0.8 x 0.35 mm | SOT1255 |

4 Marking

Table 2. Marking

| Type number | Marking code ^[1] |
|-------------|-----------------------------|
| 74LVC2G07GW | V7 |
| 74LVC2G07GV | V07 |
| 74LVC2G07GM | V7 |
| 74LVC2G07GF | V7 |
| 74LVC2G07GN | V7 |
| 74LVC2G07GS | V7 |
| 74LVC2G07GX | V7 |

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5 Functional diagram

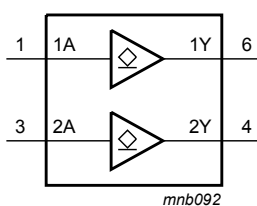


Figure 1. Logic symbol

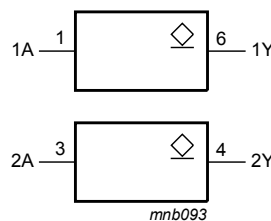


Figure 2. IEC logic symbol

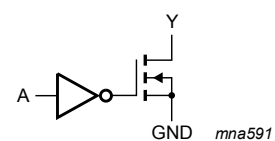


Figure 3. Logic diagram (one driver)

6 Pinning information

6.1 Pinning

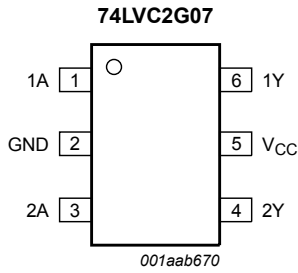


Figure 4. Pin configuration SOT363 and SOT457

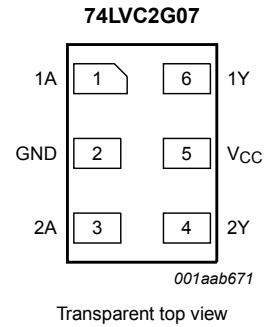


Figure 5. Pin configuration SOT886

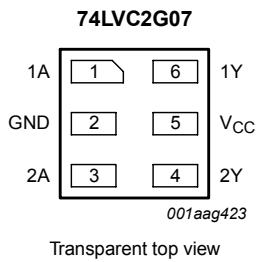


Figure 6. Pin configuration SOT891, SOT1115 and SOT1202

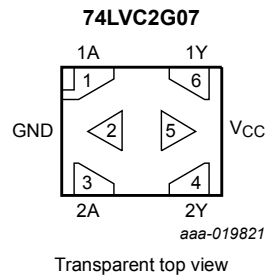


Figure 7. Pin configuration SOT1255 (X2SON6)

6.2 Pin description

Table 3. Pin description

| Symbol | Pin | Description |
|-----------------|-----|----------------|
| 1A | 1 | data input |
| GND | 2 | ground (0 V) |
| 2A | 3 | data input |
| 2Y | 4 | data output |
| V _{CC} | 5 | supply voltage |
| 1Y | 6 | data output |

7 Functional description

Table 4. Function table ^[1]

| Input nA | Output nY |
|----------|-----------|
| L | L |
| H | Z |

[1] H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.

8 Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|-------------------------|--|------|------|------|
| V _{CC} | supply voltage | | -0.5 | +6.5 | V |
| I _{IK} | input clamping current | V _I < 0 V | -50 | - | mA |
| V _I | input voltage | [1] | -0.5 | +6.5 | V |
| I _{OK} | output clamping current | V _O < 0 V | -50 | - | mA |
| V _O | output voltage | Active mode [1] | -0.5 | +6.5 | V |
| | | Power-down mode [1] [2] | -0.5 | +6.5 | V |
| I _O | output current | V _O = 0 V to 6.5 V | - | 50 | mA |
| I _{CC} | supply current | | - | 100 | mA |
| I _{GND} | ground current | | -100 | - | mA |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| P _{tot} | total power dissipation | T _{amb} = -40 °C to +125 °C [3] | - | 250 | mW |

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When V_{CC} = 0 V (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For SC-88 and SC-74 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.

For X2SON6 and XSON6 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9 Recommended operating conditions

Table 6. Recommended operating conditions

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|------------------|-------------------------------------|--|------|-----|------|------|
| V _{CC} | supply voltage | | 1.65 | - | 5.5 | V |
| V _I | input voltage | | 0 | - | 5.5 | V |
| V _O | output voltage | Active mode | 0 | - | 5.5 | V |
| | | Power-down mode; V _{CC} = 0 V | 0 | - | 5.5 | V |
| T _{amb} | ambient temperature | | -40 | - | +125 | °C |
| Δt/ΔV | input transition rise and fall rate | V _{CC} = 1.65 V to 2.7 V | - | - | 20 | ns/V |
| | | V _{CC} = 2.7 V to 5.5 V | - | - | 10 | ns/V |

10 Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--|---------------------------|--|----------------------|-----------|----------------------|---------------|
| $T_{amb} = -40\text{ °C to }+85\text{ °C}^{[1]}$ | | | | | | |
| V_{IH} | HIGH-level input voltage | $V_{CC} = 1.65\text{ V to }1.95\text{ V}$ | $0.65 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 2.3\text{ V to }2.7\text{ V}$ | 1.7 | - | - | V |
| | | $V_{CC} = 2.7\text{ V to }3.6\text{ V}$ | 2.0 | - | - | V |
| | | $V_{CC} = 4.5\text{ V to }5.5\text{ V}$ | $0.7 \times V_{CC}$ | - | - | V |
| V_{IL} | LOW-level input voltage | $V_{CC} = 1.65\text{ V to }1.95\text{ V}$ | - | - | $0.35 \times V_{CC}$ | V |
| | | $V_{CC} = 2.3\text{ V to }2.7\text{ V}$ | - | - | 0.7 | V |
| | | $V_{CC} = 2.7\text{ V to }3.6\text{ V}$ | - | - | 0.8 | V |
| | | $V_{CC} = 4.5\text{ V to }5.5\text{ V}$ | - | - | $0.3 \times V_{CC}$ | V |
| V_{OL} | LOW-level output voltage | $V_I = V_{IH}$ or V_{IL} | | | | |
| | | $I_O = 100\text{ }\mu\text{A}; V_{CC} = 1.65\text{ V to }5.5\text{ V}$ | - | - | 0.10 | V |
| | | $I_O = 4\text{ mA}; V_{CC} = 1.65\text{ V}$ | - | - | 0.45 | V |
| | | $I_O = 8\text{ mA}; V_{CC} = 2.3\text{ V}$ | - | - | 0.30 | V |
| | | $I_O = 12\text{ mA}; V_{CC} = 2.7\text{ V}$ | - | - | 0.40 | V |
| | | $I_O = 24\text{ mA}; V_{CC} = 3.0\text{ V}$ | - | - | 0.55 | V |
| | | $I_O = 32\text{ mA}; V_{CC} = 4.5\text{ V}$ | - | - | 0.55 | V |
| I_I | input leakage current | $V_I = 5.5\text{ V or GND}; V_{CC} = 0\text{ V to }5.5\text{ V}^{[2]}$ | - | ± 0.1 | ± 1 | μA |
| I_{OZ} | OFF-state output current | $V_I = V_{IH}$ or $V_{IL}; V_O = V_{CC}$ or GND; $V_{CC} = 5.5\text{ V}$ | - | ± 0.1 | ± 2 | μA |
| I_{OFF} | power-off leakage current | V_I or $V_O = 5.5\text{ V}; V_{CC} = 0\text{ V}$ | - | ± 0.1 | ± 2 | μA |
| I_{CC} | supply current | $V_I = 5.5\text{ V or GND}; I_O = 0\text{ A};$ $V_{CC} = 1.65\text{ V to }5.5\text{ V}$ | - | 0.1 | 4 | μA |
| ΔI_{CC} | additional supply current | per pin; $V_I = V_{CC} - 0.6\text{ V}; I_O = 0\text{ A};$ $V_{CC} = 2.3\text{ V to }5.5\text{ V}^{[2]}$ | - | 5 | 500 | μA |
| C_I | input capacitance | | - | 2.5 | - | pF |

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|---------------------------|---|----------------------|-----|----------------------|---------------|
| $T_{amb} = -40\text{ °C to }+125\text{ °C}$ | | | | | | |
| V_{IH} | HIGH-level input voltage | $V_{CC} = 1.65\text{ V to }1.95\text{ V}$ | $0.65 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 2.3\text{ V to }2.7\text{ V}$ | 1.7 | - | - | V |
| | | $V_{CC} = 2.7\text{ V to }3.6\text{ V}$ | 2.0 | - | - | V |
| | | $V_{CC} = 4.5\text{ V to }5.5\text{ V}$ | $0.7 \times V_{CC}$ | - | - | V |
| V_{IL} | LOW-level input voltage | $V_{CC} = 1.65\text{ V to }1.95\text{ V}$ | - | - | $0.35 \times V_{CC}$ | V |
| | | $V_{CC} = 2.3\text{ V to }2.7\text{ V}$ | - | - | 0.7 | V |
| | | $V_{CC} = 2.7\text{ V to }3.6\text{ V}$ | - | - | 0.8 | V |
| | | $V_{CC} = 4.5\text{ V to }5.5\text{ V}$ | - | - | $0.3 \times V_{CC}$ | V |
| V_{OL} | LOW-level output voltage | $V_I = V_{IH}$ or V_{IL} | | | | |
| | | $I_O = 100\text{ }\mu\text{A}$; $V_{CC} = 1.65\text{ V to }5.5\text{ V}$ | - | - | 0.10 | V |
| | | $I_O = 4\text{ mA}$; $V_{CC} = 1.65\text{ V}$ | - | - | 0.70 | V |
| | | $I_O = 8\text{ mA}$; $V_{CC} = 2.3\text{ V}$ | - | - | 0.45 | V |
| | | $I_O = 12\text{ mA}$; $V_{CC} = 2.7\text{ V}$ | - | - | 0.60 | V |
| | | $I_O = 24\text{ mA}$; $V_{CC} = 3.0\text{ V}$ | - | - | 0.80 | V |
| | | $I_O = 32\text{ mA}$; $V_{CC} = 4.5\text{ V}$ | - | - | 0.80 | V |
| I_I | input leakage current | $V_I = 5.5\text{ V or GND}$; $V_{CC} = 0\text{ V to }5.5\text{ V}$ | - | - | ± 1 | μA |
| I_{OZ} | OFF-state output current | $V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND ; $V_{CC} = 5.5\text{ V}$ | - | - | ± 2 | μA |
| I_{OFF} | power-off leakage current | V_I or $V_O = 5.5\text{ V}$; $V_{CC} = 0\text{ V}$ | - | - | ± 2 | μA |
| I_{CC} | supply current | $V_I = 5.5\text{ V or GND}$; $I_O = 0\text{ A}$; $V_{CC} = 1.65\text{ V to }5.5\text{ V}$ | - | - | 4 | μA |
| ΔI_{CC} | additional supply current | per pin; $V_I = V_{CC} - 0.6\text{ V}$; $I_O = 0\text{ A}$; $V_{CC} = 2.3\text{ V to }5.5\text{ V}$ | - | - | 500 | μA |

[1] All typical values are measured at $T_{amb} = 25\text{ °C}$.

[2] These typical values are measured at $V_{CC} = 3.3\text{ V}$.

11 Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see [Figure 9](#).

| Symbol | Parameter | Conditions | -40 °C to +85 °C | | | -40 °C to +125 °C | | Unit |
|-----------------|-------------------------------|--|------------------|--------------------|-----|-------------------|-----|------|
| | | | Min | Typ ^[1] | Max | Min | Max | |
| t _{pd} | propagation delay | nA to nY; see Figure 8 ^[2] | | | | | | |
| | | V _{CC} = 1.65 V to 1.95 V | 1.0 | 3.5 | 6.7 | 1.0 | 8.4 | ns |
| | | V _{CC} = 2.3 V to 2.7 V | 0.5 | 2.4 | 4.3 | 0.5 | 5.5 | ns |
| | | V _{CC} = 2.7 V | 1.0 | 2.3 | 4.2 | 1.0 | 5.3 | ns |
| | | V _{CC} = 3.0 V to 3.6 V | 0.5 | 2.6 | 3.7 | 0.5 | 4.7 | ns |
| | | V _{CC} = 4.5 V to 5.5 V | 0.5 | 1.5 | 2.9 | 0.5 | 3.7 | ns |
| C _{PD} | power dissipation capacitance | V _I = GND to V _{CC} ; V _{CC} = 3.3 V ^[3] | - | 6.5 | - | - | - | pF |

[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

[2] t_{pd} is the same as t_{PLZ} and t_{PZL}.

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o)$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

∑(C_L × V_{CC}² × f_o) = sum of outputs.

11.1 Waveform and test circuit

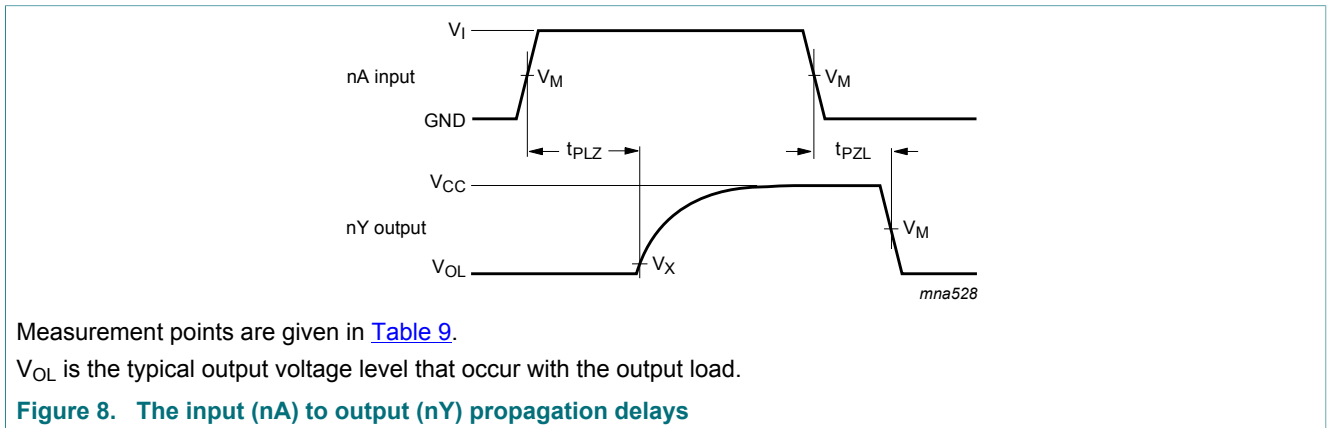
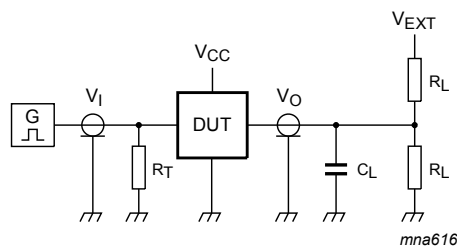


Table 9. Measurement points

| Supply voltage | Input | Output | |
|------------------|---------------------|---------------------|---------------------------|
| V_{CC} | V_M | V_M | V_X |
| 1.65 V to 1.95 V | $0.5 \times V_{CC}$ | $0.5 \times V_{CC}$ | $V_{OL} + 0.15 \text{ V}$ |
| 2.3 V to 2.7 V | $0.5 \times V_{CC}$ | $0.5 \times V_{CC}$ | $V_{OL} + 0.15 \text{ V}$ |
| 2.7 V | 1.5 V | 1.5 V | $V_{OL} + 0.3 \text{ V}$ |
| 3.0 V to 3.6 V | 1.5 V | 1.5 V | $V_{OL} + 0.3 \text{ V}$ |
| 4.5 V to 5.5 V | $0.5 \times V_{CC}$ | $0.5 \times V_{CC}$ | $V_{OL} + 0.3 \text{ V}$ |



Test data is given in [Table 10](#).

Definitions for test circuit:

R_L = Load resistance.

C_L = Load capacitance including jig and probe capacitance.

R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

V_{EXT} = External voltage for measuring switching times.

Figure 9. Test circuit for measuring switching times

Table 10. Test data

| Supply voltage | Input | | Load | | V_{EXT} |
|------------------|----------|-----------------------|-------|--------------|--------------------|
| V_{CC} | V_I | t_r, t_f | C_L | R_L | t_{pZL}, t_{pLZ} |
| 1.65 V to 1.95 V | V_{CC} | $\leq 2.0 \text{ ns}$ | 30 pF | 1 k Ω | $2 \times V_{CC}$ |
| 2.3 V to 2.7 V | V_{CC} | $\leq 2.0 \text{ ns}$ | 30 pF | 500 Ω | $2 \times V_{CC}$ |
| 2.7 V | 2.7 V | $\leq 2.5 \text{ ns}$ | 50 pF | 500 Ω | 6 V |
| 3.0 V to 3.6 V | 2.7 V | $\leq 2.5 \text{ ns}$ | 50 pF | 500 Ω | 6 V |
| 4.5 V to 5.5 V | V_{CC} | $\leq 2.5 \text{ ns}$ | 50 pF | 500 Ω | $2 \times V_{CC}$ |

12 Package outline

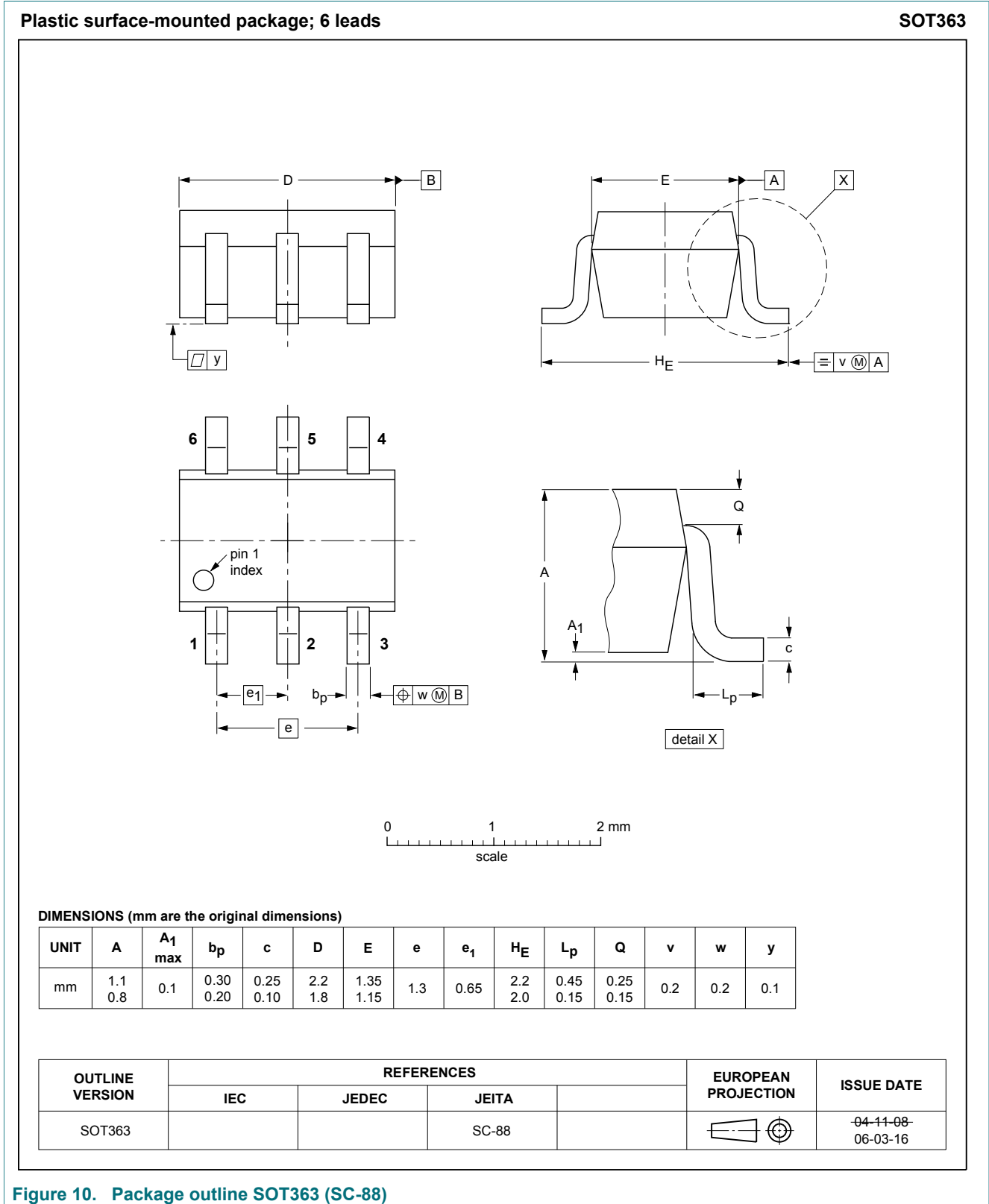
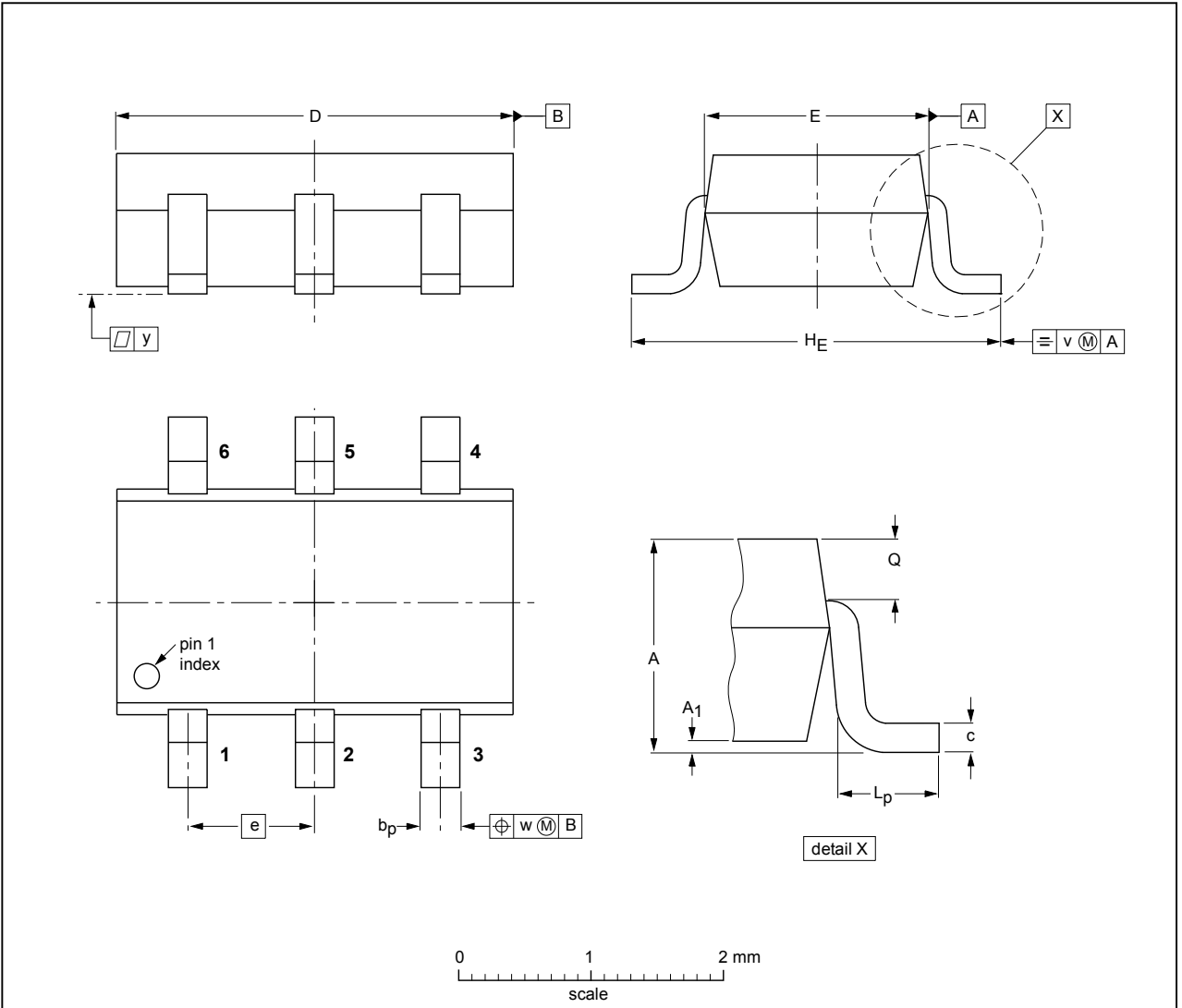


Figure 10. Package outline SOT363 (SC-88)

Plastic surface-mounted package (TSOP6); 6 leads

SOT457



DIMENSIONS (mm are the original dimensions)

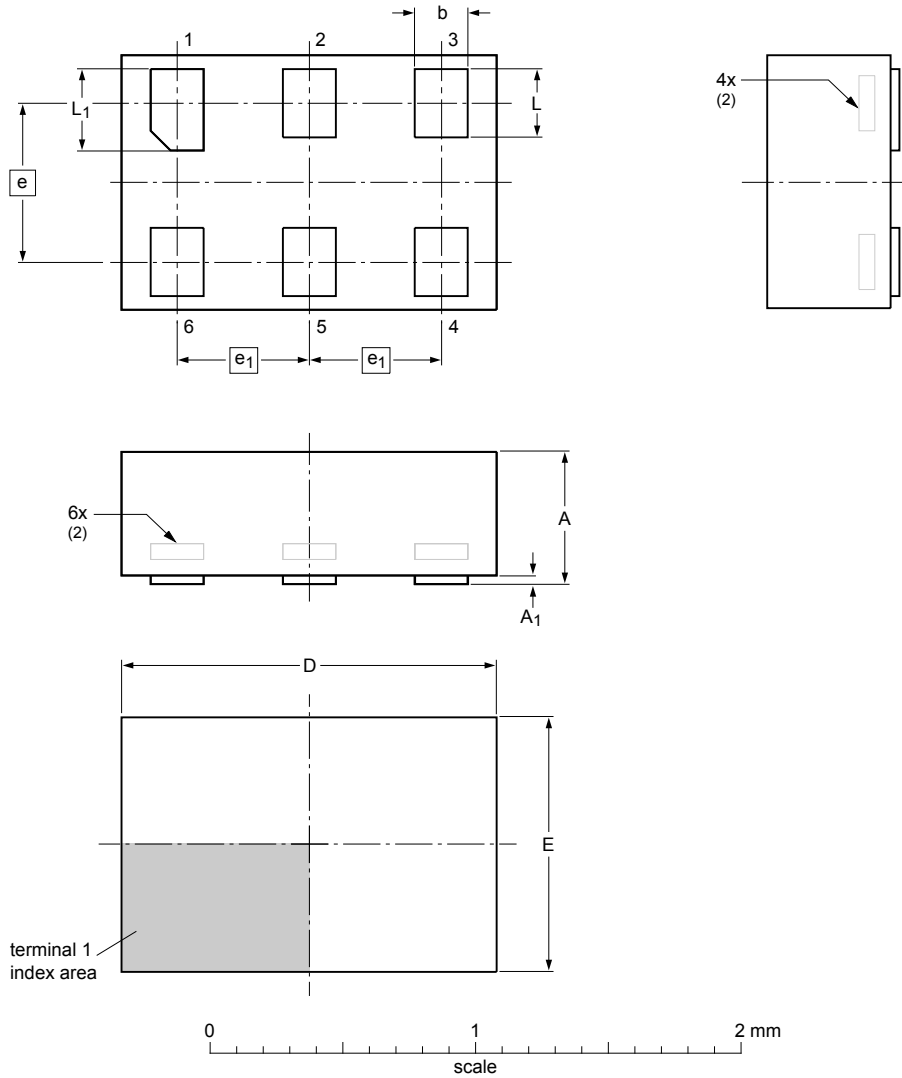
| UNIT | A | A ₁ | b _p | c | D | E | e | H _E | L _p | Q | v | w | y |
|------|------------|----------------|----------------|--------------|------------|------------|------|----------------|----------------|--------------|-----|-----|-----|
| mm | 1.1 0.9 | 0.1 0.013 | 0.40 0.25 | 0.26 0.10 | 3.1 2.7 | 1.7 1.3 | 0.95 | 3.0 2.5 | 0.6 0.2 | 0.33 0.23 | 0.2 | 0.2 | 0.1 |

| OUTLINE VERSION | REFERENCES | | | EUROPEAN PROJECTION | ISSUE DATE |
|-----------------|------------|-------|-------|---------------------|------------------------|
| | IEC | JEDEC | JEITA | | |
| SOT457 | | | SC-74 | | -05-11-07- 06-03-16 |

Figure 11. Package outline SOT457 (TSOP6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886



Dimensions (mm are the original dimensions)

| Unit | A ⁽¹⁾ | A ₁ | b | D | E | e | e ₁ | L | L ₁ |
|------|------------------|----------------|------|------|------|-----|----------------|------|----------------|
| max | 0.5 | 0.04 | 0.25 | 1.50 | 1.05 | | | 0.35 | 0.40 |
| nom | | | 0.20 | 1.45 | 1.00 | 0.6 | 0.5 | 0.30 | 0.35 |
| min | | | 0.17 | 1.40 | 0.95 | | | 0.27 | 0.32 |

Notes

- Including plating thickness.
- Can be visible in some manufacturing processes.

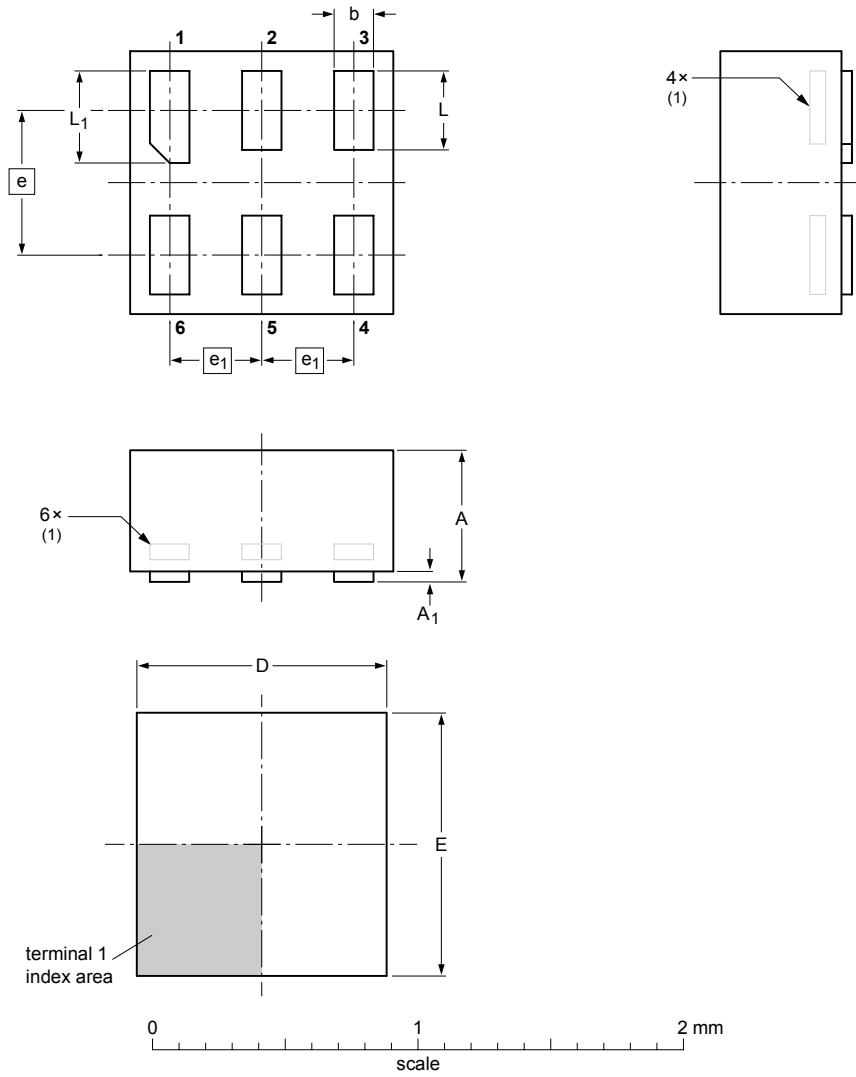
sot886_po

| Outline version | References | | | European projection | Issue date |
|-----------------|------------|--------|-------|---------------------|----------------------|
| | IEC | JEDEC | JEITA | | |
| SOT886 | | MO-252 | | | 04-07-22 12-01-05 |

Figure 12. Package outline SOT886 (XSON6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm

SOT891



DIMENSIONS (mm are the original dimensions)

| UNIT | A max | A ₁ max | b | D | E | e | e ₁ | L | L ₁ |
|------|----------|-----------------------|--------------|--------------|--------------|------|----------------|--------------|----------------|
| mm | 0.5 | 0.04 | 0.20 0.12 | 1.05 0.95 | 1.05 0.95 | 0.55 | 0.35 | 0.35 0.27 | 0.40 0.32 |

Note

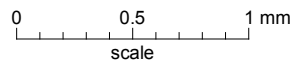
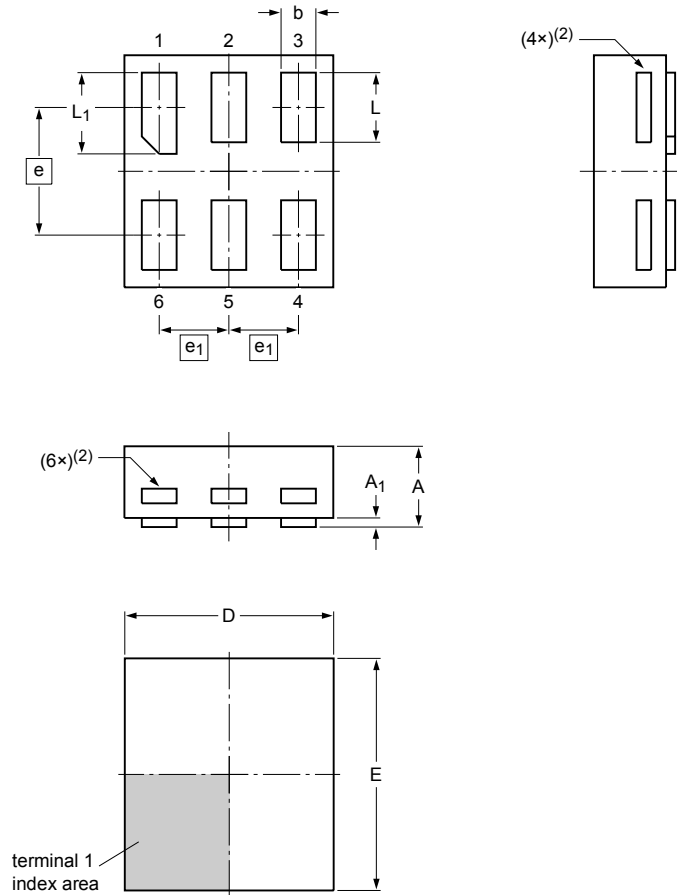
1. Can be visible in some manufacturing processes.

| OUTLINE VERSION | REFERENCES | | | EUROPEAN PROJECTION | ISSUE DATE |
|--------------------|------------|-------|-------|------------------------|-----------------------|
| | IEC | JEDEC | JEITA | | |
| SOT891 | | | | | -05-04-06 07-05-15 |

Figure 13. Package outline SOT891 (XSON6)

XSON6: extremely thin small outline package; no leads;
6 terminals; body 0.9 x 1.0 x 0.35 mm

SOT1115



Dimensions

| Unit | A ⁽¹⁾ | A ₁ | b | D | E | e | e ₁ | L | L ₁ |
|------|------------------|----------------|------|------|------|------|----------------|------|----------------|
| mm | max 0.35 | 0.04 | 0.20 | 0.95 | 1.05 | | | 0.35 | 0.40 |
| | nom 0.15 | 0.90 | 1.00 | 0.55 | 0.3 | 0.30 | 0.35 | | |
| | min 0.12 | 0.85 | 0.95 | | | 0.27 | 0.32 | | |

Note

- Including plating thickness.
- Visible depending upon used manufacturing technology.

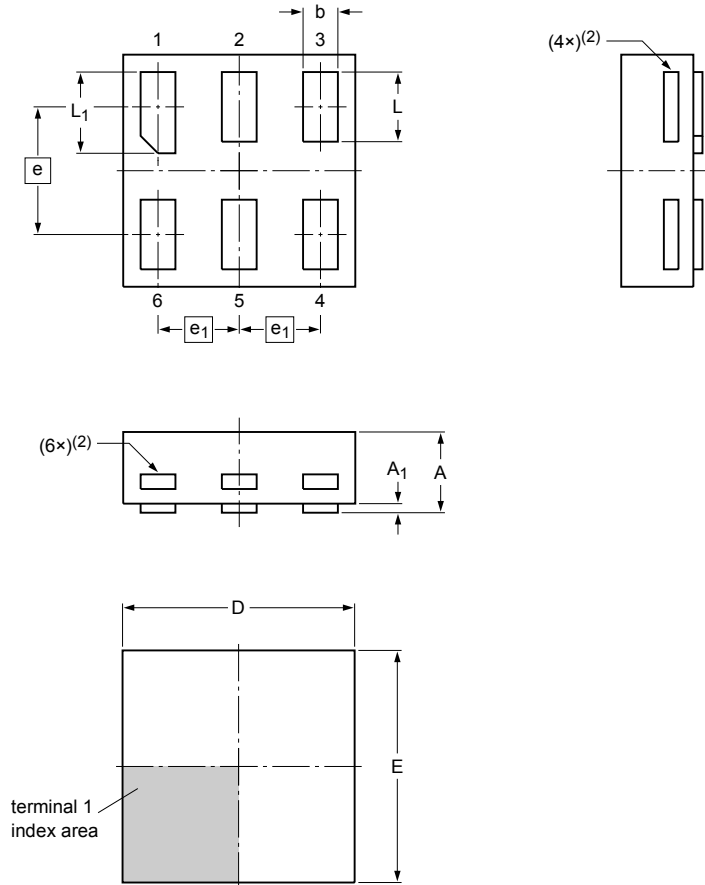
sot1115_po

| Outline version | References | | | | European projection | Issue date |
|-----------------|------------|-------|-------|--|---------------------|------------------------|
| | IEC | JEDEC | JEITA | | | |
| SOT1115 | | | | | | -10-04-02- 10-04-07 |

Figure 14. Package outline SOT1115 (XSON6)

**XSON6: extremely thin small outline package; no leads;
6 terminals; body 1.0 x 1.0 x 0.35 mm**

SOT1202



Dimensions

| Unit | A ⁽¹⁾ | A ₁ | b | D | E | e | e ₁ | L | L ₁ |
|------|------------------|----------------|------|------|------|------|----------------|------|----------------|
| max | 0.35 | 0.04 | 0.20 | 1.05 | 1.05 | | | 0.35 | 0.40 |
| nom | | | 0.15 | 1.00 | 1.00 | 0.55 | 0.35 | 0.30 | 0.35 |
| min | | | 0.12 | 0.95 | 0.95 | | | 0.27 | 0.32 |

Note

- Including plating thickness.
- Visible depending upon used manufacturing technology.

sot1202_po

| Outline version | References | | | | European projection | Issue date |
|-----------------|------------|-------|-------|--|---------------------|------------------------|
| | IEC | JEDEC | JEITA | | | |
| SOT1202 | | | | | | -10-04-02- 10-04-06 |

Figure 15. Package outline SOT1202 (XSON6)

X2SON6: plastic thermal enhanced extremely thin small outline package; no leads;
6 terminals; body 1.0 x 0.8 x 0.35 mm

SOT1255

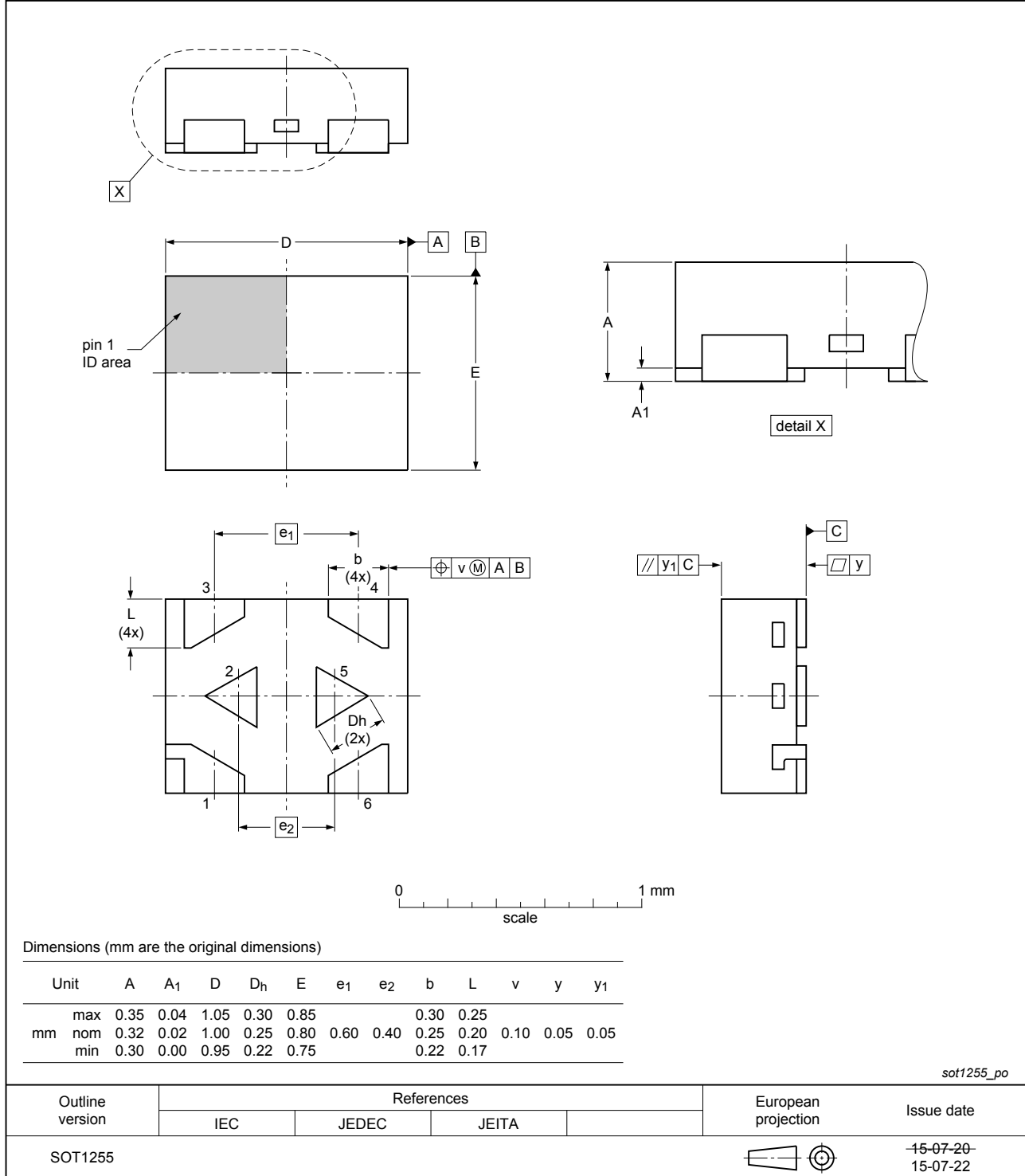


Figure 16. Package outline SOT1255 (X2SON6)

13 Abbreviations

Table 11. Abbreviations

| Acronym | Description |
|---------|---|
| CMOS | Complementary Metal Oxide Semiconductor |
| DUT | Device Under Test |
| ESD | ElectroStatic Discharge |
| HBM | Human Body Model |
| MM | Machine Model |
| TTL | Transistor-Transistor Logic |

14 Revision history

Table 12. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|---|--------------------|---------------|---------------|
| 74LVC2G07 v.10 | 20170821 | Product data sheet | - | 74LVC2G07 v.9 |
| Modifications: | <ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. | | | |
| 74LVC2G07 v.9 | 20161212 | Product data sheet | - | 74LVC2G07 v.8 |
| Modifications: | <ul style="list-style-type: none"> Table 7: The maximum limits for leakage current and supply current have changed. | | | |
| 74LVC2G07 v.8 | 20150923 | Product data sheet | - | 74LVC2G07 v.7 |
| Modifications: | <ul style="list-style-type: none"> Added type number 74LVC2G07GX (SOT1255/X2SON6). | | | |
| 74LVC2G07 v.7 | 20120704 | Product data sheet | - | 74LVC2G07 v.6 |
| Modifications: | <ul style="list-style-type: none"> Package outline drawing of SOT886 (Figure 12) modified. | | | |
| 74LVC2G07 v.6 | 20111130 | Product data sheet | - | 74LVC2G07 v.5 |
| Modifications: | <ul style="list-style-type: none"> Legal pages updated. | | | |
| 74LVC2G07 v.5 | 20100806 | Product data sheet | - | 74LVC2G07 v.4 |
| 74LVC2G07 v.4 | 20070521 | Product data sheet | - | 74LVC2G07 v.3 |
| 74LVC2G07 v.3 | 20040908 | Product data sheet | - | 74LVC2G07 v.2 |
| 74LVC2G07 v.2 | 20040319 | Product data sheet | - | 74LVC2G07 v.1 |
| 74LVC2G07 v.1 | 20030825 | Product data sheet | - | - |

15 Legal information

15.1 Data sheet status

| Document status ^{[1][2]} | Product status ^[3] | Definition |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

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