



THE DATASHEET OF LM309H/NOPB

LM109,LM309

LM109/LM309 5-Volt Regulator



Literature Number: SNVS752A

LM109/LM309

5-Volt Regulator

General Description

The LM109 series are complete 5V regulators fabricated on a single silicon chip. They are designed for local regulation on digital logic cards, eliminating the distribution problems associated with single-point regulation. The devices are available in two standard transistor packages. In the solid-kovar TO-5 header, it can deliver output currents in excess of 200 mA, if adequate heat sinking is provided. With the TO-3 power package, the available output current is greater than 1A.

The regulators are essentially blowout proof. Current limiting is included to limit the peak output current to a safe value. In addition, thermal shutdown is provided to keep the IC from overheating. If internal dissipation becomes too great, the regulator will shut down to prevent excessive heating.

Considerable effort was expended to make these devices easy to use and to minimize the number of external components. It is not necessary to bypass the output, although this

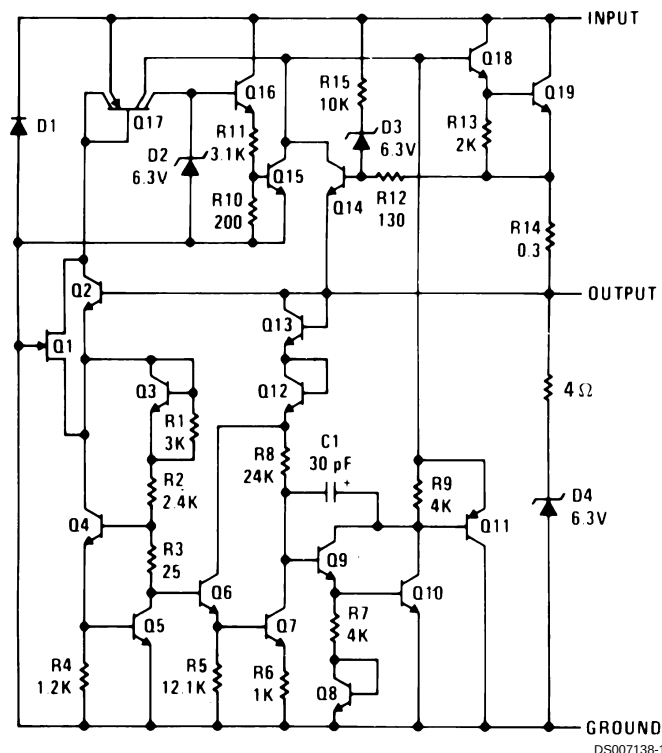
does improve transient response somewhat. Input bypassing is needed, however, if the regulator is located very far from the filter capacitor of the power supply. Stability is also achieved by methods that provide very good rejection of load or line transients as are usually seen with TTL logic.

Although designed primarily as a fixed-voltage regulator, the output of the LM109 series can be set to voltages above 5V, as shown. It is also possible to use the circuits as the control element in precision regulators, taking advantage of the good current-handling capability and the thermal overload protection.

Features

- Specified to be compatible, worst case, with TTL and DTL
- Output current in excess of 1A
- Internal thermal overload protection
- No external components required

Schematic Diagram



Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Input Voltage 35V
Power Dissipation Internally Limited

Operating Junction Temperature Range

LM109 -55°C to +150°C
LM309 0°C to +125°C

Storage Temperature Range -65°C to +150°C

Lead Temperature

(Soldering, 10 sec.) 300°C

Electrical Characteristics (Note 2)

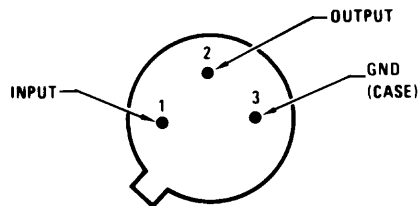
Parameter	Conditions	LM109			LM309			Units
		Min	Typ	Max	Min	Typ	Max	
Output Voltage	$T_j = 25^\circ\text{C}$	4.7	5.05	5.3	4.8	5.05	5.2	V
Line Regulation	$T_j = 25^\circ\text{C}$ $7.10\text{V} \leq V_{\text{IN}} \leq 25\text{V}$		4.0	50		4.0	50	mV
Load Regulation	$T_j = 25^\circ\text{C}$							
TO-39 Package	$5\text{ mA} \leq I_{\text{OUT}} \leq 0.5\text{A}$		15	50		15	50	mV
TO-3 Package	$5\text{ mA} \leq I_{\text{OUT}} \leq 1.5\text{A}$		15	100		15	100	mV
Output Voltage	$7.40\text{V} \leq V_{\text{IN}} \leq 25\text{V}$, $5\text{ mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$, $P < P_{\text{MAX}}$	4.6		5.4	4.75		5.25	V
Quiescent Current	$7.40\text{V} \leq V_{\text{IN}} \leq 25\text{V}$		5.2	10		5.2	10	mA
Quiescent Current Change	$7.40\text{V} \leq V_{\text{IN}} \leq 25\text{V}$ $5\text{ mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$			0.5 0.8			0.5 0.8	mA mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ $10\text{ Hz} \leq f \leq 100\text{ kHz}$		40			40		μV
Long Term Stability			10			20		mV
Ripple Rejection	$T_j = 25^\circ\text{C}$	50			50			dB
Thermal Resistance, Junction to Case	(Note 3)							
TO-39 Package			15			15		$^\circ\text{C/W}$
TO-3 Package			2.5			2.5		$^\circ\text{C/W}$

Note 1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: Unless otherwise specified, these specifications apply $-55^\circ\text{C} \leq T_j \leq +150^\circ\text{C}$ for the LM109 and $0^\circ\text{C} \leq T_j \leq +125^\circ\text{C}$ for the LM309; $V_{\text{IN}} = 10\text{V}$; and $I_{\text{OUT}} = 0.1\text{A}$ for the TO-39 package or $I_{\text{OUT}} = 0.5\text{A}$ for the TO-3 package. For the TO-39 package, $I_{\text{MAX}} = 0.2\text{A}$ and $P_{\text{MAX}} = 2.0\text{W}$. For the TO-3 package, $I_{\text{MAX}} = 1.0\text{A}$ and $P_{\text{MAX}} = 20\text{W}$.

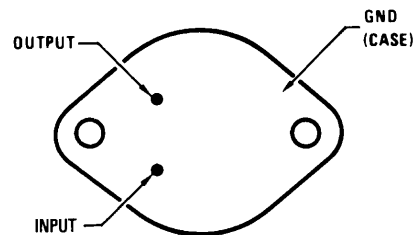
Note 3: Without a heat sink, the thermal resistance of the TO-39 package is about 150°C/W , while that of the TO-3 package is approximately 35°C/W . With a heat sink, the effective thermal resistance can only approach the values specified, depending on the efficiency of the sink.

Note 4: Refer to RETS109H drawing for LM109H or RETS109K drawing for LM109K military specifications.

Connection Diagrams**Metal Can Packages**

DS007138-33

Order Number LM109H, LM109H/883 or LM309H
See NS Package Number H03A



DS007138-34

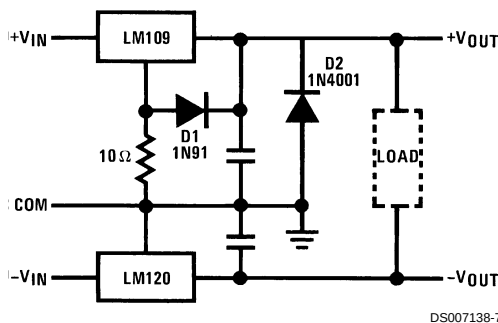
Order Number LM109K STEEL or
LM309K STEEL
See NS Package Number K02A
Order Number LM109K/883
See NS Package Number K02C

Application Hints

1. **Bypass the input** of the LM109 to ground with $\geq 0.2 \mu\text{F}$ ceramic or solid tantalum capacitor if main filter capacitor is more than 4 inches away.
2. **Avoid insertion of regulator into "live" socket** if input voltage is greater than 10V. The output will rise to within 2V of the unregulated input if the ground pin does not make contact, possibly damaging the load. The LM109 may also be damaged if a large output capacitor is charged up, then discharged through the internal clamp zener when the ground pin makes contact.
3. **The output clamp zener** is designed to absorb transients only. It will not clamp the output effectively if a failure occurs in the internal power transistor structure. Zener dynamic impedance is $\approx 4\Omega$. Continuous RMS current into the zener should not exceed 0.5A.
4. **Paralleling of LM109s** for higher output current is not recommended. Current sharing will be almost nonexistent, leading to a current limit mode operation for devices with the highest initial output voltage. The current limit devices may also heat up to the thermal shutdown point ($\approx 175^\circ\text{C}$). Long term reliability cannot be guaranteed under these conditions.

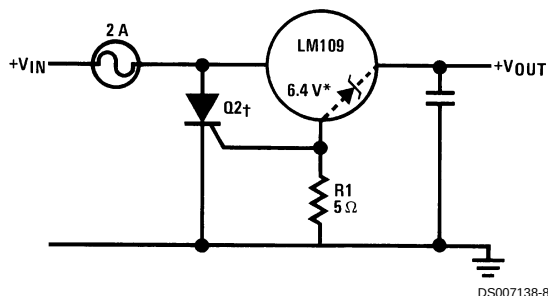
5. Preventing latchoff for loads connected to negative voltage:

If the output of the LM109 is pulled negative by a high current supply so that the output pin is more than 0.5V negative with respect to the ground pin, the LM109 can latch off. This can be prevented by clamping the ground pin to the output pin with a germanium or Schottky diode as shown. A silicon diode (1N4001) at the output is also needed to keep the positive output from being pulled too far negative. The 10Ω resistor will raise $+V_{\text{OUT}}$ by $\approx 0.05\text{V}$.

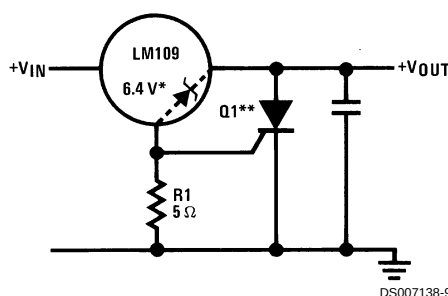


Crowbar Overvoltage Protection

Input Crowbar



Output Crowbar



*Zener is internal to LM109.

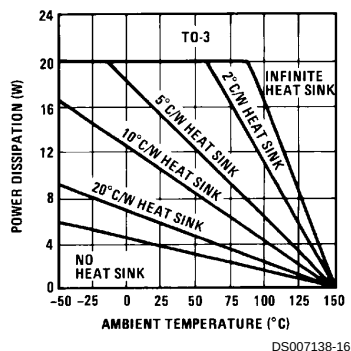
**Q1 must be able to withstand 7A continuous current if fusing is not used at regulator input. LM109 bond wires will fuse at currents above 7A.

†Q2 is selected for surge capability. Consideration must be given to filter capacitor size, transformer impedance, and fuse blowing time.

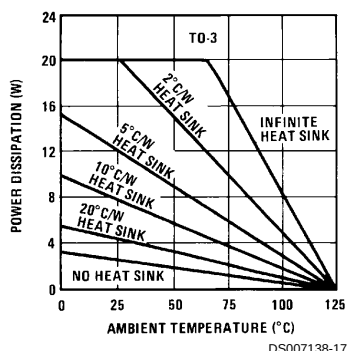
††Trip point is $\approx 7.5\text{V}$.

Typical Performance Characteristics

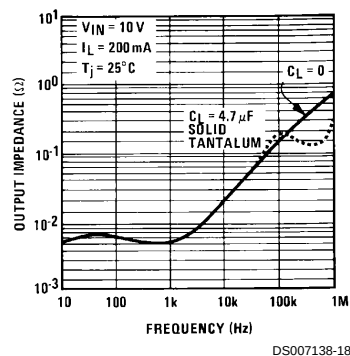
Maximum Average Power Dissipation (LM109K)



Maximum Average Power Dissipation (LM309K)

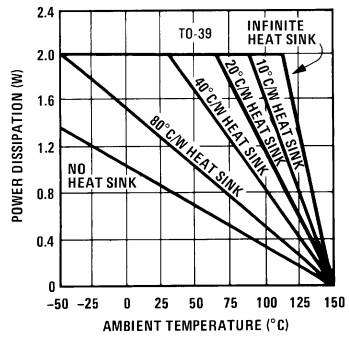


Output Impedance

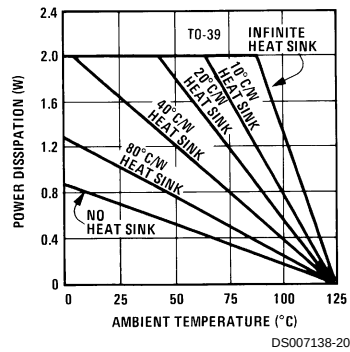


Typical Performance Characteristics (Continued)

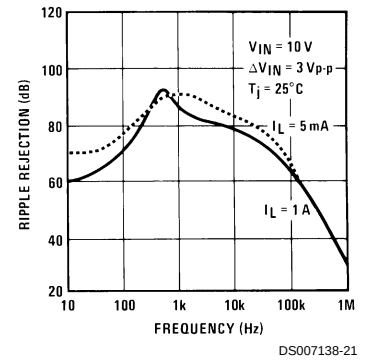
Maximum Average Power Dissipation (LM109H)



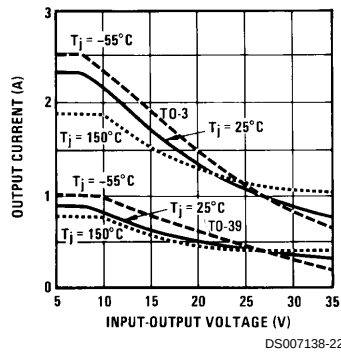
Maximum Average Power Dissipation (LM309H)



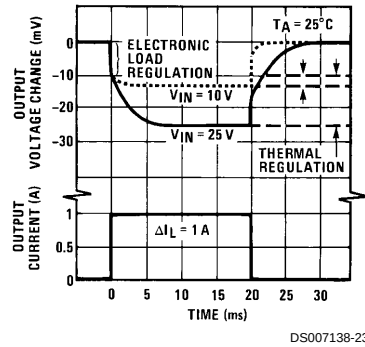
Ripple Rejection



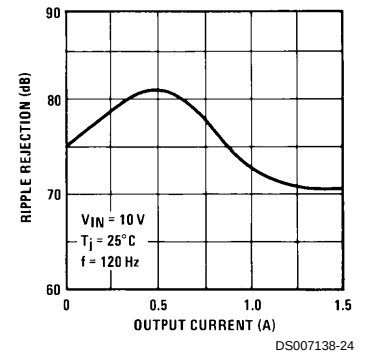
Current Limit Characteristics (Note 5)



Thermally Induced Output Voltage Variation

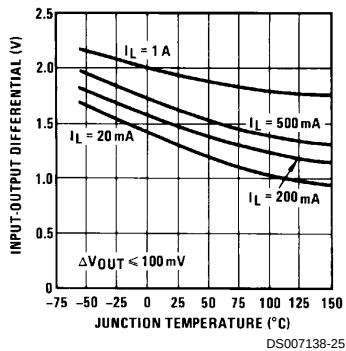


Ripple Rejection

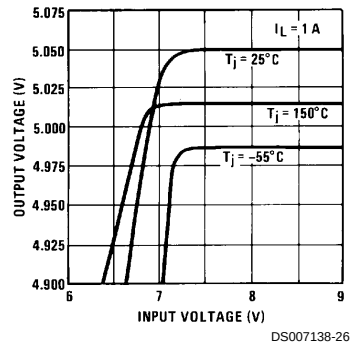


Note 5: Current limiting foldback characteristics are determined by input output differential, not by output voltage.

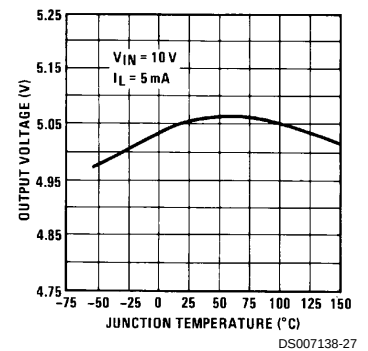
Input-Output Differential (V)



Output Voltage (V)

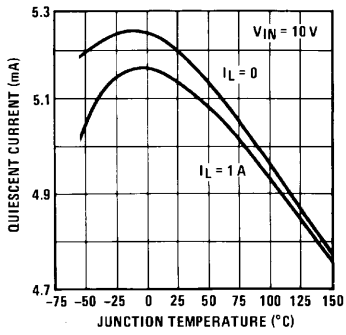


Output Voltage (V)

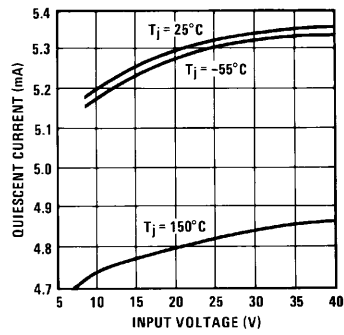


Typical Performance Characteristics (Continued)

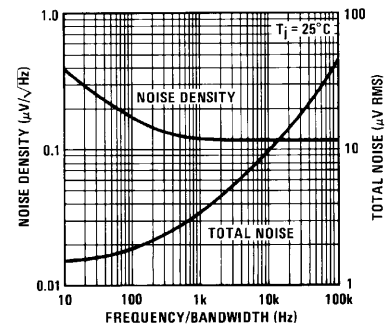
Quiescent Current



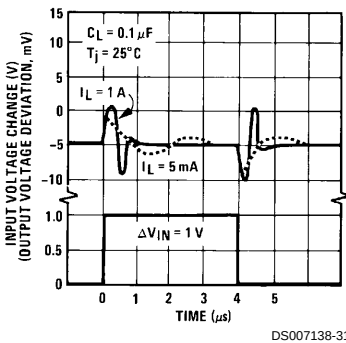
Quiescent Current



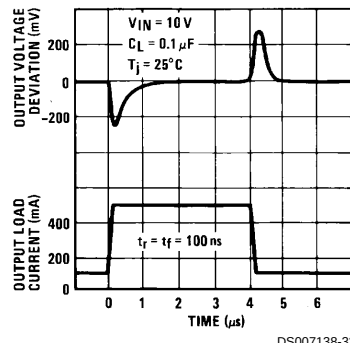
Output Voltage Noise



Line Transient Response

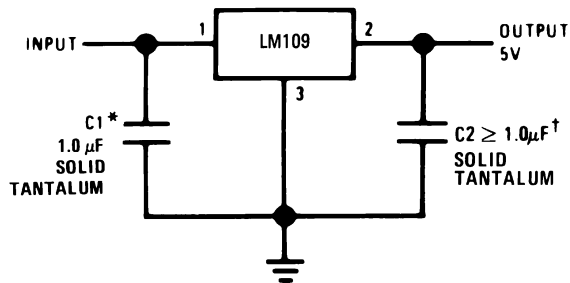


Load Transient Response

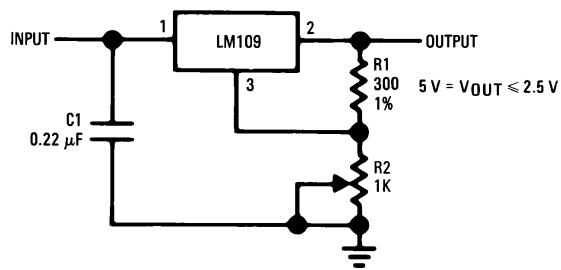


Typical Applications

Fixed 5V Regulator



Adjustable Output Regulator



*Required if regulator is located more than 4" from power supply filter capacitor.

†Although no output capacitor is needed for stability, it does improve transient response.

C2 should be used whenever long wires are used to connect to the load, or when transient response is critical.

Note: Pin 3 electrically connected to case.

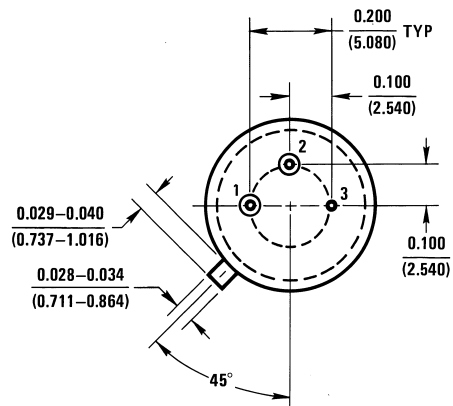
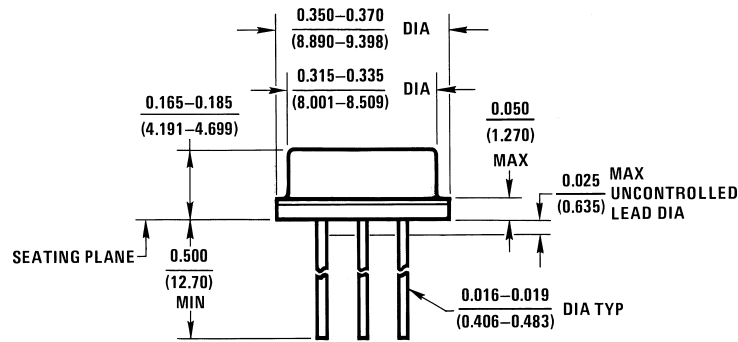
The circuit diagram shows a precision rectifier. The input signal is connected to the non-inverting input (pin 1) of the LM109. A coupling capacitor C1 (2.2 μF) is connected between the input and pin 1. The output of the LM109 (pin 2) is connected to the inverting input (pin 7) of the LM108A. The LM108A is configured as a precision rectifier. Its non-inverting input (pin 4) is connected to ground. The inverting input (pin 7) is connected to the output of the LM109. The output of the LM108A (pin 2) is connected to a feedback network consisting of a resistor R2 (6K, .005%) and a diode Q1 (1N829, 6.2V) in series. The other end of the diode Q1 is connected to ground. The output of the precision rectifier (pin 3 of the LM108A) is connected to a load resistor R3 (10K, .005%) and a diode Q2 (1N829, 6.2V) in series. The other end of the diode Q2 is connected to ground. The output of the circuit is taken from the junction between R3 and Q2. A capacitor C2 (100 pF) is connected between the output and ground. A resistor R1 (30K) is connected between the output and the inverting input of the LM108A (pin 7). A capacitor C3 (10 μF) is connected between the output and ground. The output voltage is specified as 10V ≤ 1A.

‡Solid tantalum.

The diagram shows an LM109 inverter circuit. The input signal is connected to pin 1. A capacitor C_1 (0.22 μF) is connected between pin 1 and ground. Pin 2 is connected to a resistor R_1 , which is then connected to the output. Pin 3 is connected to ground.

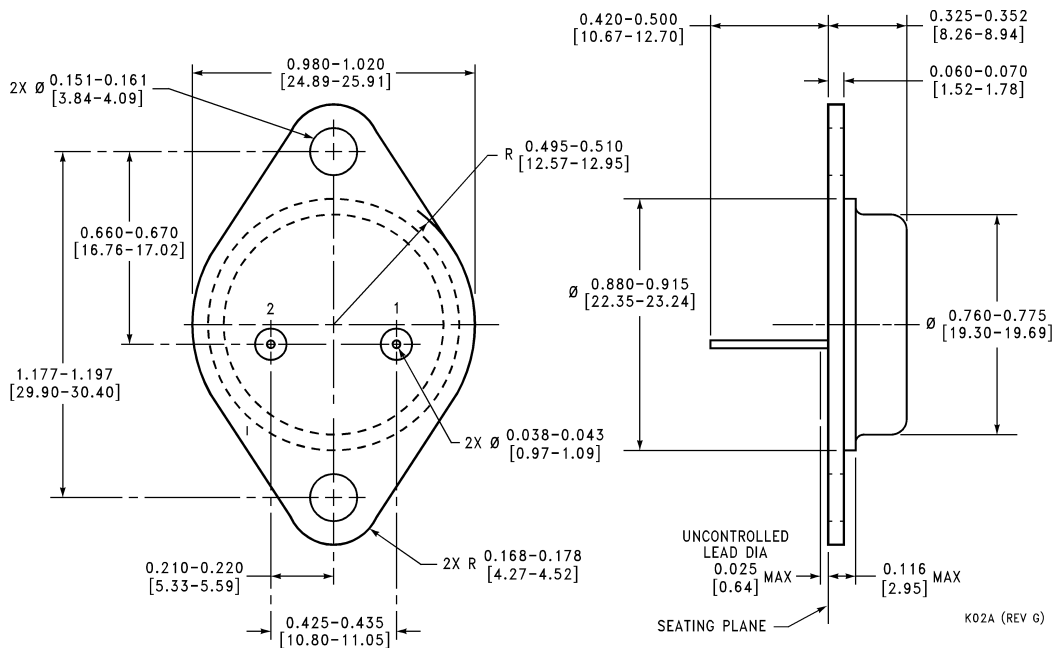
*Determines output current. If wirewound resistor is used, bypass with 0.1 μ F.

Physical Dimensions inches (millimeters) unless otherwise noted

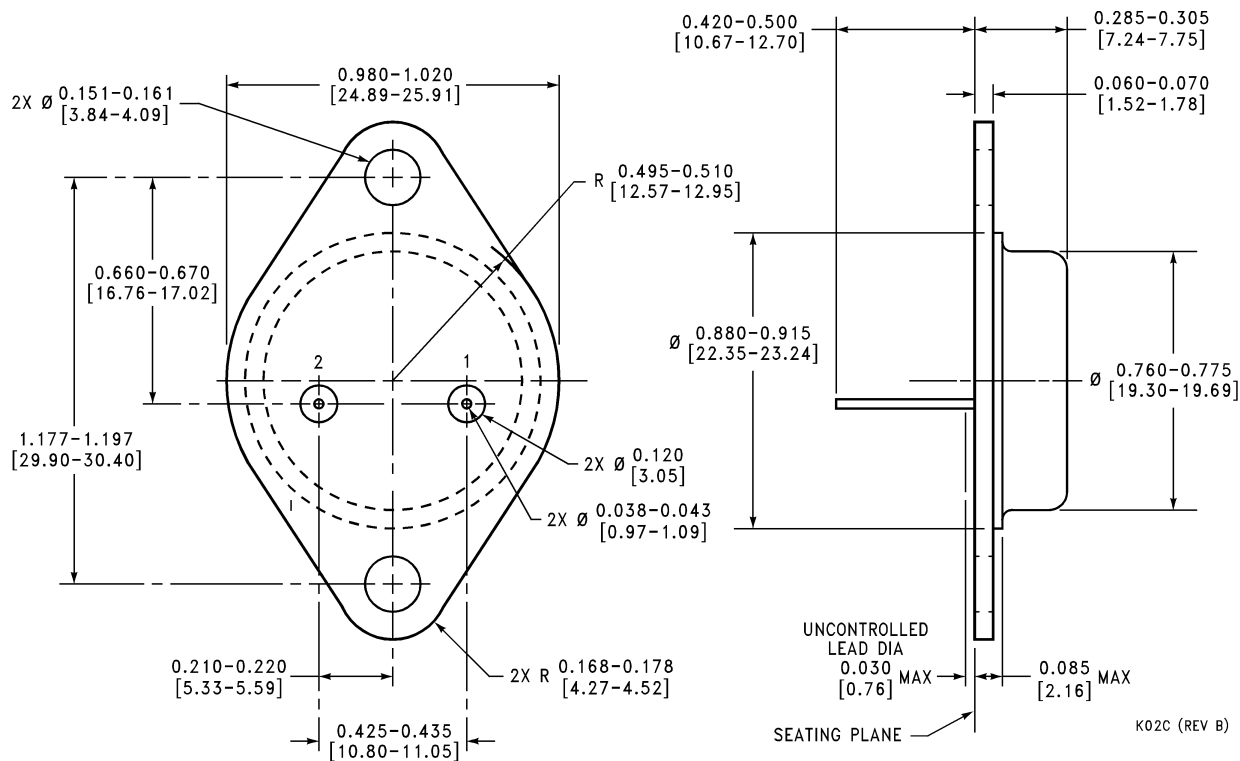


H03A (REV B)

Metal Can Package (H)
Order Number LM109H, LM109H/883 or LM309H
NS Package Number H03A



Metal Can Package (K)
Order Number LM109K STEEL, LM309K STEEL
NS Package Number K02A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

Metal Can Package (K)
Mil-Aero Product
Order Number LM109K/883
NS Package Number K02C

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