



## Product Summary

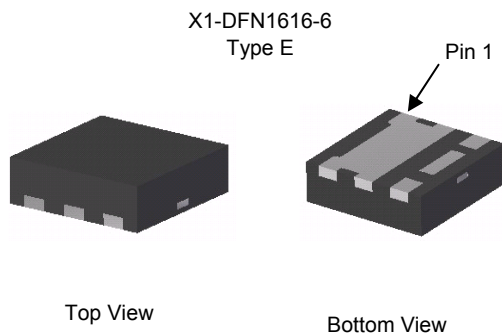
$V_{(BR)DSS}$	$R_{DS(ON)}$ max	$I_D$ max $T_A = +25^\circ\text{C}$
60V	85 m $\Omega$ @ $V_{GS} = 10\text{V}$	3.0A
	120 m $\Omega$ @ $V_{GS} = 4\text{V}$	2.5A

## Description

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- Power Management Functions
- Analog Switch

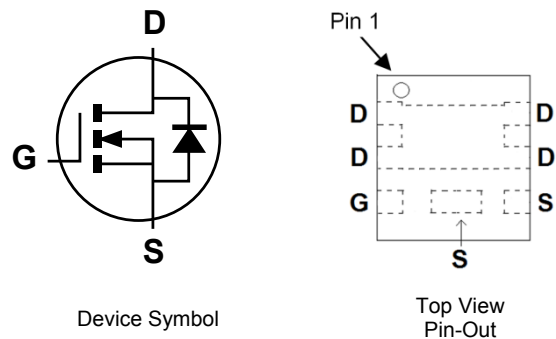


## Features and Benefits

- Typical off board profile of 0.5mm - ideally suited for thin applications
- Low  $R_{DS(ON)}$  – minimizes conduction losses
- PCB footprint of 2.56mm<sup>2</sup>
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 standards for High Reliability**

## Mechanical Data

- Case: X1-DFN1616-6 Type E
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead Free Plating (NiPdAu Finish over Copper leadframe)
- Terminals: Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.04 grams (approximate)

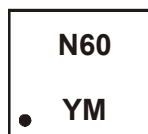


## Ordering Information (Note 4)

Product	Reel size (inches)	Tape Width (mm)	Quantity per Reel
DMN6070SFCL-7	7	8	3,000

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  - See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  - For packaging details, go to our website at <http://www.diodes.com/products/packages.html>

## Marking Information



N60 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: A = 2013)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2011	2012	2013	2014	2015	2016	2017
Code	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	60	V
Gate-Source Voltage			$V_{GSS}$	$\pm 20$	V
Continuous Drain Current (Note 6) $V_{GS} = 10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	$I_D$	3.0	A
		$T_A = +70^\circ\text{C}$		2.5	
Pulsed Drain Current (10 $\mu\text{s}$ pulse, Duty cycle = 1%)			$I_{DM}$	10	A

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation	(Note 5)	$P_D$	0.6	W
	(Note 6)		1.8	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	200	$^\circ\text{C/W}$
	(Note 6)		67	
Operating and Storage Temperature Range		$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics N-CHANNEL** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	60	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current $T_J = +25^\circ\text{C}$	$I_{DSS}$	—	—	1.0	$\mu\text{A}$	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 16\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	67	85	m $\Omega$	$V_{GS} = 10\text{V}, I_D = 1.5\text{A}$
			74	120		$V_{GS} = 4\text{V}, I_D = 0.5\text{A}$
Forward Transfer Admittance	$ Y_{fs} $	—	2.6	—	S	$V_{DS} = 5\text{V}, I_D = 1.5\text{A}$
Diode Forward Voltage	$V_{SD}$	—	0.7	1.2	V	$V_{GS} = 0\text{V}, I_S = 3\text{A}$
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	$C_{iss}$	—	606	—	pF	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	$C_{oss}$	—	32.6	—	pF	
Reverse Transfer Capacitance	$C_{rss}$	—	24.6	—	pF	
Gate Resistance	$R_g$	—	1.5	—	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge ( $V_{GS} = 10\text{V}$ )	$Q_g$	—	12.3	—	nC	$V_{DS} = 30\text{V}, I_D = 3\text{A}$
Total Gate Charge ( $V_{GS} = 4.5\text{V}$ )	$Q_g$	—	5.6	—	nC	
Gate-Source Charge	$Q_{gs}$	—	1.7	—	nC	
Gate-Drain Charge	$Q_{gd}$	—	1.9	—	nC	
Turn-On Delay Time	$t_{D(on)}$	—	3.5	—	ns	$V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, R_G = 20\Omega, R_L = 50\Omega$
Turn-On Rise Time	$t_r$	—	4.1	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	35	—	ns	
Turn-Off Fall Time	$t_f$	—	11	—	ns	

- Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout  
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate  
7. Short duration pulse test used to minimize self-heating effect.  
8. Guaranteed by design. Not subject to product testing.

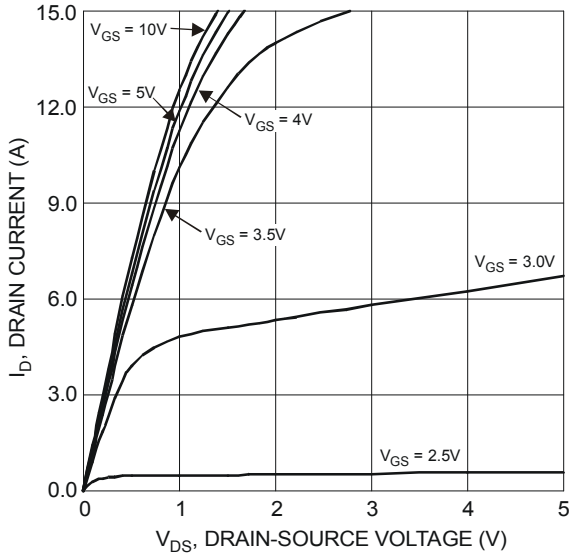


Figure 1 Typical Output Characteristics

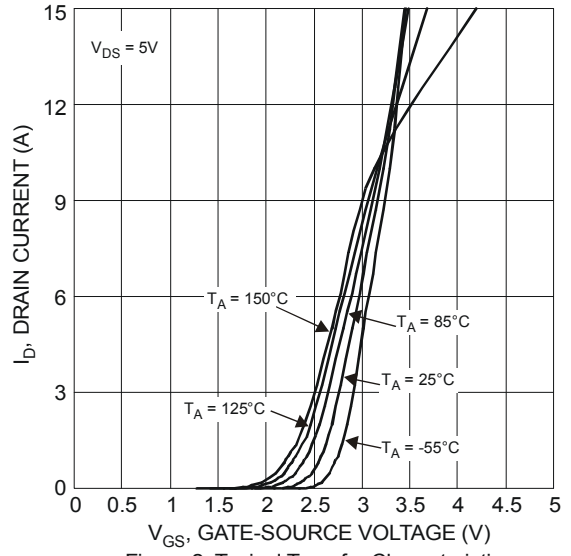


Figure 2 Typical Transfer Characteristics

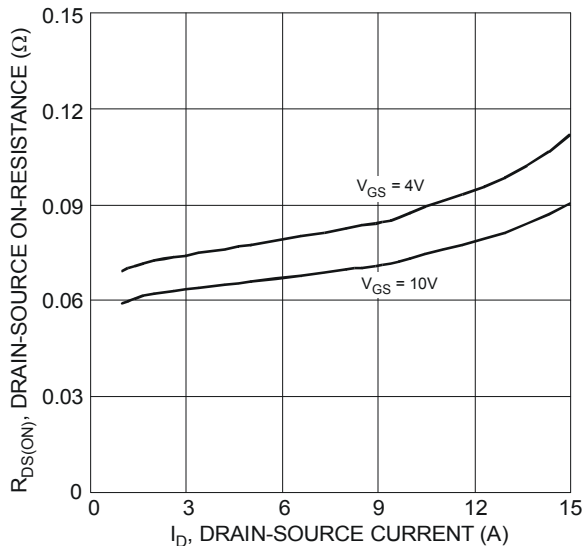


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

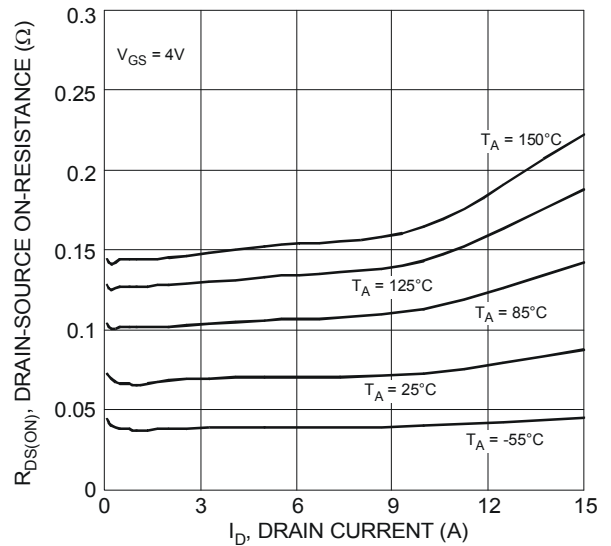


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

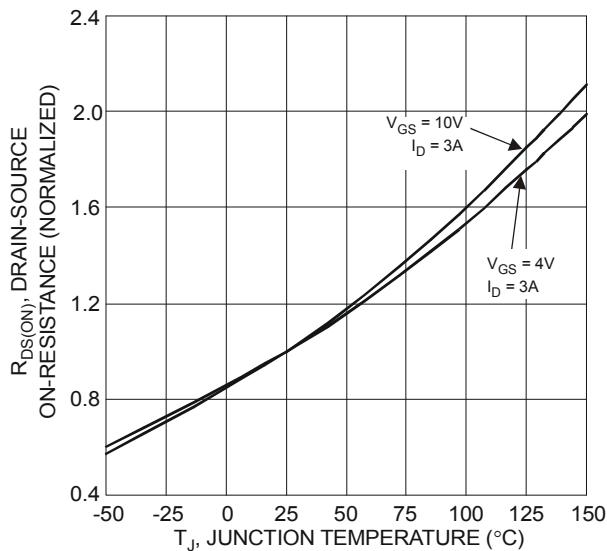


Figure 5 On-Resistance Variation with Temperature

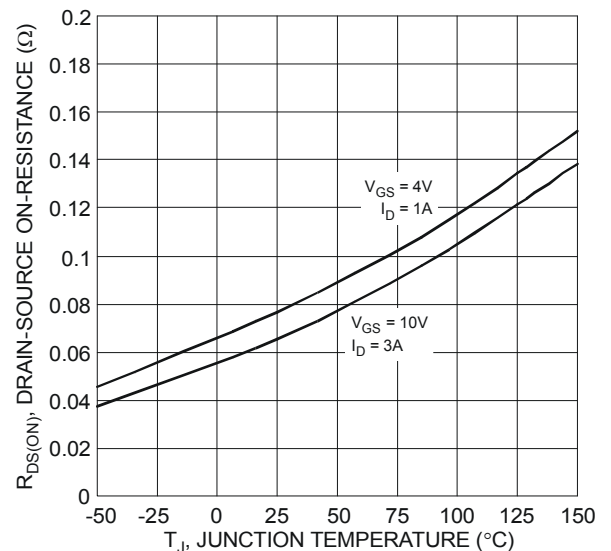


Figure 6 On-Resistance Variation with Temperature

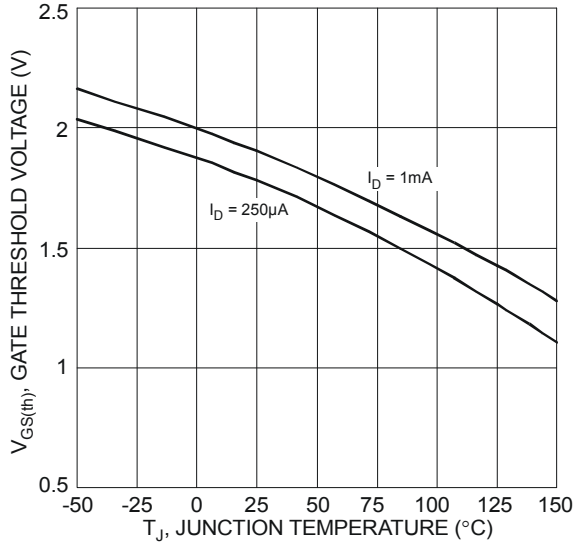


Figure 7 Gate Threshold Variation vs. Ambient Temperature

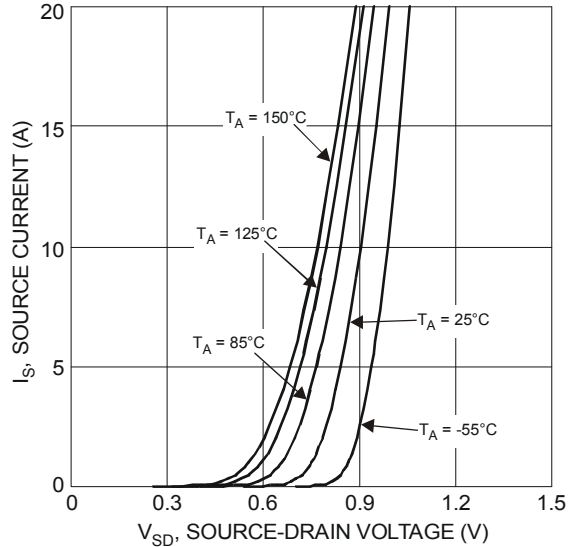


Figure 8 Diode Forward Voltage vs. Current

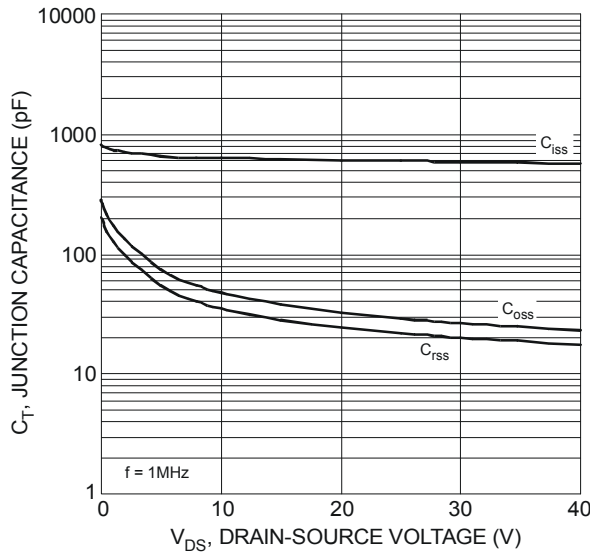


Figure 9 Typical Junction Capacitance

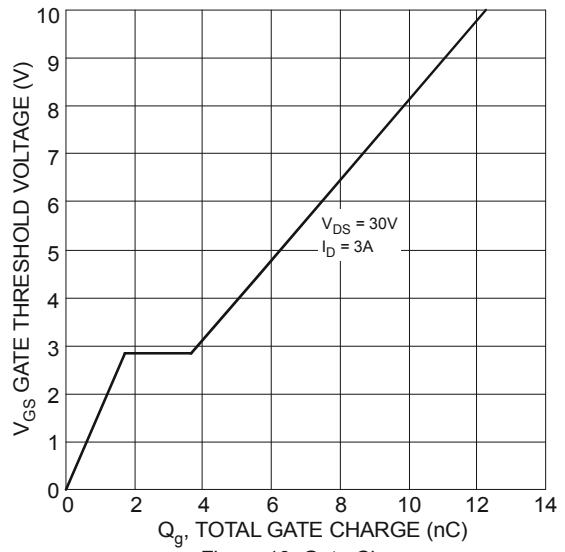
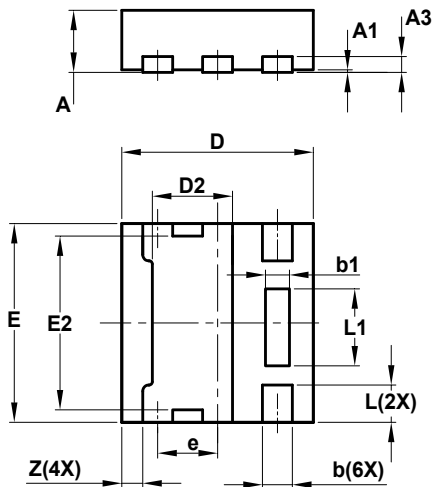


Figure 10 Gate Charge

**Package Outline Dimensions**

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.

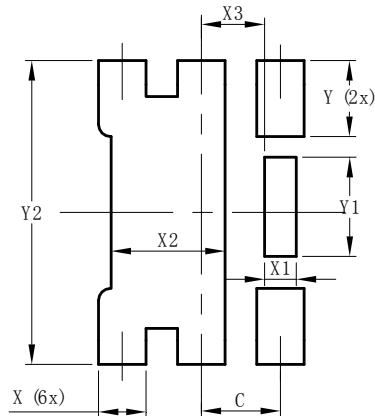


X1-DFN1616-6 Type E			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.02
A3	—	—	0.13
b	0.20	0.30	0.25
b1	0.10	0.30	0.20
D	1.55	1.65	1.60
D2	0.57	0.77	0.67
E	1.55	1.65	1.60
E2	1.30	1.50	1.40
e	—	—	0.50
L	0.25	0.35	0.30
L1	0.52	0.72	0.62
Z	—	—	0.175

All Dimensions in mm

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for latest version.



Dimensions	Value (in mm)
C	0.500
X	0.300
X1	0.200
X2	0.720
X3	0.400
Y	0.475
Y1	0.620
Y2	1.900

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