



**THE DATASHEET OF  
MJD148T4**



# MJD148

## NPN Silicon Power Transistor

### DPAK for Surface Mount Applications

Designed for general purpose amplifier and low speed switching applications.

#### Features

- High Gain
- Low Saturation Voltage
- High Current Gain – Bandwidth Product
- Epoxy Meets UL 94 V-0 @ 0.125 in
- NJV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	45	Vdc
Collector-Base Voltage	$V_{CB}$	45	Vdc
Emitter-Base Voltage	$V_{EB}$	5.0	Vdc
Collector Current – Continuous	$I_C$	4.0	Adc
Collector Current – Peak	$I_{CM}$	7.0	Adc
Base Current	$I_B$	50	mAdc
Total Power Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	20 0.16	W W/ $^\circ\text{C}$
Total Power Dissipation (Note 1) @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.75 0.014	W W/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$
ESD – Human Body Model	HBM	3B	V
ESD – Machine Model	MM	C	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

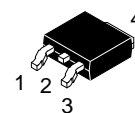
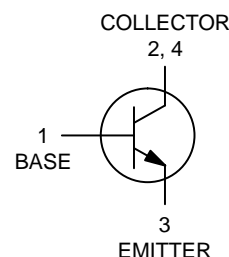
1. These ratings are applicable when surface mounted on the minimum pad sizes recommended.



ON Semiconductor®

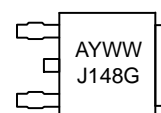
[www.onsemi.com](http://www.onsemi.com)

### POWER TRANSISTOR 4.0 AMPERES 45 VOLTS, 20 WATTS



DPAK  
CASE 369C  
STYLE 1

#### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
WW = Work Week  
J148 = Device Code  
G = Pb-Free Package

#### ORDERING INFORMATION

Device	Package	Shipping†
MJD148T4G	DPAK (Pb-Free)	2,500/Tape & Reel
NJVMJD148T4G	DPAK (Pb-Free)	2,500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MJD148

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	6.25	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	71.4	$^{\circ}\text{C/W}$

2. These ratings are applicable when surface mounted on the minimum pad sizes recommended.

## ELECTRICAL CHARACTERISTICS ( $T_C = 25^{\circ}\text{C}$ , unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
----------------	--------	-----	-----	------

### OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage (Note 3) ( $I_C = 100 \text{ mAdc}$ , $I_B = 0$ )	$V_{CEO(sus)}$	45	–	Vdc
Collector Cutoff Current ( $V_{CB} = 45 \text{ Vdc}$ , $I_E = 0$ )	$I_{CBO}$	–	20	$\mu\text{Adc}$
Emitter Cutoff Current ( $V_{BE} = 5 \text{ Vdc}$ , $I_C = 0$ )	$I_{EBO}$	–	1	mAdc

### ON CHARACTERISTICS (Note 3)

DC Current Gain ( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 5 \text{ Vdc}$ ) ( $I_C = 0.5 \text{ Adc}$ , $V_{CE} = 1 \text{ Vdc}$ ) ( $I_C = 2 \text{ Adc}$ , $V_{CE} = 1 \text{ Vdc}$ ) ( $I_C = 3 \text{ Adc}$ , $V_{CE} = 1 \text{ Vdc}$ )	$h_{FE}$	40 85 50 30	– 375 – –	–
Collector-Emitter Saturation Voltage ( $I_C = 2 \text{ Adc}$ , $I_B = 0.2 \text{ Adc}$ )	$V_{CE(sat)}$	–	0.5	Vdc
Base-Emitter On Voltage ( $I_C = 2 \text{ Adc}$ , $V_{CE} = 1 \text{ Vdc}$ )	$V_{BE(on)}$	–	1.1	Vdc

### DYNAMIC CHARACTERISTICS

Current-Gain-Bandwidth Product ( $I_C = 250 \text{ mAdc}$ , $V_{CE} = 1 \text{ Vdc}$ , $f = 1 \text{ MHz}$ )	$f_T$	3	–	MHz
---	-------	---	---	-----

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

TYPICAL CHARACTERISTICS

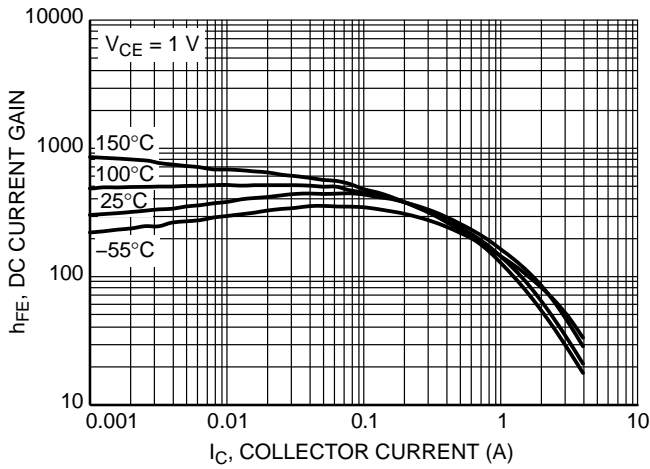


Figure 1. DC Current Gain

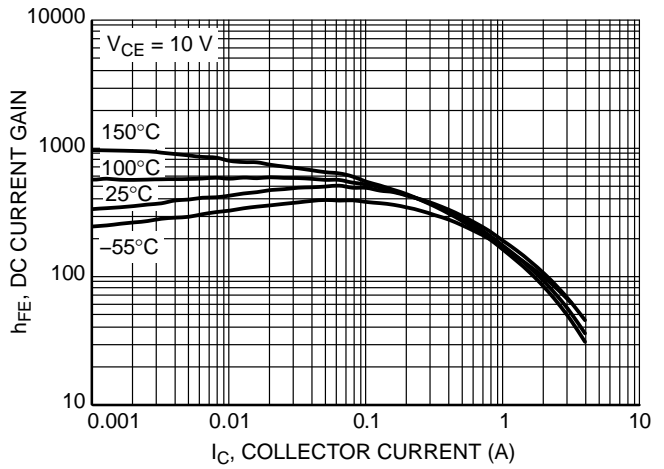


Figure 2. DC Current Gain

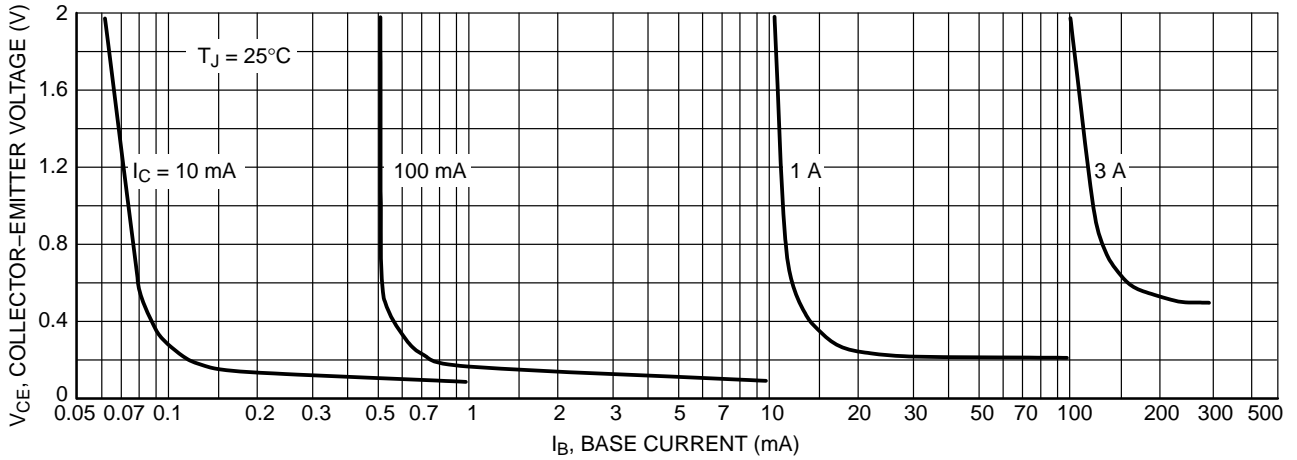


Figure 3. Collector Saturation Region

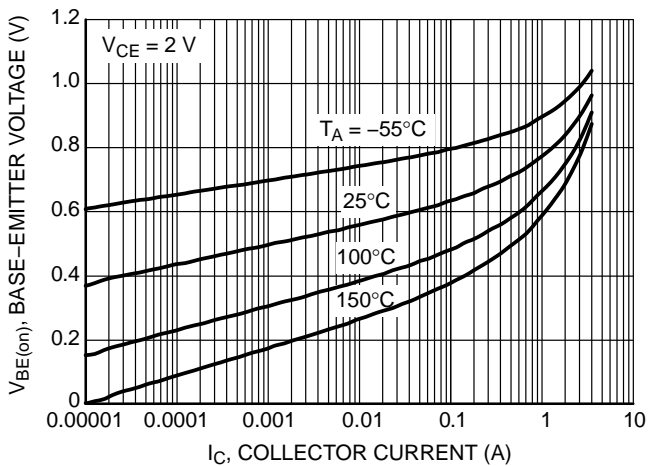


Figure 4. Base Emitter Voltage vs. Collector Current

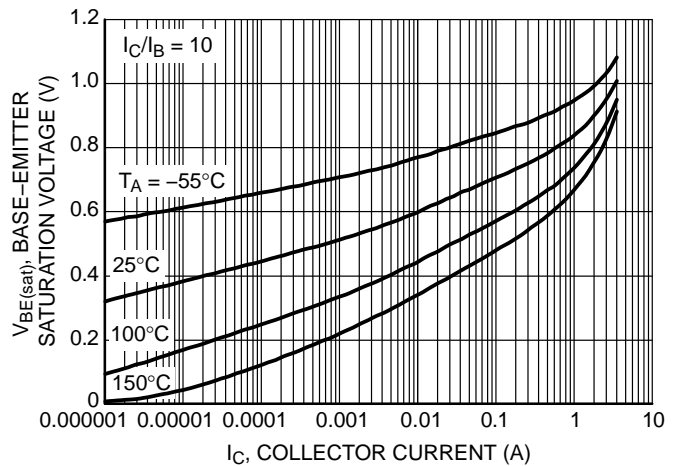
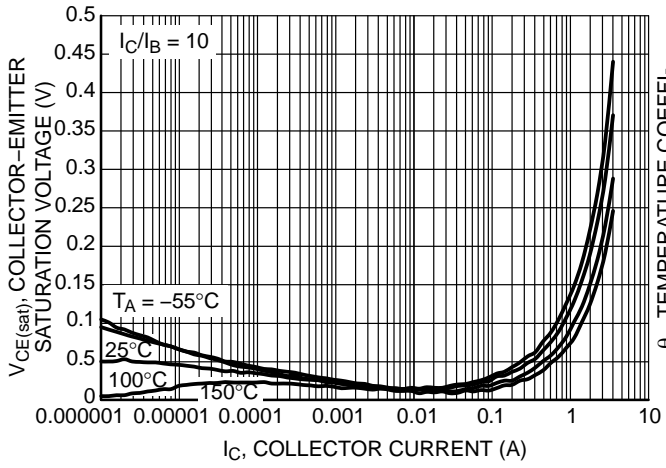
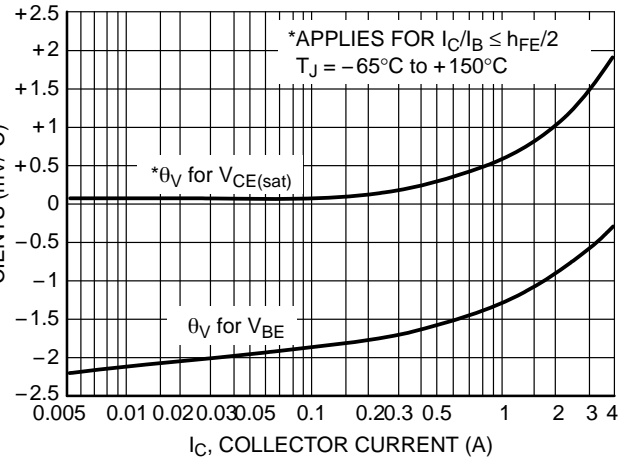


Figure 5. Base Emitter Saturation Voltage vs. Collector Current

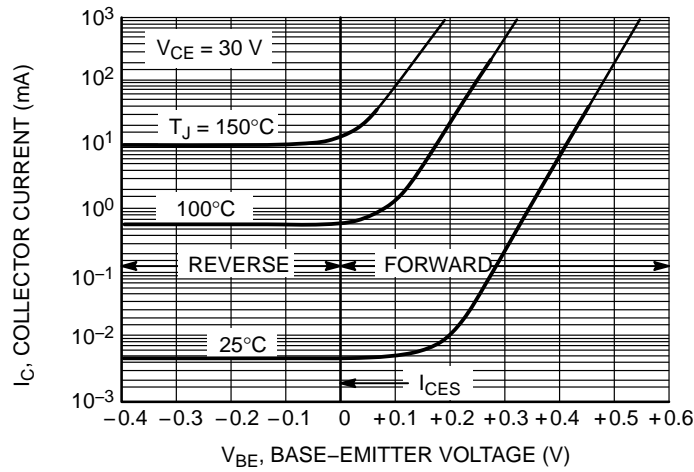
# MJD148



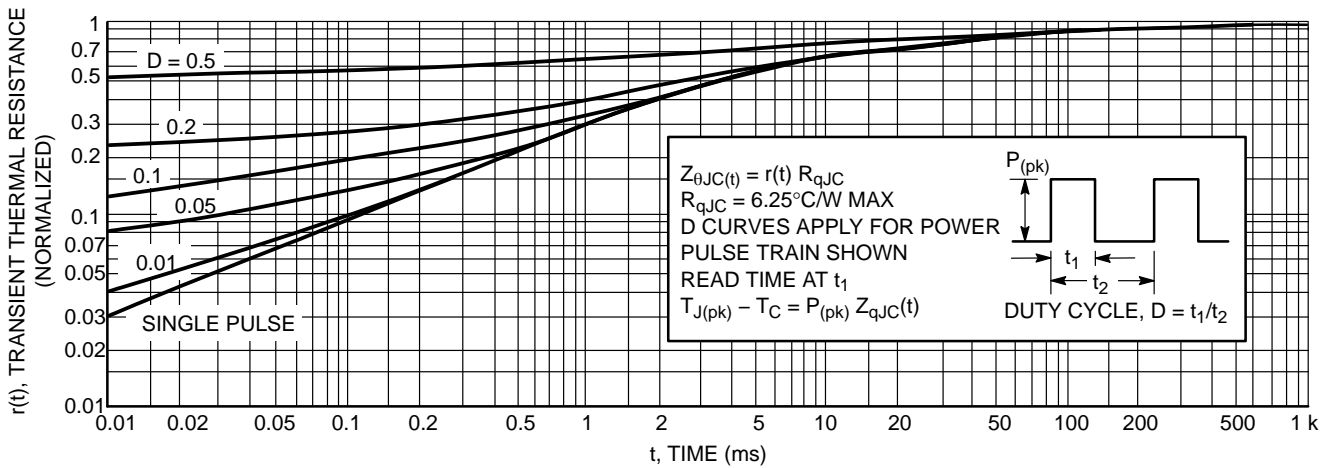
**Figure 6. Collector Emitter Saturation Voltage vs. Collector Current**



**Figure 7. Temperature Coefficients**



**Figure 8. Collector Cut-Off Region**



**Figure 9. Thermal Response**

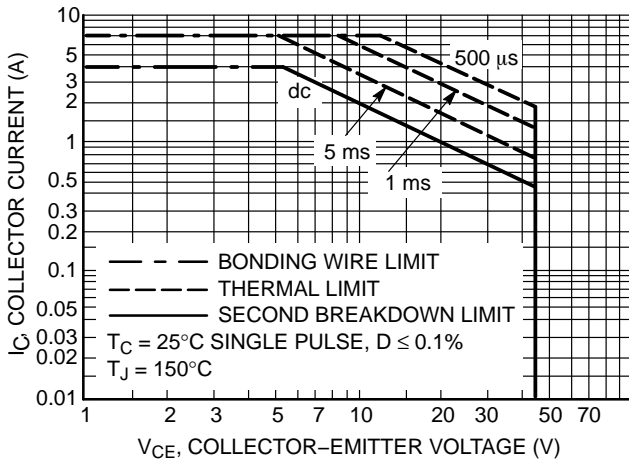


Figure 10. Maximum Rated Forward Bias

**Forward Bias Safe Operating Area Information**

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 10 is based on  $T_{J(pk)} = 150^\circ\text{C}$ ;  $T_C$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 150^\circ\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 9. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at [www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)

## Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

 [View MJD148T4 on WIN SOURCE](#)

 [ON Semiconductor](#) Information

## Optimize Your Supply Chain with WIN SOURCE Solutions

-  Global Sourcing Solution
-  Obsolete Management
-  Cost Control Management
-  Shortage Management
-  Alternative Solution
-  Excess Inventory Management