



NPN Darlington Power Silicon Transistor

Qualified per MIL-PRF-19500/523

Qualified Levels:
JAN, JANTX, and
JANTXV

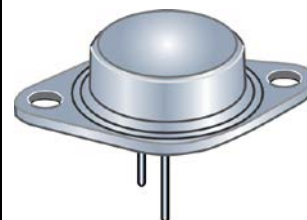
DESCRIPTION

This high speed NPN transistor is rated at 10 amps and is military qualified up to the JANTXV level. This TO-204AA isolated package features a 180 degree lead orientation.

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FEATURES

- JEDEC registered 2N6383 through 2N6385
- JAN, JANTX, and JANTXV qualifications are available per MIL-PRF-19500/523. (See [part nomenclature](#) for all available options.)
- RoHS compliant versions are available (commercial grade only)



**TO-204AA (TO-3)
Package**

APPLICATIONS / BENEFITS

- Military and other high reliability applications
- High frequency response
- TO-204AA case with isolated terminals

MAXIMUM RATINGS @ $T_A = +25^\circ\text{C}$ unless otherwise noted

Parameters/Test Conditions	Symbol	Value	Unit
Junction and Storage Temperature	T_J and T_{STG}	-55 to +175	$^\circ\text{C}$
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	1.75	$^\circ\text{C/W}$
Collector-Emitter Voltage	V_{CEO}	2N6383	40
		2N6384	60
		2N6385	80
Collector-Base Voltage	V_{CBO}	2N6383	40
		2N6384	60
		2N6385	80
Emitter-Base Voltage	V_{EBO}	5	V
Total Power Dissipation	P_T	@ $T_A = +25^\circ\text{C}$ ⁽¹⁾	6.0
		@ $T_C = +25^\circ\text{C}$ ⁽²⁾	100
Base Current	I_B	0.25	A
Collector Current	I_C	10	A

Notes:

1. Derate linearly 34.2 mW/ $^\circ\text{C}$ above $T_A > +25^\circ\text{C}$.
2. Derate linearly 571 mW/ $^\circ\text{C}$ above $T_C > +25^\circ\text{C}$.

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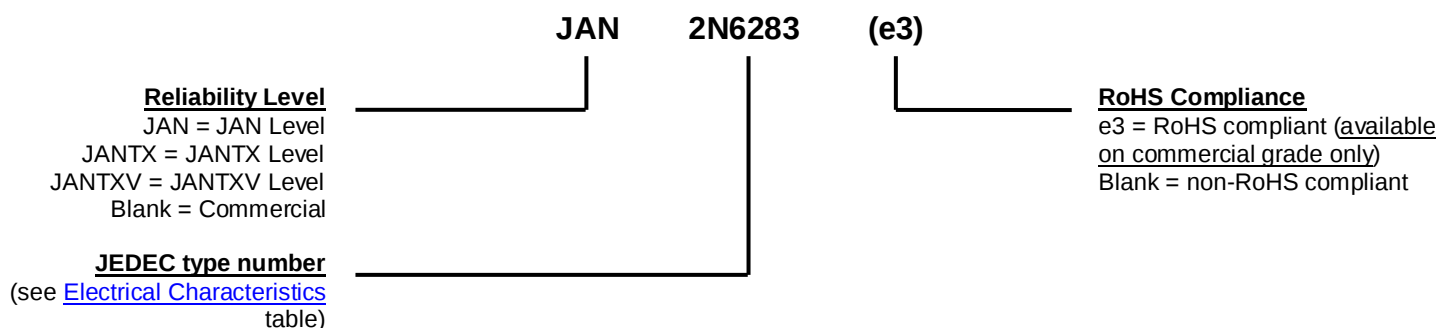
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MECHANICAL and PACKAGING

- CASE: Industry standard TO-204AA (TO-3), hermetically sealed, 0.040 inch diameter pins
- FINISH: Solder dipped tin-lead over nickel plated alloy 52 or RoHS compliant matte-tin plating. Solderable per MIL-STD-750 method 2026.
- POLARITY: NPN (see [schematic](#))
- MOUNTING HARDWARE: Consult factory for optional insulator and sheet metal screws
- WEIGHT: Approximately 15 grams
- See [package dimensions](#) on last page.

PART NOMENCLATURE



SYMBOLS & DEFINITIONS

Symbol	Definition
I_B	Base current: The value of the dc current into the base terminal.
I_C	Collector current: The value of the dc current into the collector terminal.
I_E	Emitter current: The value of the dc current into the emitter terminal.
T_C	Case temperature: The temperature measured at a specified location on the case of a device.
V_{CB}	Collector-base voltage: The dc voltage between the collector and the base.
V_{CBO}	Collector-base voltage, base open: The voltage between the collector and base terminals when the emitter terminal is open-circuited.
V_{CC}	Collector-supply voltage: The supply voltage applied to a circuit connected to the collector.
V_{CEO}	Collector-emitter voltage, base open: The voltage between the collector and the emitter terminals when the base terminal is open-circuited.
V_{CE}	Collector-emitter voltage: The dc voltage between the collector and the emitter.
V_{EB}	Emitter-base voltage: The dc voltage between the emitter and the base.
V_{EBO}	Emitter-base voltage, collector open: The voltage between the emitter and base terminals with the collector terminal open-circuited.

ELECTRICAL CHARACTERISTICS @ $T_A = +25^\circ\text{C}$ unless otherwise noted

Characteristics	Symbol	Min.	Max.	Unit
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OFF CHARACTERISTICS

Collector-Emitter Breakdown Voltage $I_C = 200\text{ mA}$	2N6383 2N6384 2N6385	$V_{(BR)CEO}$	40 60 80	V
Collector-Emitter Breakdown Voltage $I_C = 200\text{ mA}, R_{BB} = 100\ \Omega$	2N6383 2N6384 2N6385	$V_{(BR)CER}$	40 60 80	V
Collector-Emitter Cutoff Current $V_{CE} = 40\text{ V}$ $V_{CE} = 60\text{ V}$ $V_{CE} = 80\text{ V}$	2N6383 2N6384 2N6385	I_{CEO}	1.0	mA
Collector-Emitter Cutoff Current $V_{CE} = 40\text{ V}, V_{BE} = 1.5\text{ V}$ $V_{CE} = 60\text{ V}, V_{BE} = 1.5\text{ V}$ $V_{CE} = 80\text{ V}, V_{BE} = 1.5\text{ V}$	2N6383 2N6384 2N6385	I_{CEX}	100	μA
Emitter-Base Cutoff Current $V_{EB} = 5.0\text{ V}$		I_{EBO}	5.0	mA
Collector-Emitter Cutoff Current $V_{CE} = 40\text{ V}$ $V_{CE} = 60\text{ V}$ $V_{CE} = 80\text{ V}$	2N6383 2N6384 2N6385	I_{CBO}	1.0	mA

ON CHARACTERISTICS

Forward-Current Transfer Ratio $I_C = 5.0\text{ A}, V_{CE} = 3.0\text{ V}$ $I_C = 10\text{ A}, V_{CE} = 3.0\text{ V}$ $I_C = 5.0\text{ A}, V_{CE} = 3.0\text{ V}, T_A = -55^\circ\text{C}$	h_{FE}	1,000 100 200	20,000	
Collector-Emitter Saturation Voltage $I_C = 5.0\text{ A}, I_B = 10\text{ mA}$ $I_C = 10\text{ A}, I_B = 0.1\text{ A}$	$V_{CE(sat)}$		2.0 3.0	V
Base-Emitter Voltage Non-saturated $V_{CE} = 3.0\text{ V}, I_C = 5.0\text{ A}$ $V_{CE} = 3.0\text{ V}, I_C = 10\text{ A}$	$V_{BE(on)}$		2.8 4.5	V

DYNAMIC CHARACTERISTICS

Magnitude of Common Emitter Small-Signal Short-Circuit Forward Current Transfer Ratio $I_C = 1.0\text{ A}, V_{CE} = 5.0\text{ V}, f = 1.0\text{ MHz}$	$ h_{fe} $	20	300	
Output Capacitance $V_{CB} = 10\text{ V}, I_E = 0, f = 100\text{ kHz} \leq f \leq 1\text{ MHz}$	C_{obo}		200	pF

ELECTRICAL CHARACTERISTICS @ $T_C = 25^\circ\text{C}$ unless otherwise noted. (continued)**SWITCHING CHARACTERISTICS**

Turn-On Time $V_{CC} = 30\text{ V}$, $I_C = 5.0\text{ A}$; $I_B = 20\text{ mA}$	t_{on}		2.5	μs
Turn-Off Time $V_{CC} = 30\text{ V}$, $I_C = 5.0\text{ A}$; $I_{B1} = -I_{B2} = 20\text{ mA}$	t_{off}		10	μs

SAFE OPERATING AREA (See [Figures 1 and 2](#) and [MIL-STD-750, Test Method 3053](#))**DC Tests** $T_C = +25^\circ\text{C}$, $t = 1\text{ second}$, 1 Cycle**Test 1** $V_{CE} = 10\text{ V}$, $I_C = 10\text{ A}$ **Test 2** $V_{CE} = 30\text{ V}$, $I_C = 3.33\text{ A}$ **Test 3** $V_{CE} = 40\text{ V}$, $I_C = 1.5\text{ A}$ (2N6383) $V_{CE} = 60\text{ V}$, $I_C = 0.4\text{ A}$ (2N6384) $V_{CE} = 80\text{ V}$, $I_C = 0.16\text{ A}$ (2N6385)

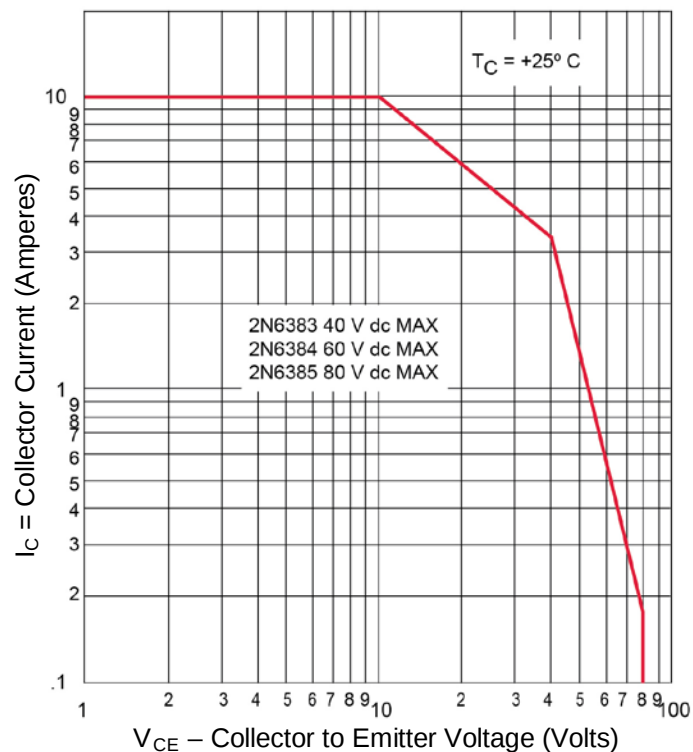
SAFE OPERATING AREA


FIGURE 1
Maximum Safe Operating Graph (continuous dc)

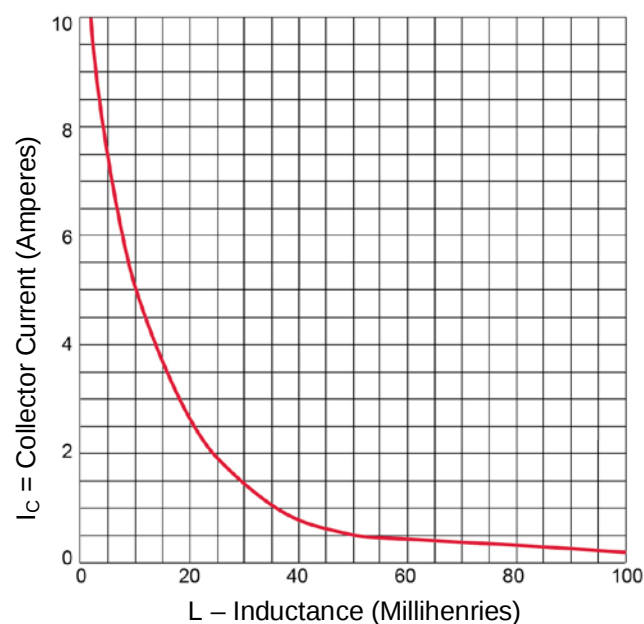
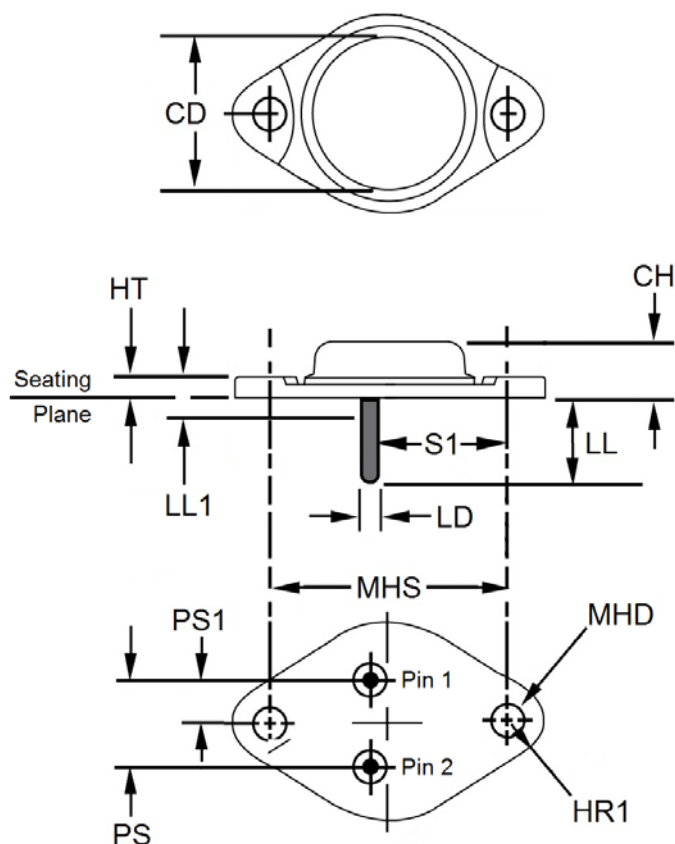


FIGURE 2
Safe Operating Area for Switching Between Saturation and Cutoff (unclamped inductive load)

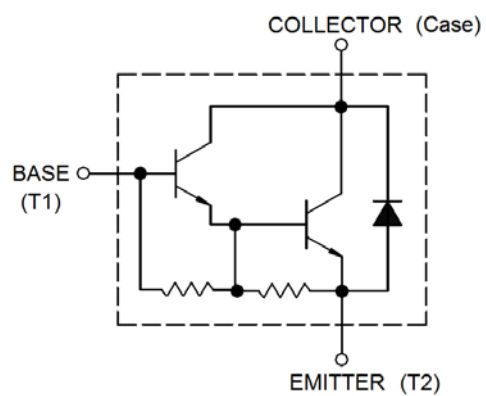
PACKAGE DIMENSIONS


Ltr	Dimensions				Notes
	Inches		Millimeters		
	Min	Max	Min	Max	
CD	-	0.875	-	22.23	3
CH	0.250	0.450	6.35	11.43	
HR	0.495	0.525	12.57	13.34	
HR1	0.131	0.188	3.33	4.78	4
HT	0.060	0.135	1.52	3.43	
LD	0.038	0.043	0.97	1.09	5, 6
LL	0.312	0.500	7.92	12.70	5
LL1	-	0.050	-	1.27	5, 6
MHD	0.151	0.161	3.84	4.09	7
MHS	1.177	1.197	29.90	30.40	
PS	0.420	0.440	10.67	11.18	8, 9
PS1	0.205	0.225	5.21	5.72	5, 8, 9
S1	0.655	0.675	16.64	17.15	8

NOTES:

1. Dimensions are in inches. Millimeters are given for information only.
2. Terminal 1 is the base and terminal 2 is the emitter. The collector shall be electrically connected to the case.
3. Body contour is optional within zone defined by dimension CD.
4. Applies to both ends.
5. Applies to both terminals.
6. Dimension LD applies between L1 and LL. Lead diameter shall not exceed twice dimension LD within dimension L1. Diameter is uncontrolled in dimension L1.
7. Two holes.
8. These dimensions shall be measured at points 0.050 inch (1.27 mm) to 0.055 inch (1.40 mm) below the seating plane. When gauge is not used, measurement shall be made at seating plane.
9. The seating plane of the header shall be flat within 0.001 inch (0.03 mm) concave to 0.004 inch (0.10 mm) convex inside a 0.930 inch (23.62 mm) diameter circle on the center of the header and flat within 0.001 inch (0.03 mm) concave to 0.006 inch (0.15 mm) convex overall.
10. Mounting holes shall be deburred on the seating plane side.
11. In accordance with ASME Y14.5M, diameters are equivalent to Φ x symbology.

See schematic on next page

SCHEMATIC

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