



**THE DATASHEET OF  
MJE18006**



# MJE18006G

## SWITCHMODE™

### NPN Bipolar Power Transistor For Switching Power Supply Applications

The MJE18006G has an applications specific state-of-the-art die designed for use in 220 V line-operated SWITCHMODE Power supplies and electronic light ballasts.

#### Features

- Improved Efficiency Due to Low Base Drive Requirements:
  - ◆ High and Flat DC Current Gain  $h_{FE}$
  - ◆ Fast Switching
  - ◆ No Coil Required in Base Circuit for Turn-Off (No Current Tail)
- Tight Parametric Distributions are Consistent Lot-to-Lot
- Standard TO-220
- These Devices are Pb-Free and are RoHS Compliant\*

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Sustaining Voltage	$V_{CEO}$	450	Vdc
Collector-Emitter Breakdown Voltage	$V_{CES}$	1000	Vdc
Emitter-Base Voltage	$V_{EBO}$	9.0	Vdc
Collector Current – Continuous – Peak (Note 1)	$I_C$ $I_{CM}$	6.0 15	Adc
Base Current – Continuous – Peak (Note 1)	$I_B$ $I_{BM}$	4.0 8.0	Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	100 0.8	W W/ $^\circ\text{C}$
Operating and Storage Temperature	$T_J, T_{stg}$	-65 to 150	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.25	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 Seconds	$T_L$	260	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

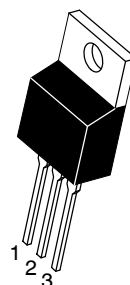
1. Pulse Test: Pulse Width = 5 ms, Duty Cycle  $\leq 10\%$ .



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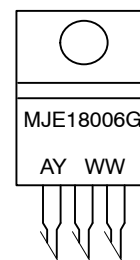
<http://onsemi.com>

**POWER TRANSISTOR  
6.0 AMPERES  
1000 VOLTS – 100 WATTS**



TO-220AB  
CASE 221A-09  
STYLE 1

#### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
WW = Work Week  
G = Pb-Free Package

#### ORDERING INFORMATION

Device	Package	Shipping
MJE18006G	TO-220 (Pb-Free)	50 Units / Rail

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MJE18006G

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise specified)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector–Emitter Sustaining Voltage (I <sub>C</sub> = 100 mA, L = 25 mH)	V <sub>CEO(sus)</sub>	450	–	–	Vdc
Collector Cutoff Current (V <sub>CE</sub> = Rated V <sub>CEO</sub> , I <sub>B</sub> = 0)	I <sub>CEO</sub>	–	–	100	μAdc
Collector Cutoff Current (V <sub>CE</sub> = Rated V <sub>CE(s)</sub> , V <sub>EB</sub> = 0)	I <sub>CES</sub>	–	–	100	μAdc
		–	–	500	
(V <sub>CE</sub> = 800 V, V <sub>EB</sub> = 0)		–	–	100	
(T <sub>C</sub> = 125°C)		–	–	–	
(T <sub>C</sub> = 125°C)		–	–	–	
Emitter Cutoff Current (V <sub>EB</sub> = 9.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	–	–	100	μAdc

## ON CHARACTERISTICS

Base–Emitter Saturation Voltage (I <sub>C</sub> = 1.3 Adc, I <sub>B</sub> = 0.13 Adc) (I <sub>C</sub> = 3.0 Adc, I <sub>B</sub> = 0.6 Adc)	V <sub>BE(sat)</sub>	–	0.83 0.94	1.2 1.3	Vdc
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 1.3 Adc, I <sub>B</sub> = 0.13 Adc)	V <sub>CE(sat)</sub>	–	0.25	0.6	Vdc
		–	0.27	0.65	
(T <sub>C</sub> = 125°C)		–	0.35	0.7	
(I <sub>C</sub> = 3.0 Adc, I <sub>B</sub> = 0.6 Adc)		–	0.4	0.8	
		–	–	–	
(T <sub>C</sub> = 125°C)		–	–	–	
DC Current Gain (I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	14	–	34	–
		–	32	–	
(T <sub>C</sub> = 125°C)		–	–	–	
(I <sub>C</sub> = 3.0 Adc, V <sub>CE</sub> = 1.0 Vdc)		6.0	10	–	
		5.0	8.0	–	
(I <sub>C</sub> = 1.3 Adc, V <sub>CE</sub> = 1.0 Vdc)		11	17	–	
(I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 5.0 Vdc)		10	22	–	
		–	–	–	
(T <sub>C</sub> = 25 to 125°C)		–	–	–	

## DYNAMIC CHARACTERISTICS

Current Gain Bandwidth (I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 10 Vdc, f = 1.0 MHz)	f <sub>T</sub>	–	14	–	MHz			
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	–	75	120	pF			
Input Capacitance (V <sub>EB</sub> = 8.0 V)	C <sub>ib</sub>	–	1000	1500	pF			
Dynamic Saturation Voltage:	V <sub>CE(dsat)</sub>	(I <sub>C</sub> = 1.3 Adc, I <sub>B1</sub> = 130 mAdc, V <sub>CC</sub> = 300 V)	1.0 μs	(T <sub>C</sub> = 125°C)	–	5.5	–	V
Determined 1.0 μs and 3.0 μs respectively after rising I <sub>B1</sub> reaches 90% of final I <sub>B1</sub> (see Figure 18)			3.0 μs	(T <sub>C</sub> = 125°C)	–	3.0	–	–
		(I <sub>C</sub> = 3.0 Adc, I <sub>B1</sub> = 0.6 Adc, V <sub>CC</sub> = 300 V)	1.0 μs	(T <sub>C</sub> = 125°C)	–	9.5	–	–
			3.0 μs	(T <sub>C</sub> = 125°C)	–	14.5	–	–
						–	2.0	–
					–	7.5	–	–

## SWITCHING CHARACTERISTICS: Resistive Load (D.C. ≤ 10%, Pulse Width = 20 μs)

Turn–On Time	(I <sub>C</sub> = 3.0 Adc, I <sub>B1</sub> = 0.6 Adc, I <sub>B2</sub> = 1.5 Adc, V <sub>CC</sub> = 300 V)	(T <sub>C</sub> = 125°C)	t <sub>on</sub>	–	90 100	180 –	ns
Turn–Off Time			t <sub>off</sub>	–	1.7 2.1	2.5 –	μs
Turn–On Time	(I <sub>C</sub> = 1.3 Adc, I <sub>B1</sub> = 0.13 Adc, I <sub>B2</sub> = 0.65 Adc, V <sub>CC</sub> = 300 V)	(T <sub>C</sub> = 125°C)	t <sub>on</sub>	–	200 130	300 –	ns
Turn–Off Time			t <sub>off</sub>	–	1.2 1.5	2.5 –	μs

## SWITCHING CHARACTERISTICS: Inductive Load (V<sub>clamp</sub> = 300 V, V<sub>CC</sub> = 15 V, L = 200 μH)

Fall Time	(I <sub>C</sub> = 1.5 Adc, I <sub>B1</sub> = 0.13 Adc, I <sub>B2</sub> = 0.65 Adc)	(T <sub>C</sub> = 125°C)	t <sub>fi</sub>	–	100 120	180 –	ns
Storage Time			t <sub>si</sub>	–	1.5 1.9	2.5 –	μs
Crossover Time			t <sub>c</sub>	–	220 230	350 –	ns
Fall Time	(I <sub>C</sub> = 3.0 Adc, I <sub>B1</sub> = 0.6 Adc, I <sub>B2</sub> = 1.5 Adc)	(T <sub>C</sub> = 125°C)	t <sub>fi</sub>	–	85 120	150 –	ns
Storage Time			t <sub>si</sub>	–	2.15 2.75	3.2 –	μs
Crossover Time			t <sub>c</sub>	–	200 310	300 –	ns

2. Proper strike and creepage distance must be provided.

TYPICAL STATIC CHARACTERISTICS

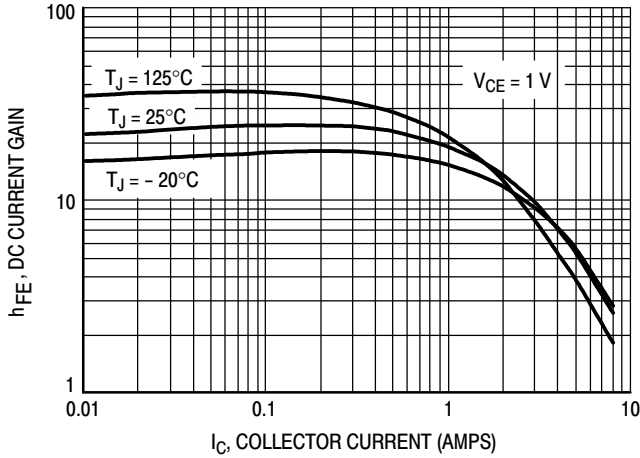


Figure 1. DC Current Gain @ 1 Volt

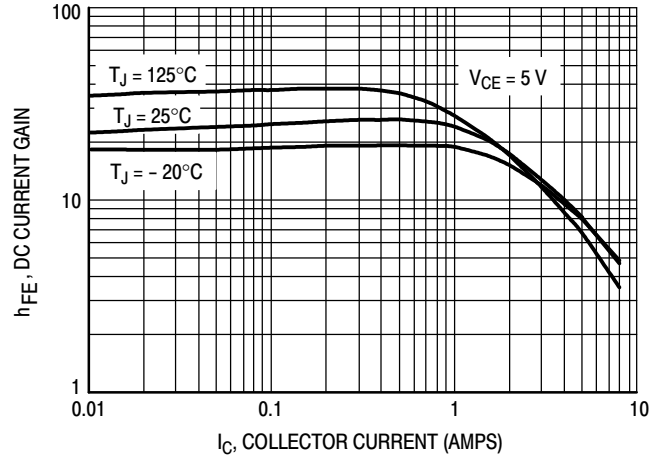


Figure 2. DC Current Gain @ 5 Volts

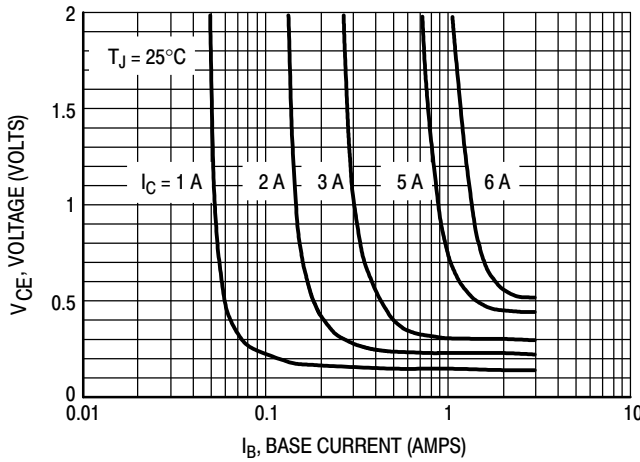


Figure 3. Collector Saturation Region

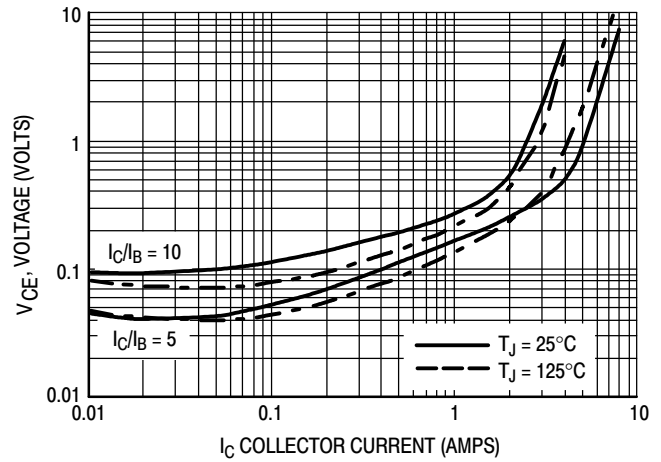


Figure 4. Collector-Emitter Saturation Voltage

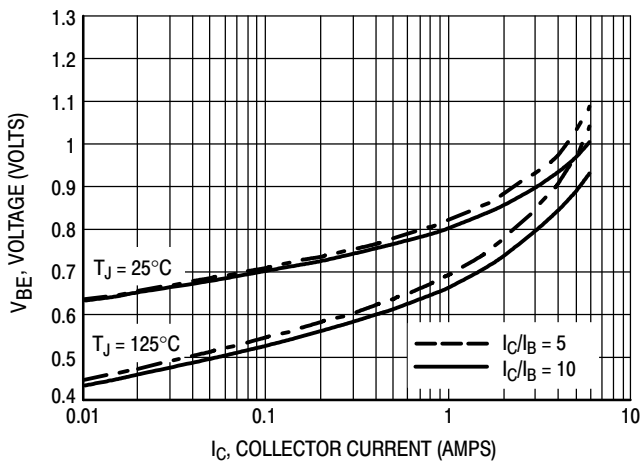


Figure 5. Base-Emitter Saturation Region

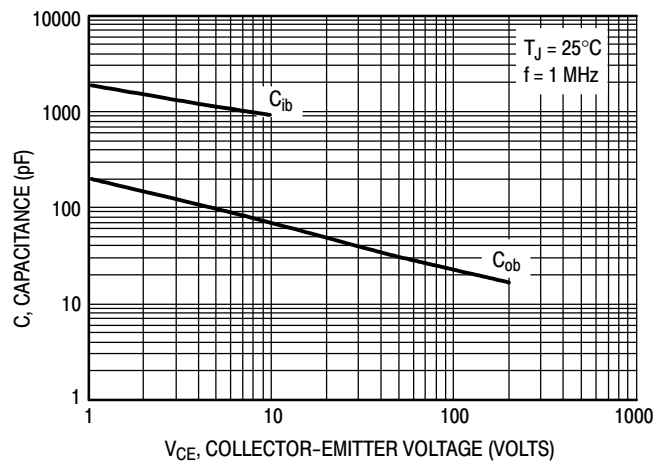


Figure 6. Capacitance

TYPICAL SWITCHING CHARACTERISTICS  
( $I_{B2} = I_C/2$  for all switching)

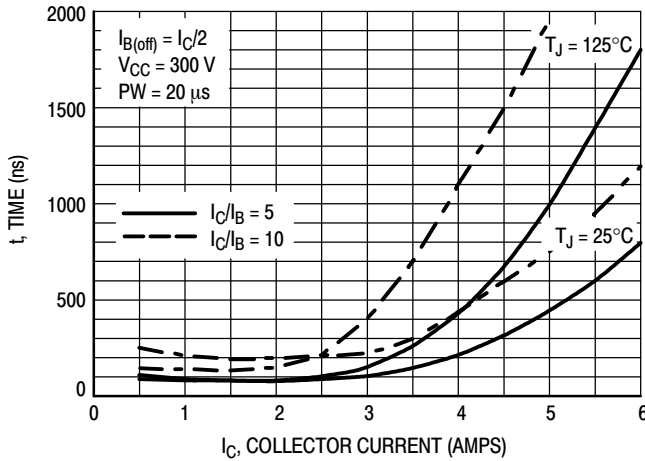


Figure 7. Resistive Switching,  $t_{on}$

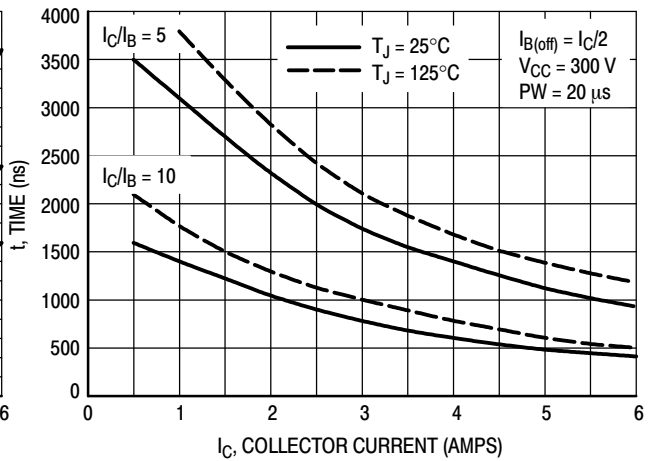


Figure 8. Resistive Switching,  $t_{off}$

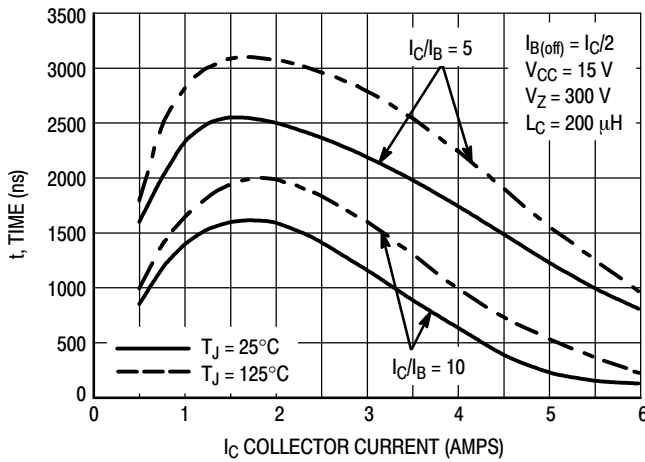


Figure 9. Inductive Storage Time,  $t_{si}$

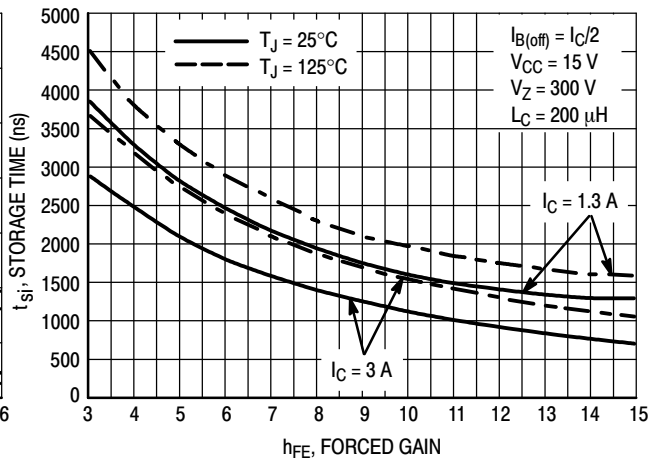


Figure 10. Inductive Storage Time,  $t_{si}(h_{FE})$

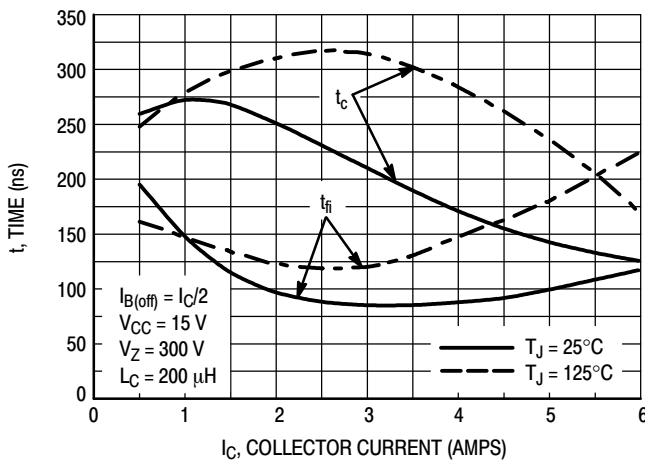


Figure 11. Inductive Switching,  $t_c$  and  $t_{fi}$   
 $I_C/I_B = 5$

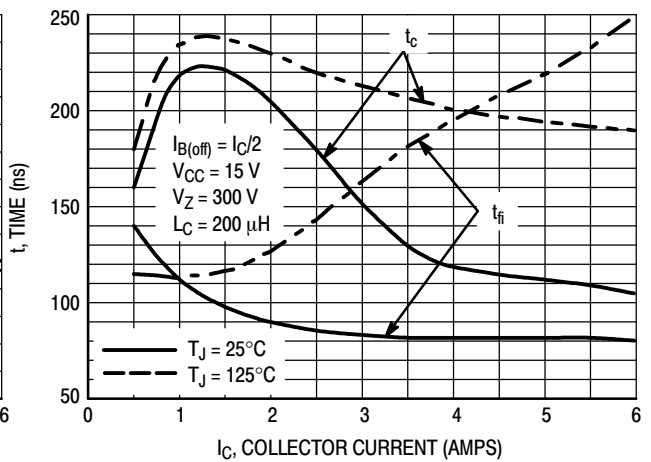


Figure 12. Inductive Switching,  $t_c$  and  $t_{fi}$   
 $I_C/I_B = 10$

**TYPICAL SWITCHING CHARACTERISTICS**  
( $I_{B2} = I_C/2$  for all switching)

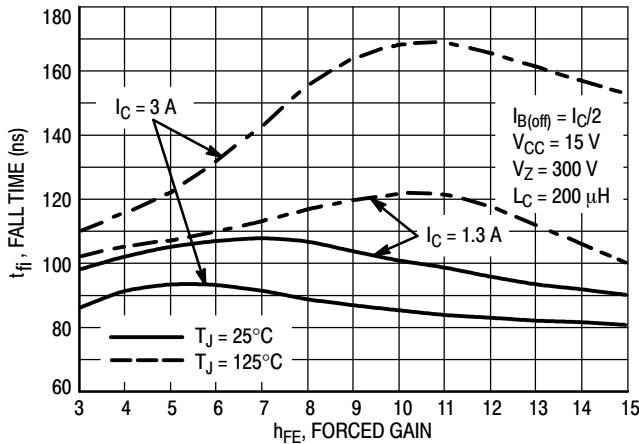


Figure 13. Inductive Fall Time

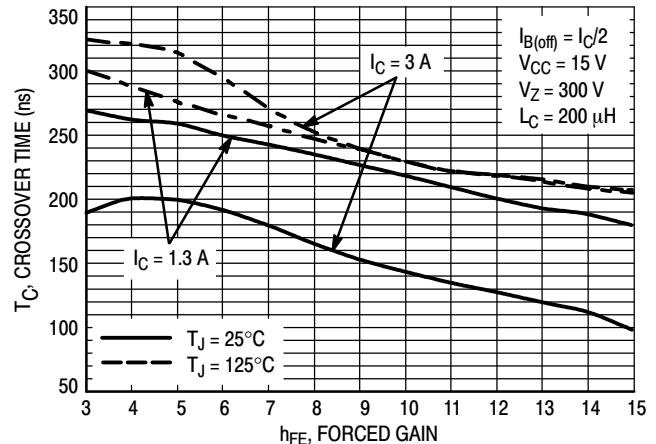


Figure 14. Inductive Crossover Time

**GUARANTEED SAFE OPERATING AREA INFORMATION**

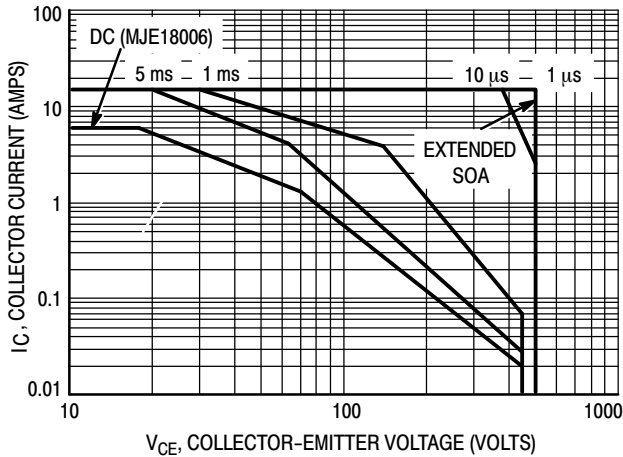


Figure 15. Forward Bias Safe Operating Area

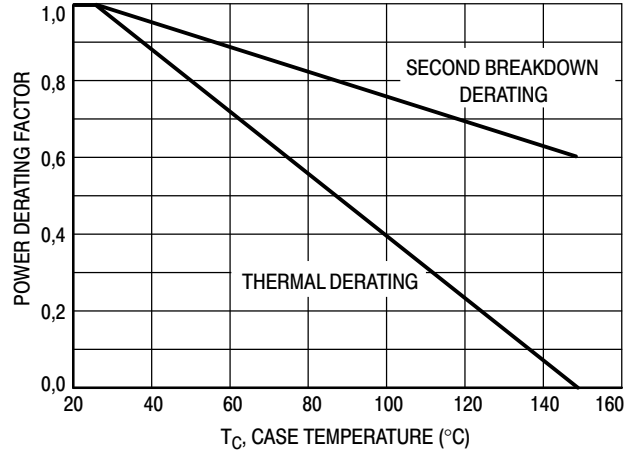


Figure 17. Forward Bias Power Derating

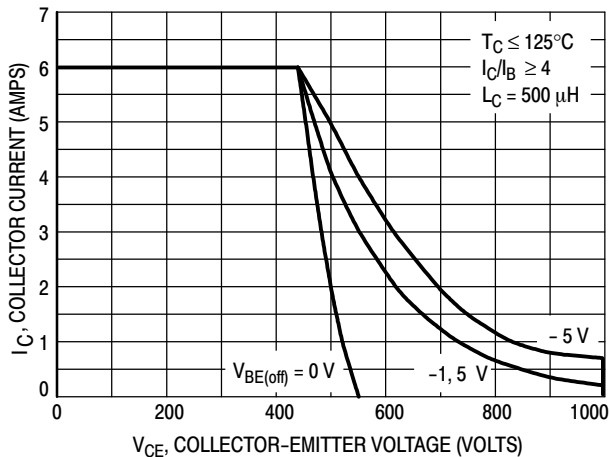


Figure 16. Reverse Bias Switching Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable

operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 15 is based on  $T_C = 25^\circ\text{C}$ ;  $T_{J(pk)}$  is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when  $T_C \geq 25^\circ\text{C}$ . Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown in Figure 15 may be found at any case temperature by using the appropriate curve on Figure 17.  $T_{J(pk)}$  may be calculated from the data in Figure 20. At any case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. For inductive loads, high voltage and current must be sustained simultaneously during turn-off with the base-to-emitter junction reverse-biased. The safe level is specified as a reverse-biased safe operating area (Figure 16). This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode.

# MJE18006G

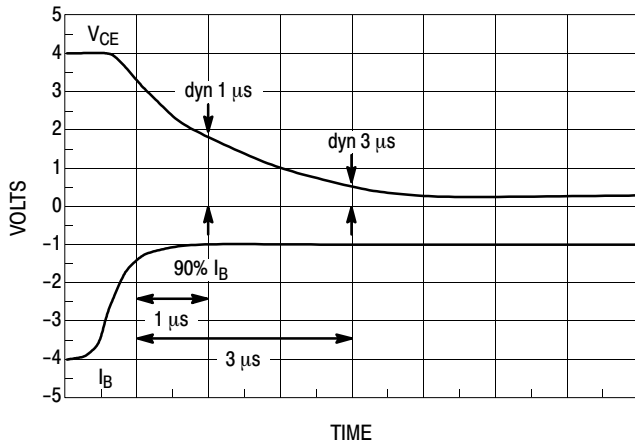


Figure 18. Dynamic Saturation Voltage Measurements

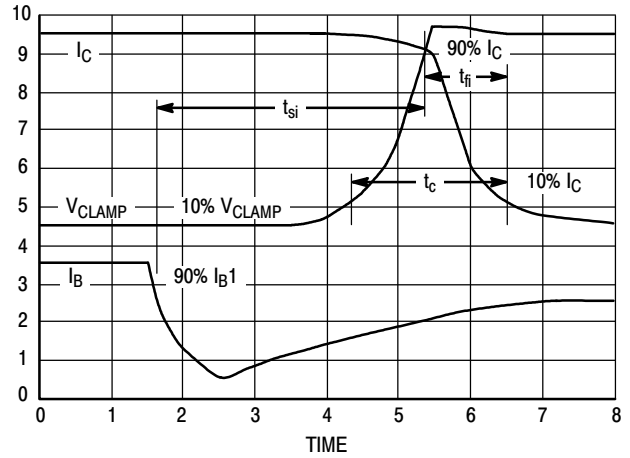
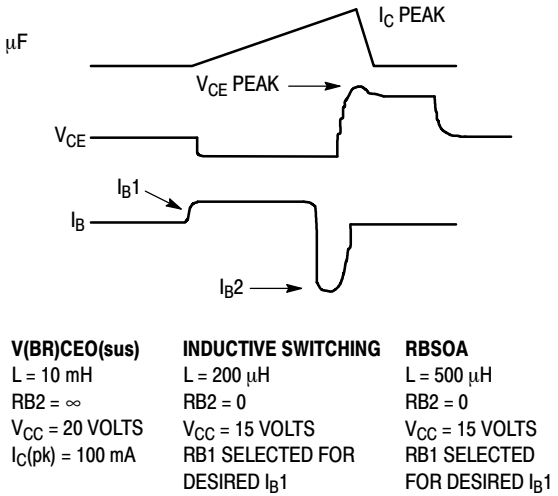
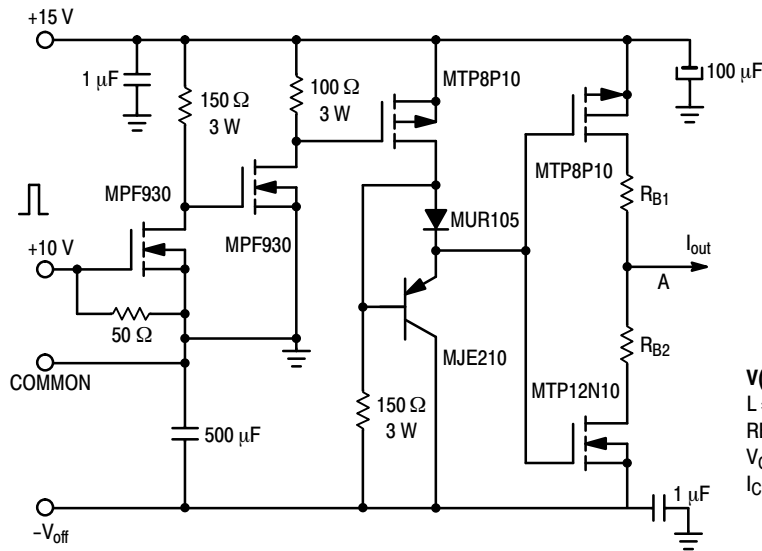


Figure 19. Inductive Switching Measurements



V(BR)CEO(sus)	INDUCTIVE SWITCHING	RBSOA
L = 10 mH	L = 200 μH	L = 500 μH
RB2 = ∞	RB2 = 0	RB2 = 0
V <sub>CC</sub> = 20 VOLTS	V <sub>CC</sub> = 15 VOLTS	V <sub>CC</sub> = 15 VOLTS
I <sub>C(pk)</sub> = 100 mA	RB1 SELECTED FOR DESIRED I <sub>B1</sub>	RB1 SELECTED FOR DESIRED I <sub>B1</sub>

Table 1. Inductive Load Switching Drive Circuit

## TYPICAL THERMAL RESPONSE

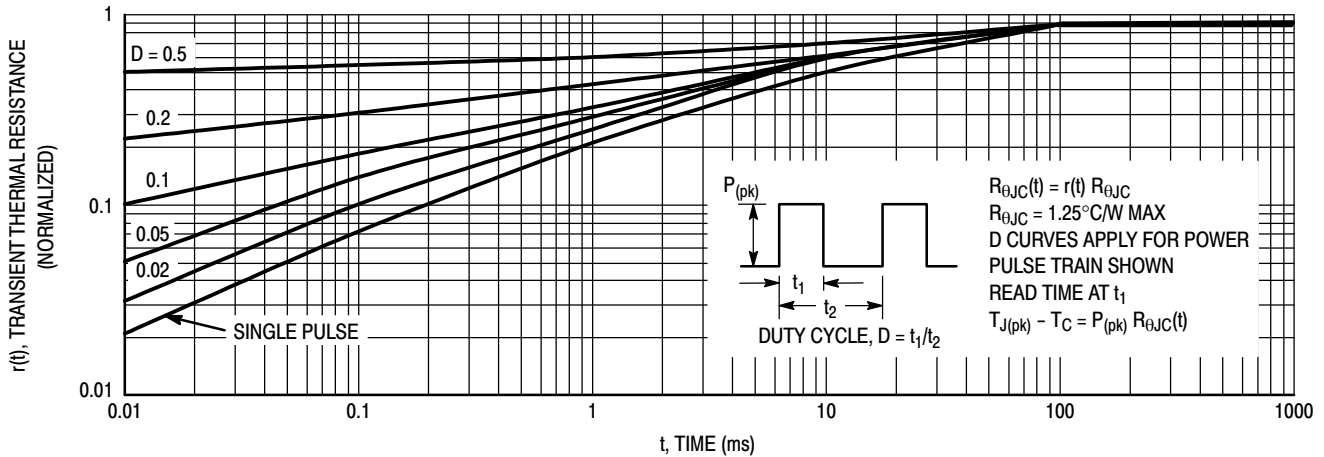
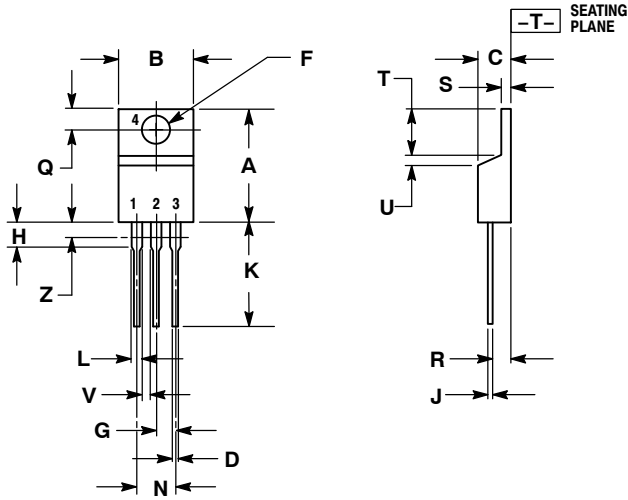


Figure 20. Typical Thermal Response ( $Z_{\theta JC}(t)$ ) for MJE18006

# MJE18006G

## PACKAGE DIMENSIONS

TO-220AB  
CASE 221A-09  
ISSUE AF



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
H	0.110	0.155	2.80	3.93
J	0.014	0.025	0.36	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

STYLE 1:

1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

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