THE DATASHEET OF

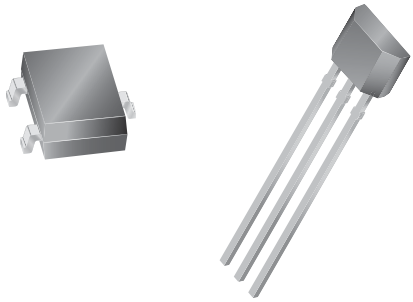
A1302ELHLT-T

Continuous-Time Ratiometric Linear Hall Effect Sensor ICs

Features and Benefits

- Low-noise output
- Fast power-on time
- Ratiometric rail-to-rail output
- 4.5 to 6.0 V operation
- Solid-state reliability
- Factory-programmed at end-of-line for optimum performance
- Robust ESD performance

Packages: 3 pin SOT23W (suffix LH), and 3 pin SIP (suffix UA)



Not to scale

Description

The A1301 and A1302 are continuous-time, ratiometric, linear Hall-effect sensor ICs. They are optimized to accurately provide a voltage output that is proportional to an applied magnetic field. These devices have a quiescent output voltage that is 50% of the supply voltage. Two output sensitivity options are provided: 2.5 mV/G typical for the A1301, and 1.3 mV/G typical for the A1302.

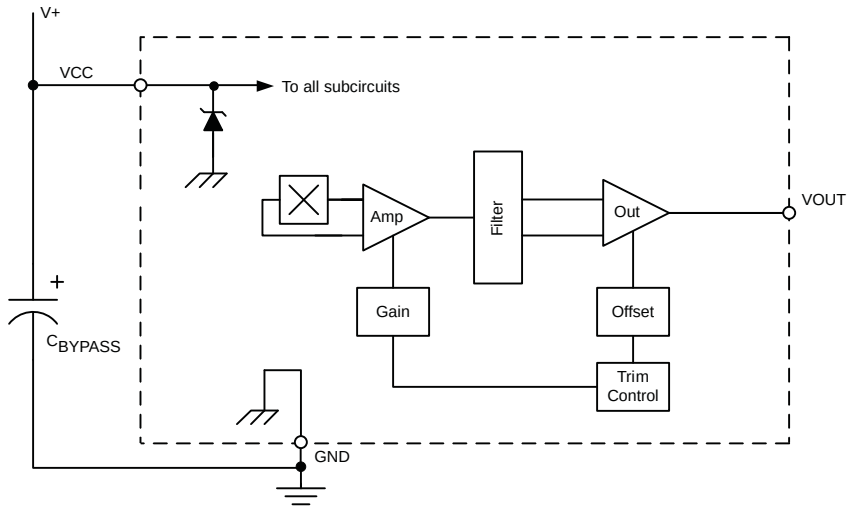
The Hall-effect integrated circuit included in each device includes a Hall circuit, a linear amplifier, and a CMOS Class A output structure. Integrating the Hall circuit and the amplifier on a single chip minimizes many of the problems normally associated with low voltage level analog signals.

High precision in output levels is obtained by internal gain and offset trim adjustments made at end-of-line during the manufacturing process.

These features make the A1301 and A1302 ideal for use in position sensing systems, for both linear target motion and rotational target motion. They are well-suited for industrial applications over extended temperature ranges, from –40°C to 125°C.

Two device package types are available: LH, a 3-pin SOT23W type for surface mount, and UA, a 3-pin ultramini SIP for through-hole mount. They are lead (Pb) free (suffix, –*T*) with 100% matte tin plated leadframes.

Functional Block Diagram



Selection Guide				
Part Number	Packing[1]	Package	Ambient, T_A	Sensitivity (Typical)
~~A1301ELHLT-T~~[2]	7-in. tape and reel, 3000 pieces/reel	Surface Mount	–40ºC to 85ºC	2.5 mV/G
A1301EUA-T	Bulk, 500 pieces/bag	SIP		
A1301KLHLT-T	7-in. tape and reel, 3000 pieces/reel	Surface Mount	–40ºC to 125ºC	
A1301KUA-T	Bulk, 500 pieces/bag	SIP		
A1302ELHLT-T	7-in. tape and reel, 3000 pieces/reel	Surface Mount	–40ºC to 85ºC	1.3 mV/G
~~A1302EUA-T~~[3]	Bulk, 500 pieces/bag	SIP		
A1302KLHLT-T	7-in. tape and reel, 3000 pieces/reel	Surface Mount	–40ºC to 125ºC	
A1302KUA-T	Bulk, 500 pieces/bag	SIP		

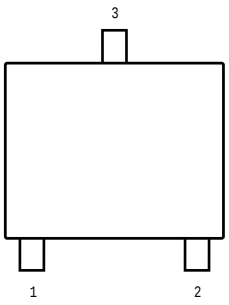
[1]Contact Allegro for additional packing options.
[2]Variant is in production but has been determined to be LAST TIME BUY. This classification indicates that the variant is obsolete and notice has been given. Sale of the variant is currently restricted to existing customer applications. The variant should not be purchased for new design applications because of obsolescence in the near future. Samples are no longer available. Status date change May 4, 2009. Deadline for receipt of LAST TIME BUY orders is November 4, 2009.
[3]Variant is in production but has been determined to be NOT FOR NEW DESIGN. This classification indicates that sale of the variant is currently restricted to existing customer applications. The variant should not be purchased for new design applications because obsolescence in the near future is probable. Samples are no longer available. Status change: May 4, 2009.

Absolute Maximum Ratings

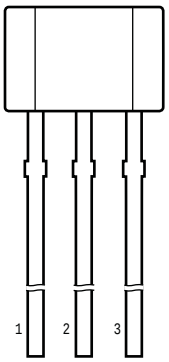
Characteristic	Symbol	Notes	Rating	Units
Supply Voltage	V_{CC}		8	V
Output Voltage	V_{OUT}		8	V
Reverse Supply Voltage	V_{RCC}		–0.1	V
Reverse Supply Voltage	V_{RCC}		–0.1	V
Output Sink Current	I_{OUT}		10	mA
Operating Ambient Temperature	T_A	Range E	–40 to 85	ºC
		Range K	–40 to 125	ºC
Maximum Junction Temperature	T_J(max)		165	ºC
Storage Temperature	T_{stg}		–65 to 170	ºC

Pin-out Drawings

Package LH



Package UA



Terminal List

Symbol	Number		Description
	Package LH	Package UA	
VCC	1	1	Connects power supply to chip
VOUT	2	3	Output from circuit
GND	3	2	Ground

DEVICE CHARACTERISTICS over operating temperature range, T_A, and V_{CC} = 5 V, unless otherwise noted

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Electrical Characteristics						
Supply Voltage	V_{CC}	Running, T_J < 165°C	4.5	–	6	V
Supply Current	I_{CC}	Output open	–	–	11	mA
Output Voltage	$V_{OUT(High)}$	I_{SOURCE} = –1 mA, Sens = nominal	4.65	4.7	–	V
	$V_{OUT(Low)}$	I_{SINK} = 1 mA, Sens = nominal	–	0.2	0.25	V
Output Bandwidth	BW		–	20	–	kHz
Power-On Time	t_{PO}	$V_{CC(min)}$ to 0.95 $V_{OUT;}$ B = ±1400 G; Slew rate = 4.5 V/µs to 4.5 V/100 ns	–	3	5	µs
Output Resistance	R_{OUT}	I_{SINK} ≤ 1 mA, I_{SOURCE} ≥ –1 mA	–	2	5	Ω
Wide Band Output Noise, rms	V_{OUTN}	External output low pass filter ≤ 10 kHz; Sens = nominal	–	150	–	µV
Ratiometry						
Quiescent Output Voltage Error with respect to ΔV_{CC}[1]	$\Delta V_{OUTQ(V)}$	T_A = 25°C	–	–	±3.0	%
Magnetic Sensitivity Error with respect to ΔV_{CC}[2]	$\Delta Sens_{(V)}$	T_A = 25°C	–	–	±3.0	%
Output						
Linearity	Lin	T_A = 25°C	–	–	±2.5	%
Symmetry	Sym	T_A = 25°C	–	–	±3.0	%
Magnetic Characteristics						
Quiescent Output Voltage	V_{OUTQ}	B = 0 G; T_A = 25°C	2.4	2.5	2.6	V
Quiescent Output Voltage over Operating Temperature Range	$V_{OUTQ(\Delta T_A)}$	B = 0 G	2.2	–	2.8	V
Magnetic Sensitivity	Sens	A1301; T_A = 25°C	2.0	2.5	3.0	mV/G
		A1302; T_A = 25°C	1.0	1.3	1.6	mV/G
Magnetic Sensitivity over Operating Temperature Range	$Sens_{(\Delta T_A)}$	A1301	1.8	–	3.2	mV/G
		A1302	0.85	–	1.75	mV/G

[1]Refer to equation (4) in Ratiometric section on page 4.
[2]Refer to equation (5) in Ratiometric section on page 4.

Characteristic Definitions

Quiescent Output Voltage. In the quiescent state (no significant magnetic field: B = 0), the output, V_{OUTQ}, equals one half of the supply voltage, V_{CC}, throughout the entire operating ranges of V_{CC} and ambient temperature, T_A. Due to internal component tolerances and thermal considerations, there is a tolerance on the quiescent output voltage, ΔV_{OUTQ}, which is a function of both ΔV_{CC} and ΔT_A. For purposes of specification, the quiescent output voltage as a function of temperature, $\Delta V_{OUTQ(\Delta T_A)}$, is defined as:

$$\Delta V_{OUTQ(\Delta T_A)} = \frac{V_{OUTQ(T_A)} - V_{OUTQ(25°C)}}{Sens_{(25°C)}} \quad (1)$$

where Sens is in mV/G, and the result is the device equivalent accuracy, in gauss (G), applicable over the entire operating temperature range.

Sensitivity. The presence of a south-polarity (+B) magnetic field, perpendicular to the branded face of the device package, increases the output voltage, V_{OUT}, in proportion to the magnetic field applied, from V_{OUTQ} toward the V_{CC} rail. Conversely, the application of a north polarity (–B) magnetic field, in the same orientation, proportionally decreases the output voltage from its quiescent value. This proportionality is specified as the magnetic sensitivity of the device and is defined as:

$$Sens = \frac{V_{OUT(-B)} - V_{OUT(+B)}}{2B} \quad (2)$$

The stability of the device magnetic sensitivity as a function of ambient temperature, $\Delta Sens_{(\Delta T_A)}$ (%) is defined as:

$$\Delta Sens_{(\Delta T_A)} = \frac{Sens_{(T_A)} - Sens_{(25°C)}}{Sens_{(25°C)}} \times 100\% \quad (3)$$

Ratiometric. The A1301 and A1302 feature a ratiometric output. This means that the quiescent voltage output, V_{OUTQ}, and the magnetic sensitivity, Sens, are proportional to the supply voltage, V_{CC}.

The ratiometric change (%) in the quiescent voltage output is defined as:

$$\Delta V_{OUTQ(\Delta V)} = \frac{V_{OUTQ(V_{CC})} \,/\, V_{OUTQ(5V)}}{V_{CC} \,/\, 5\ V} \times 100\% \quad (4)$$

and the ratiometric change (%) in sensitivity is defined as:

$$\Delta Sens_{(\Delta V)} = \frac{Sens_{(V_{CC})} \,/\, Sens_{(5V)}}{V_{CC} \,/\, 5\ V} \times 100\% \quad (5)$$

Linearity and Symmetry. The on-chip output stage is designed to provide linear output at a supply voltage of 5 V. Although the application of very high magnetic fields does not damage these devices, it does force their output into a nonlinear region. Linearity in percent is measured and defined as:

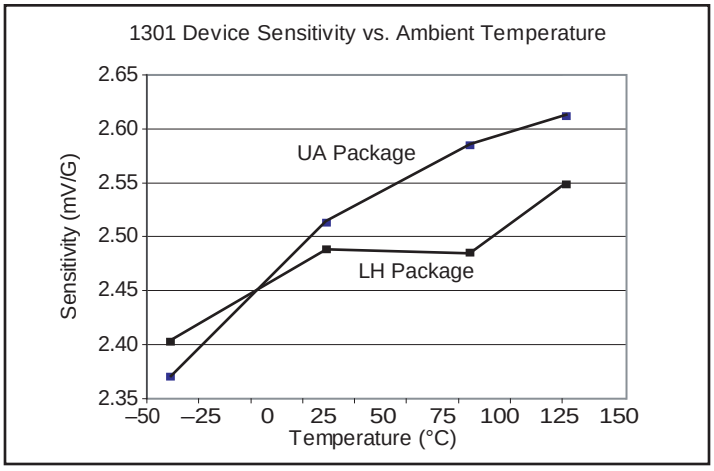
$$Lin+ = \frac{V_{OUT(+B)} - V_{OUTQ}}{2\,(V_{OUT(+B½)} - V_{VOUTQ})} \times 100\% \quad (6)$$

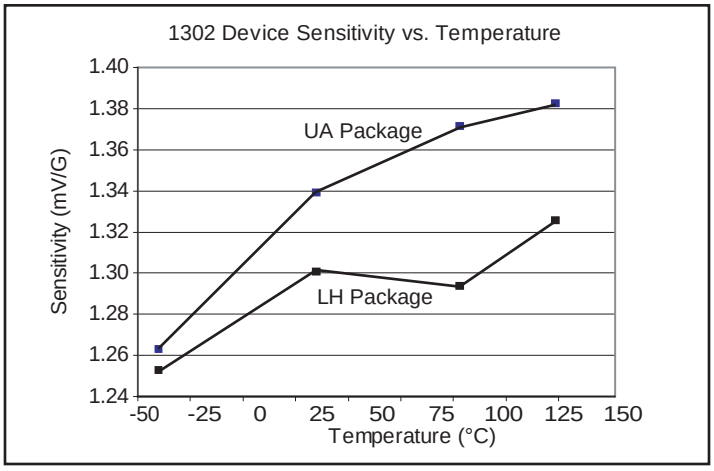
$$Lin- = \frac{V_{OUT(-B)} - V_{OUTQ}}{2(V_{OUT(-B½)} - V_{OUTQ})} \times 100\% \quad (7)$$

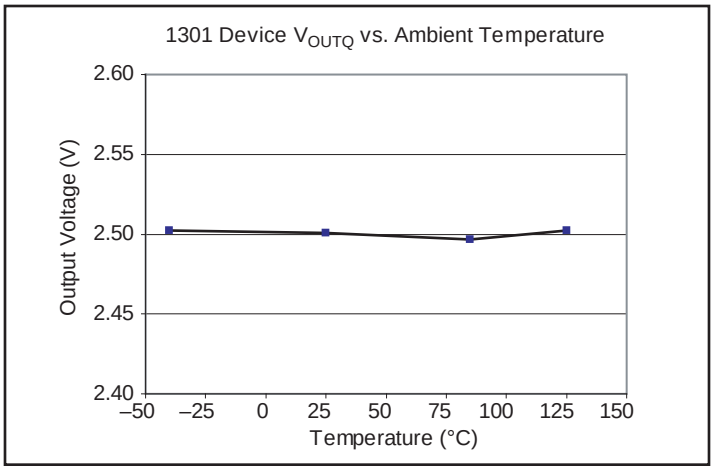
and output symmetry as:

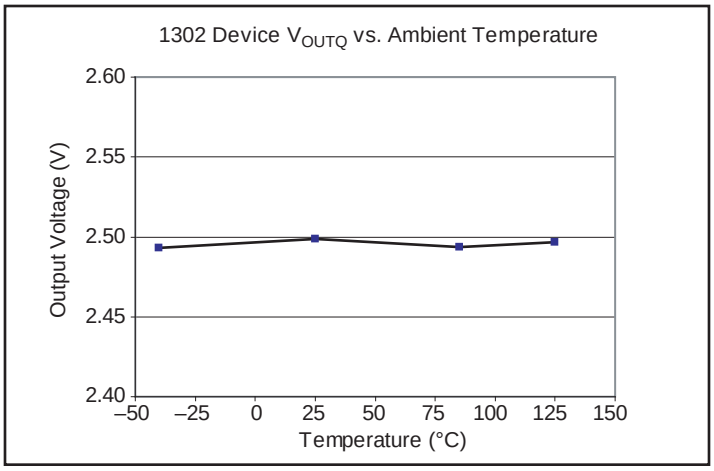
$$Sym = \frac{V_{OUT(+B)} - V_{OUTQ}}{V_{OUTQ} - V_{OUT(-B)}} \times 100\% \quad (8)$$

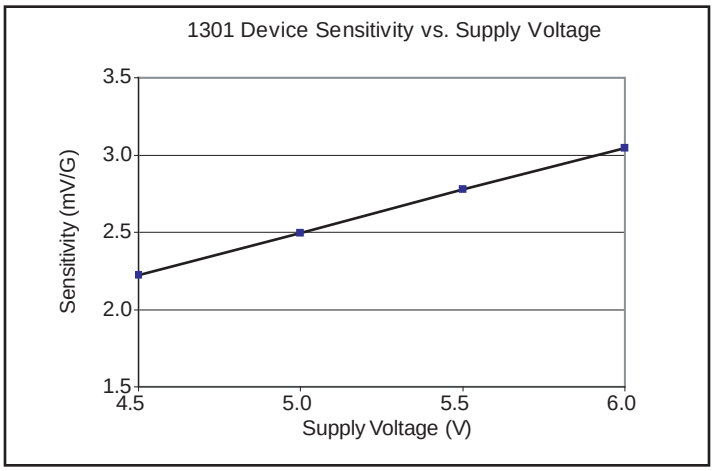
Typical Characteristics
(30 pieces, 3 fabrication lots)

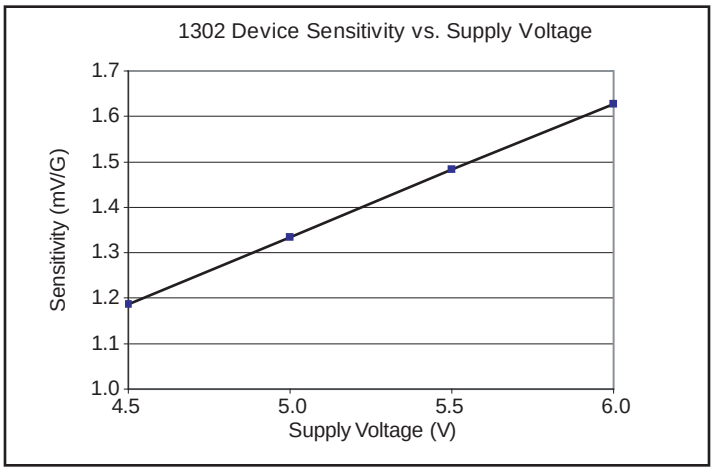












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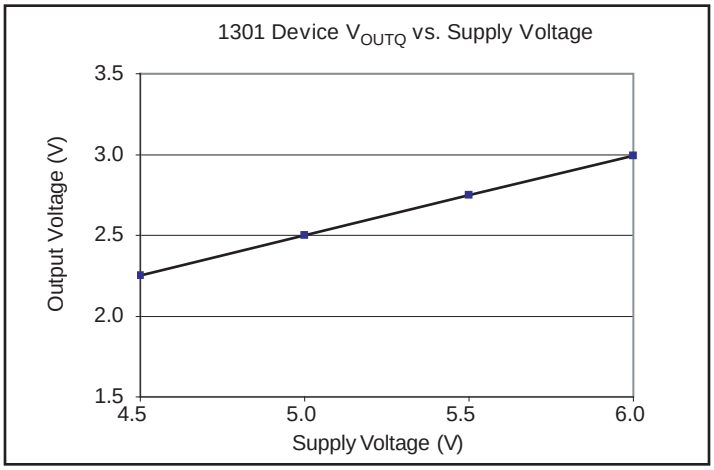
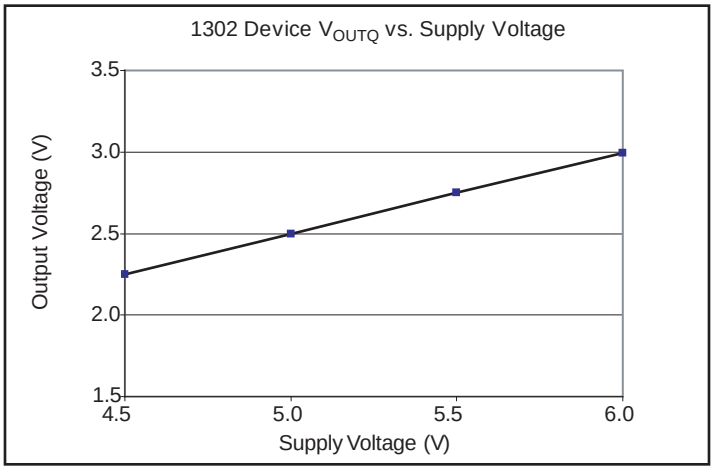
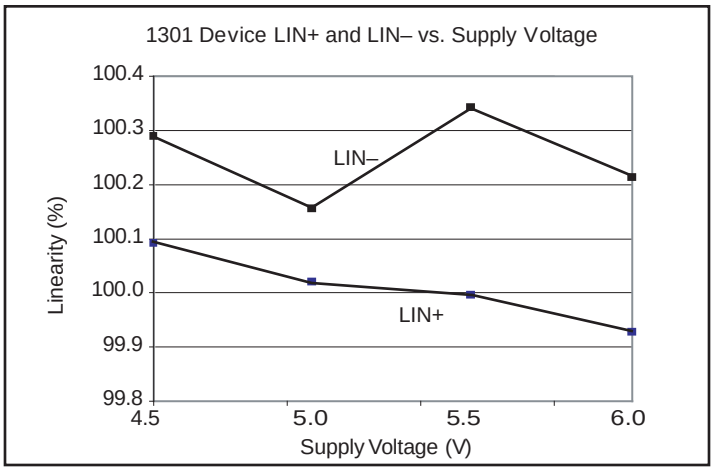
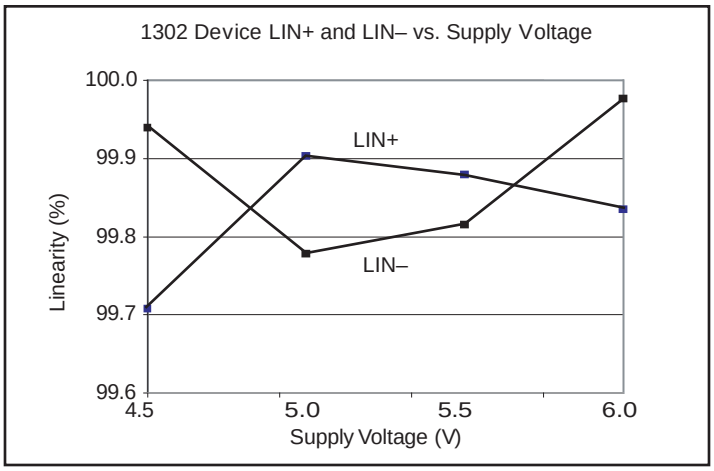
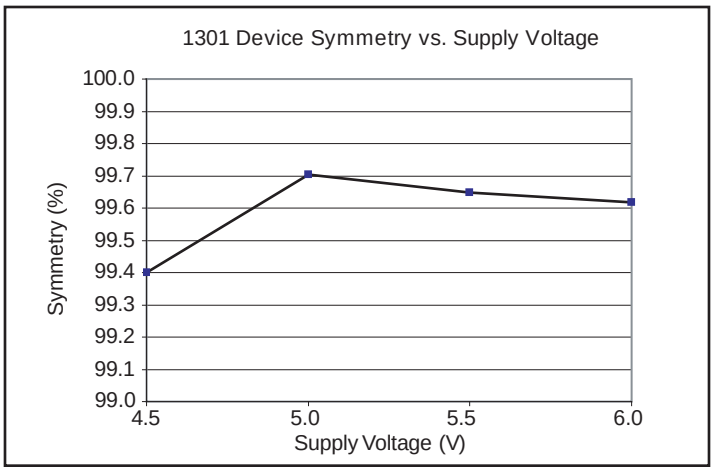
Typical Characteristics, continued
(30 pieces, 3 fabrication lots)

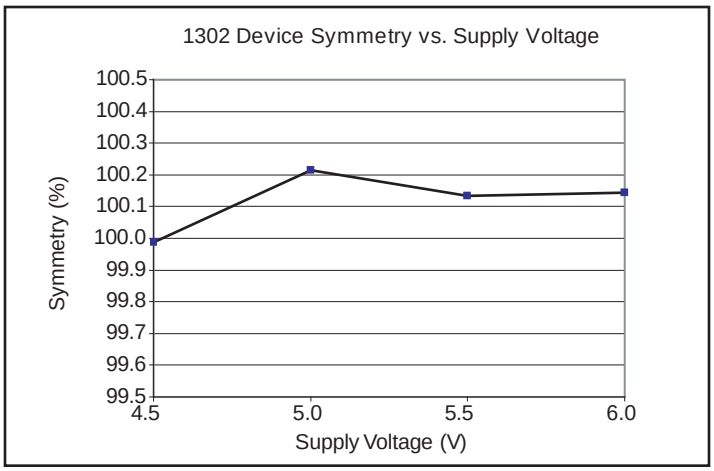




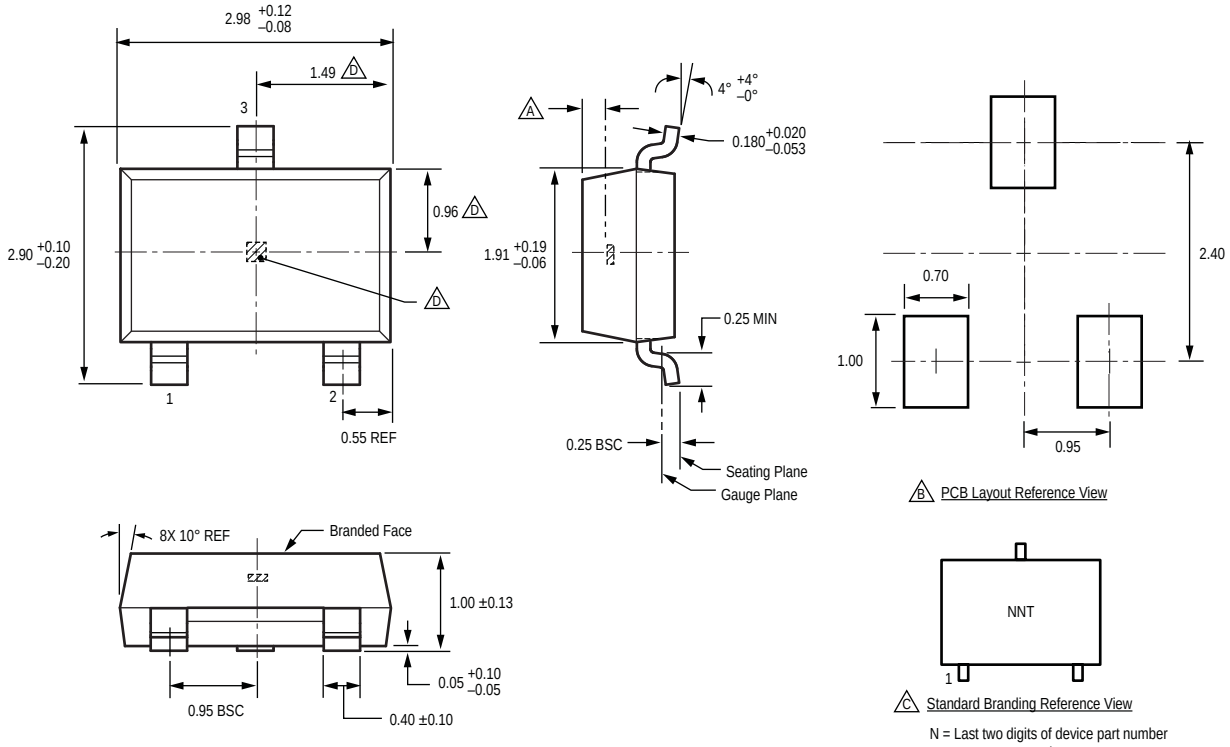








Package LH, 3-Pin; (SOT-23W)



For Reference Only; not for tooling use (reference dwg. 802840)
Dimensions in millimeters
Dimensions exclusive of mold flash, gate burrs, and dambar protrusions
Exact case and lead configuration at supplier discretion within limits shown

A Active Area Depth, 0.28 mm REF

B Reference land pattern layout
All pads a minimum of 0.20 mm from all adjacent pads; adjust as necessary to meet application process requirements and PCB layout tolerances

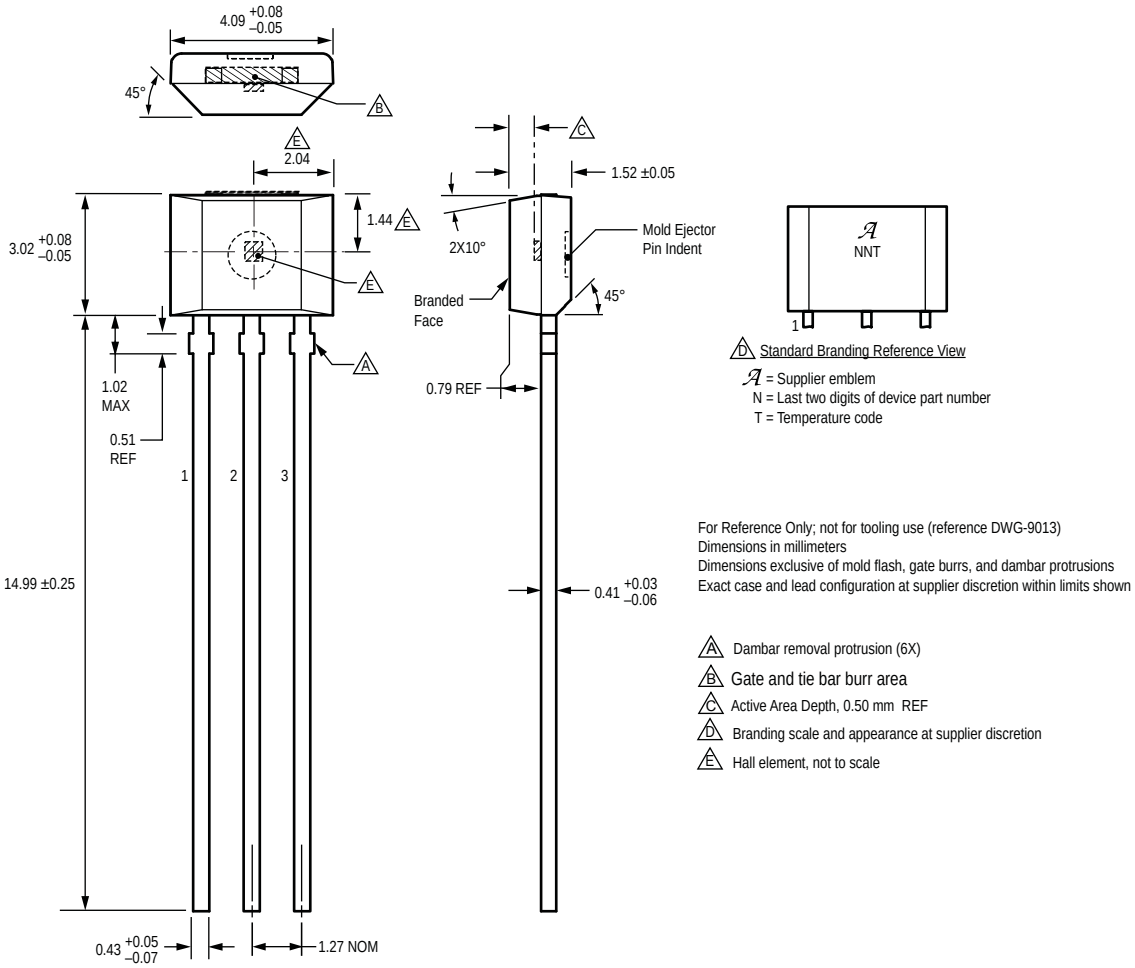
C Branding scale and appearance at supplier discretion

D Hall element, not to scale

N = Last two digits of device part number
T = Temperature code

Package UA, 3-Pin SIP

Matrix Leadframe



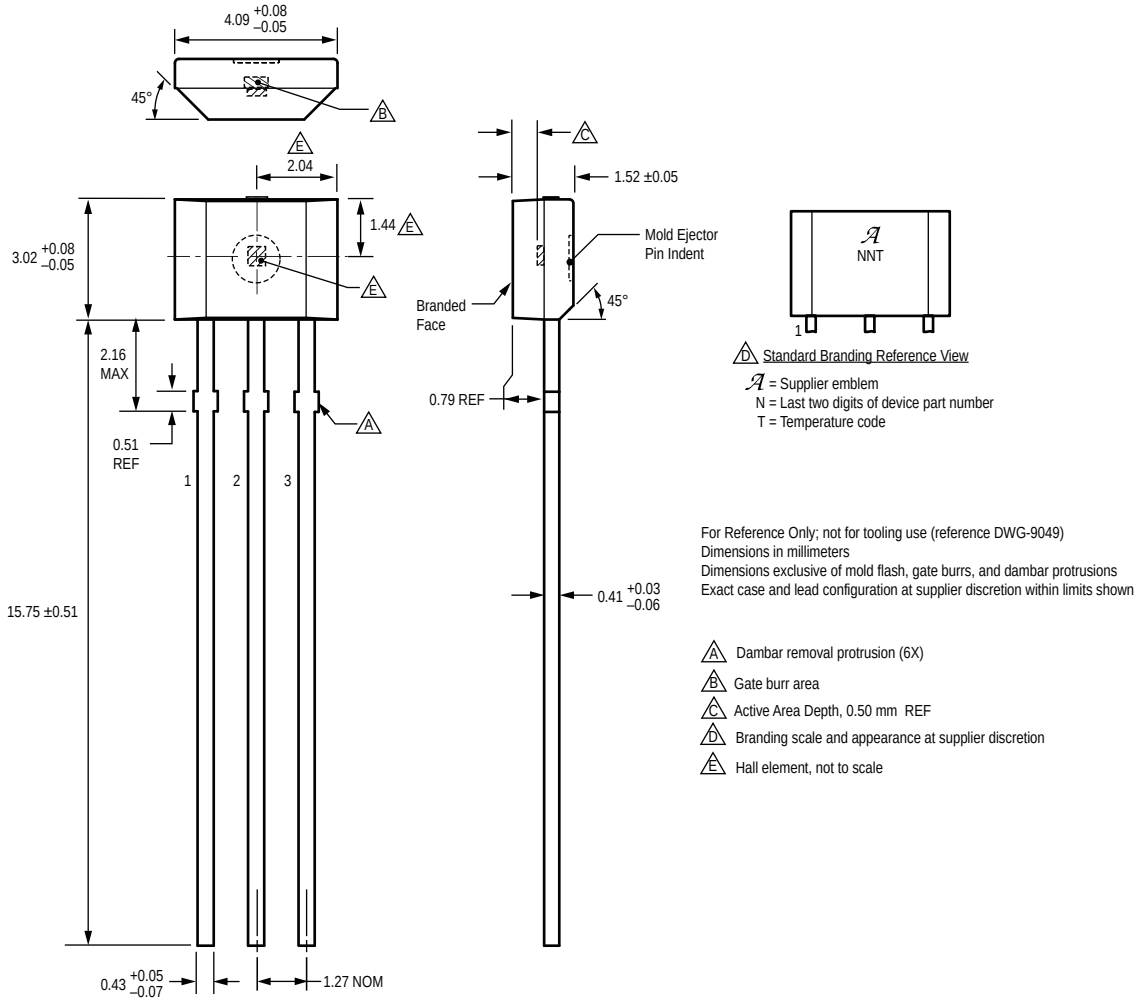
For Reference Only; not for tooling use (reference DWG-9013)
Dimensions in millimeters
Dimensions exclusive of mold flash, gate burrs, and dambar protrusions
Exact case and lead configuration at supplier discretion within limits shown

A Dambar removal protrusion (6X)
B Gate and tie bar burr area
C Active Area Depth, 0.50 mm REF
D Branding scale and appearance at supplier discretion
E Hall element, not to scale

Please note that there are changes to the existing UA package drawing pending. Please contact the Allegro Marketing department for additional information.

Package UA, 3-Pin SIP

Conventional Leadframe



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