



**THE DATASHEET OF  
MCIMX6G2CVM05AB**





Power-efficient  
and secure solutions

# i.MX 6UltraLite Applications Processors

The high-performance, ultra-efficient i.MX 6UltraLite processor features an advanced implementation of a single Arm® Cortex® core that operates at speeds up to 696 MHz.

## TARGET APPLICATIONS

- ▶ Automotive telematics
- ▶ Human-machine interface (HMI)
- ▶ IoT gateways
- ▶ Home energy management systems
- ▶ Smart energy concentrators
- ▶ Intelligent industrial control systems
- ▶ Portable medical
- ▶ Electronics point-of-sale devices
- ▶ Printers and 2D scanners
- ▶ Smart appliances
- ▶ Financial payment systems

The i.MX 6UltraLite applications processor includes an integrated power management module that reduces the complexity of an external power supply and simplifies power sequencing. Each processor in this family provides various memory interfaces. These interfaces include 16-bit LPDDR2, DDR3, DDR3L, raw and managed NAND flash, NOR flash, eMMC, Quad SPI NOR, SD and a wide range of other interfaces for connecting peripherals such as WLAN, Bluetooth®, GPS, displays and camera sensors. The i.MX 6UltraLite is supported by discrete component power circuitry.

## i.MX 6ULTRALITE FEATURES

- ▶ Single Arm Cortex-A7 core can provide a more cost-effective and more power-efficient solution
- ▶ Flexible boot options, including support for Quad SPI and raw NAND, and a memory controller that interfaces to both DDR3 and low power mobile DDR2 memory
- ▶ Hardware-enabled security features that enable secure e-commerce, digital rights management (DRM), information encryption, on-the-fly DRAM encryption, secure boot and secure software downloads
- ▶ Processor supports connections to a variety of interfaces: two high-speed USB on-the-go with PHY, multiple expansion card ports (high-speed eMMC/SDIO host and other), two 12-bit ADC modules with up to 10 total input channels, two CAN ports, two smart card interfaces compatible with EMV Standard v4.3 and a variety of other popular interfaces (such as UART, I<sup>2</sup>C, and I<sup>2</sup>S serial audio)



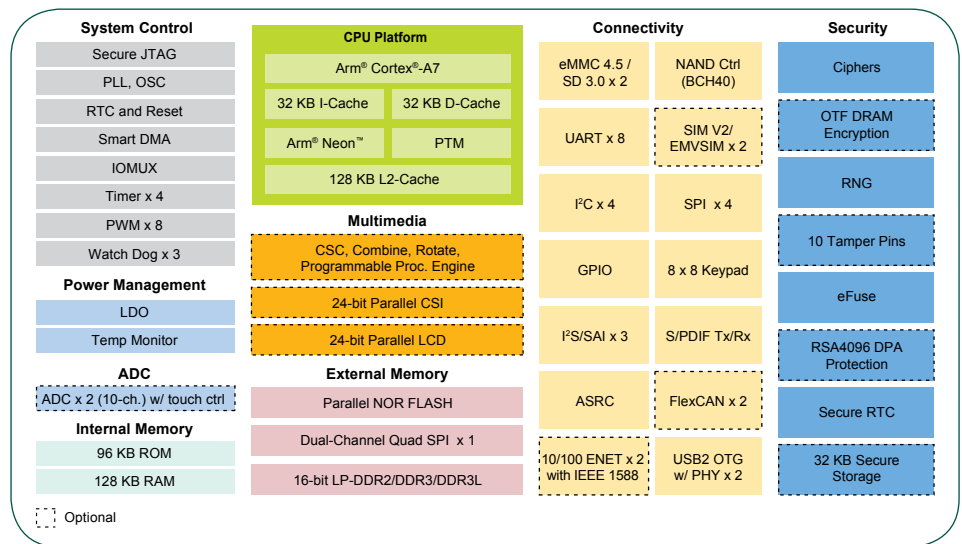
## PACKAGE TECHNOLOGY

The i.MX 6UltraLite processor provides multiple compatible and scalable package options. The 14 x 14 289 MAPBGA with 0.8 mm pitch brings out all features and GPIOs. It is ideal for simple and low-cost PCB design. The 9 x 9 272 MAPBGA with 0.5 mm pitch provides smaller form factors than ever before for space-constrained applications.

## SOFTWARE AND TOOLS

The i.MX 6UltraLite processor is supported by the i.MX 6UltraLite evaluation kit that includes a CPU module and a base board.

## i.MX 6ULTRALITE APPLICATIONS PROCESSOR BLOCK DIAGRAM



## i.MX 6ULTRALITE DEVICE OPTIONS

- Red indicates change from column to the left

Feature	MCIMX6G0	MCIMX6G1	MCIMX6G2	MCIMX6G3
Speed	528 MHz	528 MHz, 696 MHz	528 MHz, 696 MHz	528 MHz
Cache	32 KB-I, 32 KB-D	32 KB-I, 32 KB-D 128 KB L2	32 KB-I, 32 KB-D 128 KB L2	32 KB-I, 32 KB-D 128 KB L2
OCRAM	128 KB	128 KB	128 KB	128 KB
DRAM	16-bit LP-DDR2, DDR3/DDR3L	16-bit LP-DDR2, DDR3/DDR3L	16-bit LP-DDR2, DDR3/DDR3L	16-bit LP-DDR2, DDR3/DDR3L
eFuse for Customer	512-bit	1024-bit	1536-bit	2048-bit
NAND (BCH40)	Yes	Yes	Yes	Yes
Parallel NOR/EBI	Yes	Yes	Yes	Yes
Ethernet	10/100 MB x 1	10/100 MB x 1	10/100 MB x 2	10/100 MB x 2
USB with PHY	OTG, HS/FS x 1	OTG, HS/FS x 2	OTG, HS/FS x 2	OTG, HS/FS x 2
CAN	0	1	2	2
Security	Basic	TRNG, Crypto Engine (AES/TDES/SHA), Secure Boot	TRNG, Crypto Engine (AES/TDES/SHA), Secure Boot	TRNG, Crypto Engine (AES with DPA/TDES/SHA/RSA), Secure Boot, Tamper Monitor, PCI4.0 pre-certification, OTF DRAM Encryption
Graphic	None	None	PxP	PxP
CSI	None	None	24-bit Parallel CSI	24-bit Parallel CSI
LCD	None	None	24-bit Parallel LCD	24-bit Parallel LCD
Quad SPI	1	1	1	1
SDIO	2	2	2	2
UART	4	8	8	8
I²C	2	4	4	4
SPI	2	4	4	4
I²S/SAI	1	3	3	3
S/PDIF	1	1	1	1
Timer/PWM	Timer x 2, PWM x 4	Timer x 4, PWM x 8	Timer x 4, PWM x 8	Timer x 4, PWM x 8
12-bit ADC	1 x 10-ch.	1 x 10-ch.	2 x 10-ch.	2 x 10-ch.

[www.nxp.com/iMX6UltraLite](http://www.nxp.com/iMX6UltraLite)  
[imxcommunity.org](http://imxcommunity.org)

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