



**THE DATASHEET OF  
BLF642,112**



## 1. Product profile

### 1.1 General description

A 35 W LDMOS RF power transistor for broadcast transmitter and industrial applications. The transistor is suitable for the frequency range HF to 1400 MHz. The excellent ruggedness and broadband performance of this device makes it ideal for digital applications.

**Table 1. Typical performance**

*RF performance at  $T_h = 25\text{ °C}$  in a common source test circuit.*

Mode of operation	f (MHz)	V <sub>DS</sub> (V)	P <sub>L</sub> (W)	G <sub>p</sub> (dB)	η <sub>D</sub> (%)	IMD (dBc)
CW, class-AB	1300	32	35	19	63	-
2-tone, class-AB	1300	32	17.5	19	48	-28

### 1.2 Features and benefits

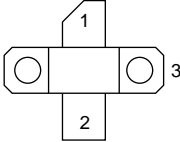
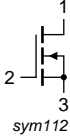
- CW performance at 1300 MHz, a drain-source voltage V<sub>DS</sub> of 32 V and a quiescent drain current I<sub>Dq</sub> = 0.2 A :
  - ◆ Average output power = 35 W
  - ◆ Power gain = 19 dB
  - ◆ Drain efficiency = 63 %
- 2-tone performance at 1300 MHz, a drain-source voltage V<sub>DS</sub> of 32 V and a quiescent drain current I<sub>Dq</sub> = 0.2 A :
  - ◆ Average output power = 17.5 W
  - ◆ Power gain = 19 dB
  - ◆ Drain efficiency = 48 %
  - ◆ Intermodulation distortion = -28 dBc
- Integrated ESD protection
- Excellent ruggedness
- High power gain
- High efficiency
- Excellent reliability
- Easy power control
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

### 1.3 Applications

- Communication transmitter applications in the HF to 1400 MHz frequency range
- Industrial applications in the HF to 1400 MHz frequency range

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	drain		 sym112
2	gate		
3	source		

[1] Connected to flange

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLF642	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT467C

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	65	V
$V_{GS}$	gate-source voltage		-0.5	+11	V
$T_{stg}$	storage temperature		-65	+150	°C
$T_j$	junction temperature		-	200	°C

## 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	$T_{case} = 80\text{ °C}; P_L = 35\text{ W}$	[1] 1.6	K/W

[1]  $R_{th(j-c)}$  is measured under RF conditions.

## 6. Characteristics

**Table 6. Characteristics per section**

$T_j = 25\text{ °C}$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0\text{ V}$ ; $I_D = 0.5\text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 32\text{ V}$ ; $I_D = 50\text{ mA}$	1.4	1.9	2.4	V
$V_{GSq}$	gate-source quiescent voltage	$V_{DS} = 32\text{ V}$ ; $I_{Dq} = 250\text{ mA}$	1.5	2.0	2.5	V
$I_{DSS}$	drain leakage current	$V_{GS} = 0\text{ V}$ ; $V_{DS} = 32\text{ V}$	-	-	1.4	$\mu\text{A}$
$I_{DSX}$	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75\text{ V}$ ; $V_{DS} = 10\text{ V}$	8.0	9.0	-	A
$I_{GSS}$	gate leakage current	$V_{GS} = \pm 10\text{ V}$ ; $V_{DS} = 0\text{ V}$	-	-	50	nA
$g_{fs}$	forward transconductance	$V_{DS} = 10\text{ V}$ ; $I_D = 2.5\text{ A}$	-	3.3	-	S
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75\text{ V}$ ; $I_D = 1.75\text{ A}$	-	300	-	$\text{m}\Omega$
$C_{iss}$	input capacitance	$V_{GS} = 0\text{ V}$ ; $V_{DS} = 32\text{ V}$ ; $f = 1\text{ MHz}$	-	39	-	pF
$C_{oss}$	output capacitance	$V_{GS} = 0\text{ V}$ ; $V_{DS} = 32\text{ V}$ ; $f = 1\text{ MHz}$	-	15	-	pF
$C_{rs}$	feedback capacitance	$V_{GS} = 0\text{ V}$ ; $V_{DS} = 32\text{ V}$ ; $f = 1\text{ MHz}$	-	0.84	-	pF

## 7. Application information

**Table 7. RF performance in a common-source class-AB circuit**

$T_h = 25\text{ °C}$ ;  $I_{Dq} = 0.2\text{ A}$ .

Mode of operation	f (MHz)	$V_{DS}$ (V)	$P_L$ (W)	$G_p$ (dB)	$\eta_D$ (%)
CW, class-AB	1300	32	35	> 18	> 59

### 7.1 Ruggedness in class-AB operation

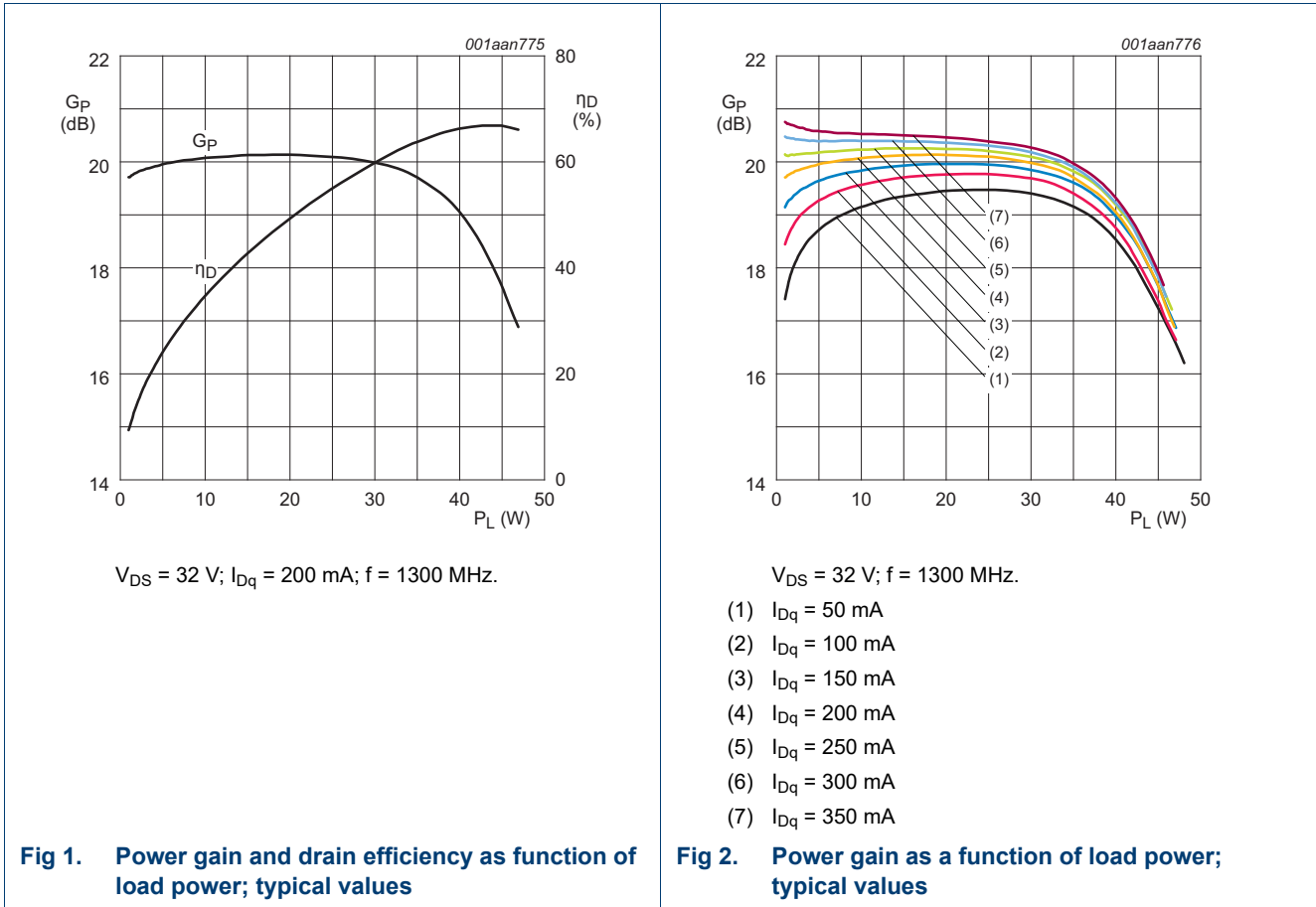
The BLF642 is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS} = 32\text{ V}$ ;  $f = 1300\text{ MHz}$  at rated load power.

8. Test information

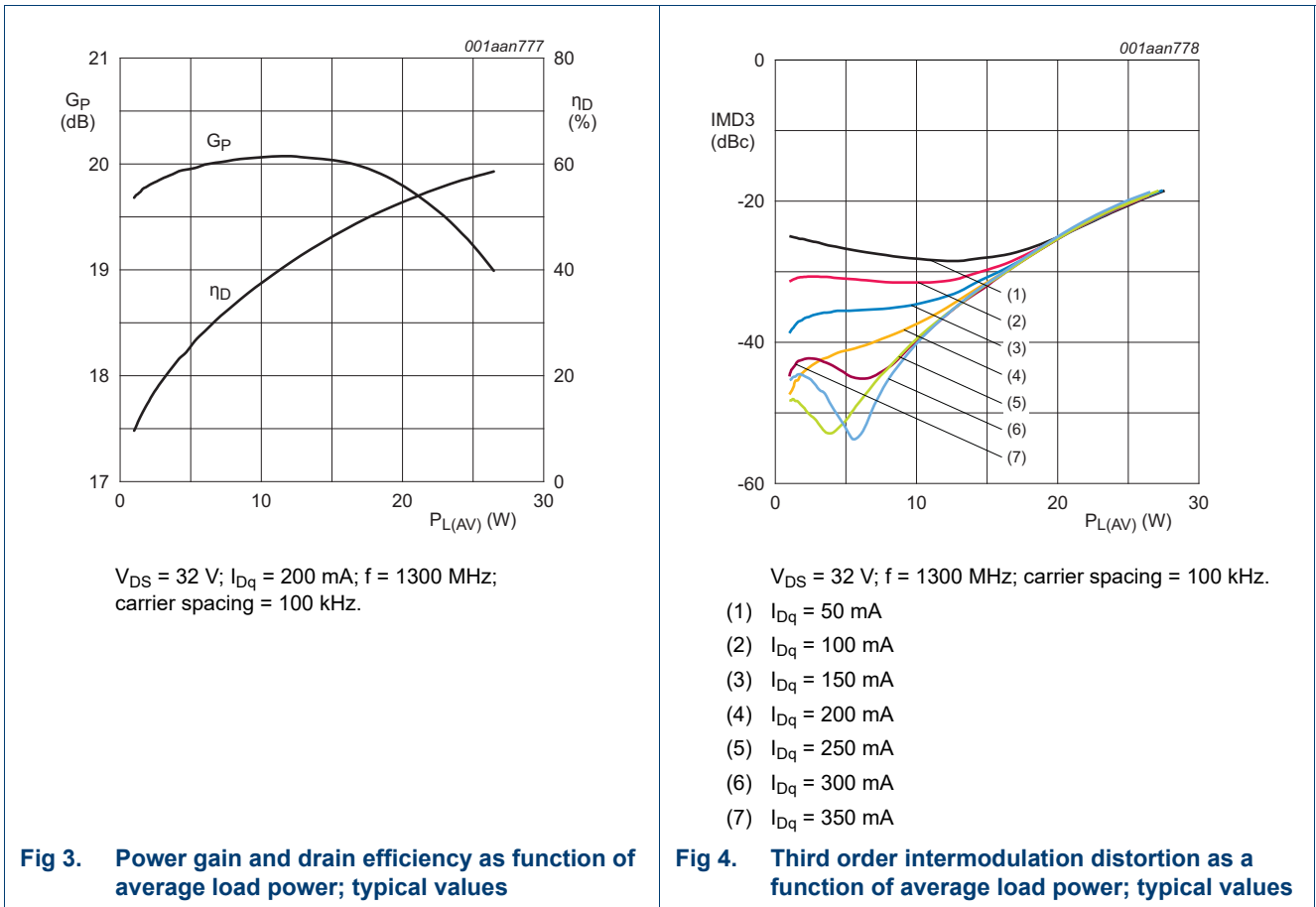
8.1 RF performance

The following figures are measured in a class-AB production test circuit.

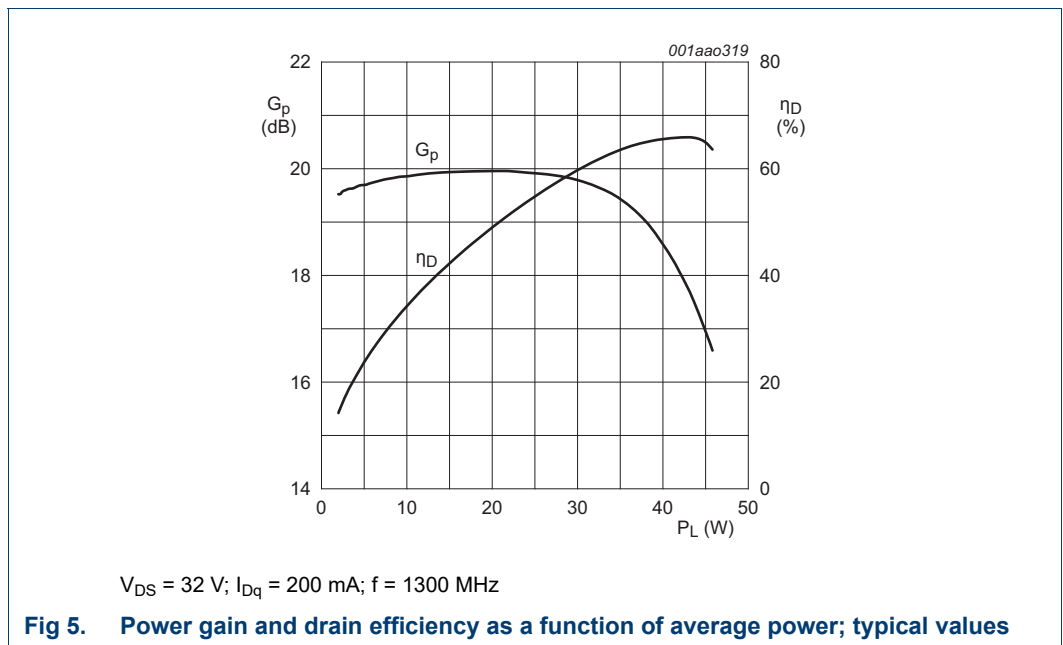
8.1.1 1-Tone CW



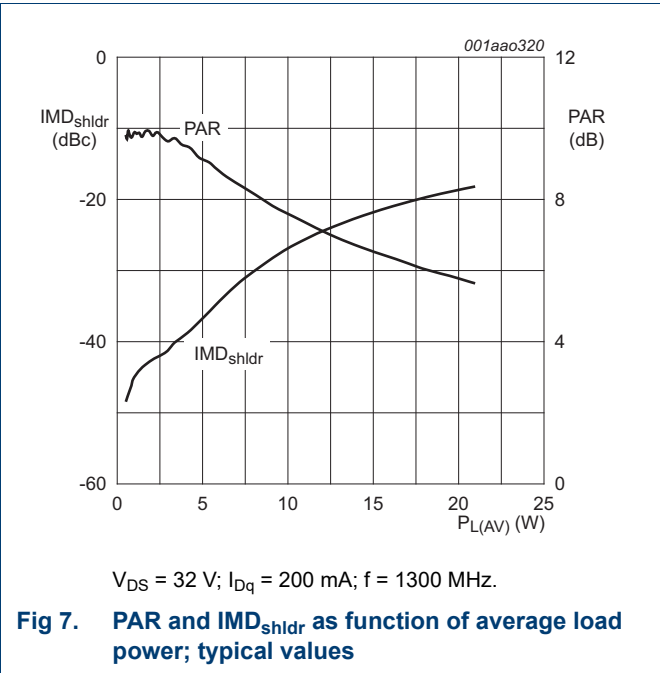
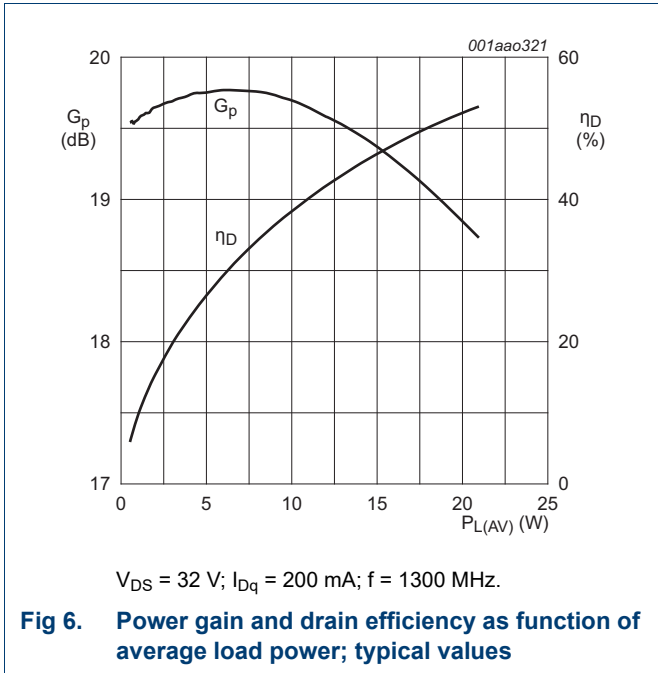
8.1.2 2-Tone CW



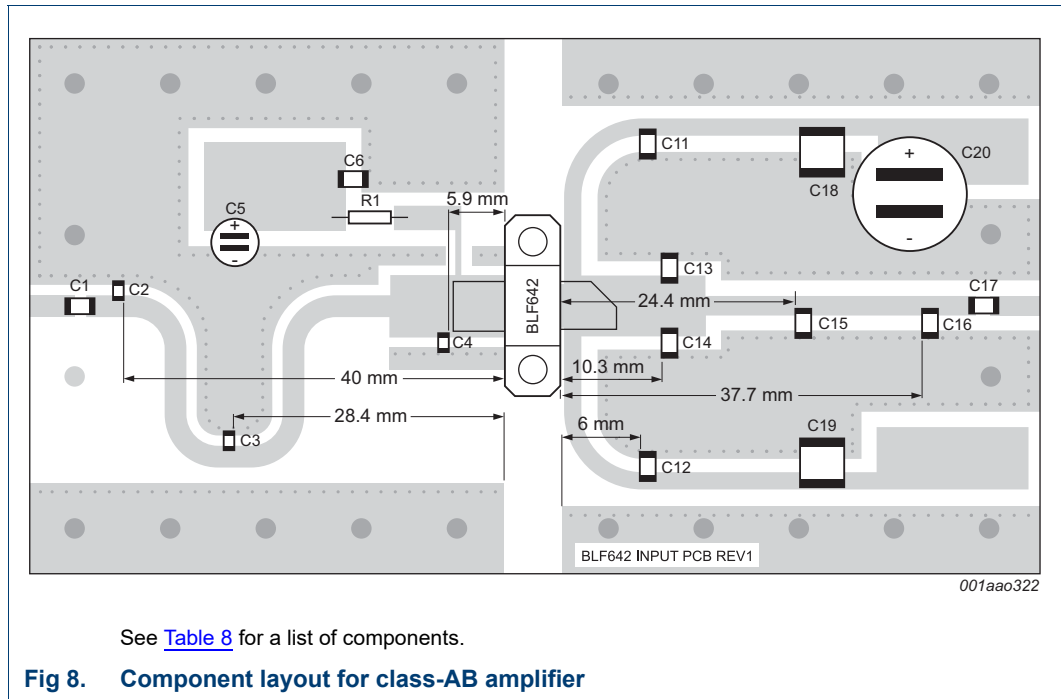
8.1.3 Pulsed CW



8.1.4 DVB-T



8.2 Test circuit



**Table 8. List of components**

For production test circuit, see [Figure 8](#).

Printed-Circuit Board (PCB): Rogers 5880;  $\epsilon_r = 2.2$ ; height = 0.762 mm; Copper (top/bottom metallization); thickness copper plating = 35  $\mu\text{m}$ .

Component	Description	Value	Remarks
C1	multilayer ceramic chip capacitor	22 pF	[1]
C2	multilayer ceramic chip capacitor	5.1 pF	[2]
C3	multilayer ceramic chip capacitor	4.3 pF	[2]
C4	multilayer ceramic chip capacitor	10 pF	[2]
C5	electrolytic chip capacitor	10 $\mu\text{F}$ ; 50 V	
C6	multilayer ceramic chip capacitor	22 nF	
C11, C12	multilayer ceramic chip capacitor	22 pF	[1]
C13, C14	multilayer ceramic chip capacitor	6.2 pF	[1]
C15	multilayer ceramic chip capacitor	4.3 pF	[1]
C16	multilayer ceramic chip capacitor	1.2 pF	[1]
C17	multilayer ceramic chip capacitor	22 pF	[1]
C18, C19	multilayer ceramic chip capacitor	10 $\mu\text{F}$	[3]
C20	electrolytic capacitor	470 $\mu\text{F}$ ; 63 V	
R1	wire resistor	100 $\Omega$	

[1] American Technical Ceramics type 100B or capacitor of same quality.

[2] American Technical Ceramics type 100A or capacitor of same quality.

[3] TDK C570X7R1H106KT000N or capacitor of same quality.

9. Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT467C

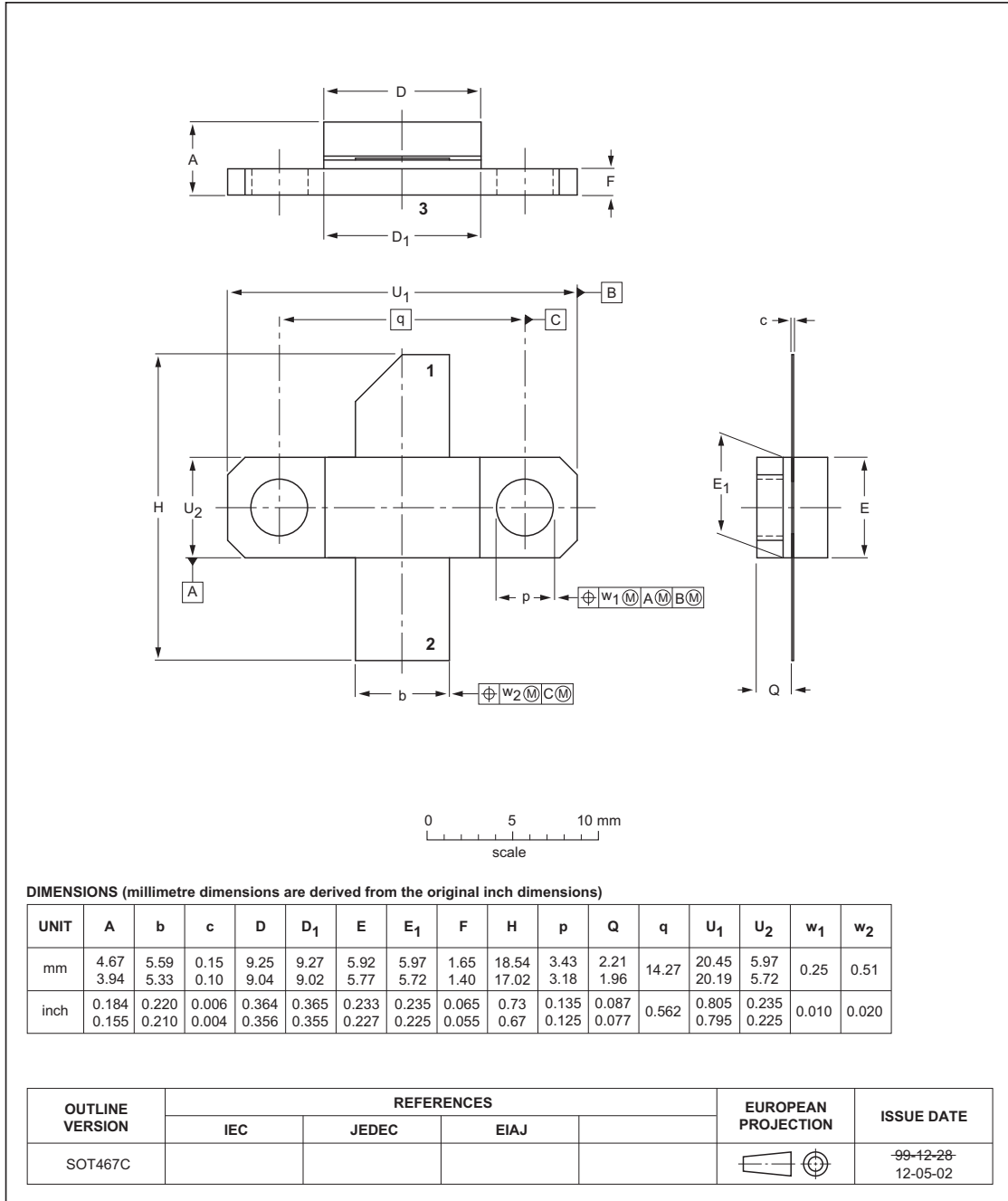


Fig 9. Package outline SOT467C

## 10. Abbreviations

Table 9. Abbreviations

Acronym	Description
CW	Continuous Waveform
DVB-T	Digital Video Broadcast - Terrestrial
ESD	ElectroStatic Discharge
HF	High Frequency
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal-Oxide Semiconductor Transistor
PAR	Peak-to-Average power Ratio
RF	Radio Frequency
VSWR	Voltage Standing-Wave Ratio

## 11. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF642#3	20150901	Product data sheet	-	BLF642 v.2
Modifications:	<ul style="list-style-type: none"> <li>The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>			
BLF642 v.2	20110722	Product data sheet	-	BLF642 v.1
BLF642 v.1	20110308	Objective data sheet	-	-

## 12. Legal information

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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