



**THE DATASHEET OF  
BAV70DW-7-F**



## Features

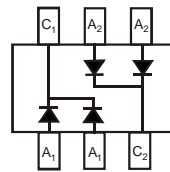
- Fast Switching Speed
- Small Surface Mount Package
- Low Leakage Current
- Two "BAV70" Circuits in One Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208<sup>(E3)</sup>
- Orientation: See Diagram
- Weight: 0.006 grams (Approximate)



TOP VIEW  
SOT-363



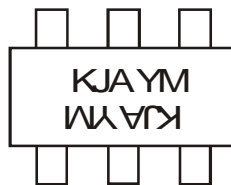
TOP VIEW  
Internal Schematic

## Ordering Information (Note 4)

Part Number	Case	Packaging
BAV70DW-7-F	SOT-363	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds<sup>1</sup>.
  4. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

## Marking Information



KJA = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year ex: B = 2014  
 M = Month ex: 9 = September

### Date Code Key

Year	2001	2002	...	2010	2011	2012	2013	2014	2015	2016	2017	2018
Code	M	N	...	X	Y	Z	A	B	C	D	E	F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	V <sub>RM</sub>	100	V
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	80	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	57	V
Forward Continuous Current (Note 5)	I <sub>FM</sub>	300	mA
Average Rectified Output Current (Note 5)	I <sub>O</sub>	150	mA
Repetitive Peak Forward Current	I <sub>FRM</sub>	450	mA
Non-repetitive Peak Forward Surge Current	I <sub>FSM</sub>	@ t = 1.0μs	4
		@ t = 1.0ms	1
		@ t = 1.0s	0.5

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R <sub>θJA</sub>	625	°C/W
Thermal Resistance, Junction to Solder Point	R <sub>θJSP</sub>	70	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V <sub>(BR)R</sub>	75 80	—	V	I <sub>F</sub> = 2.5μA I <sub>F</sub> = 20μA
Forward Voltage	V <sub>F</sub>	—	0.715 0.855 1.0 1.25	V	I <sub>F</sub> = 1.0mA I <sub>F</sub> = 10mA I <sub>F</sub> = 50mA I <sub>F</sub> = 150mA
Reverse Current (Note 6)	I <sub>R</sub>	—	2.5 50 30 25	μA μA μA nA	V <sub>R</sub> = 75V V <sub>R</sub> = 75V, T <sub>J</sub> = +150°C V <sub>R</sub> = 25V, T <sub>J</sub> = +150°C V <sub>R</sub> = 20V
Total Capacitance	C <sub>T</sub>	—	1.5	pF	V <sub>R</sub> = 0, f = 1.0MHz
Reverse Recovery Time	t <sub>rr</sub>	—	4.0	ns	I <sub>F</sub> = I <sub>R</sub> = 10mA, I <sub>tr</sub> = 0.1 x I <sub>R</sub> , R <sub>L</sub> = 100Ω
Forward Recovery Voltage	V <sub>FR</sub>	—	1.75	V	I <sub>F</sub> = 10mA, t <sub>r</sub> = 20 ns

- Notes:
- Device mounted on FR-4 PCB, 1in. x 0.85in. x 0.062in. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
  - Short duration pulse test used to minimize self-heating effect.

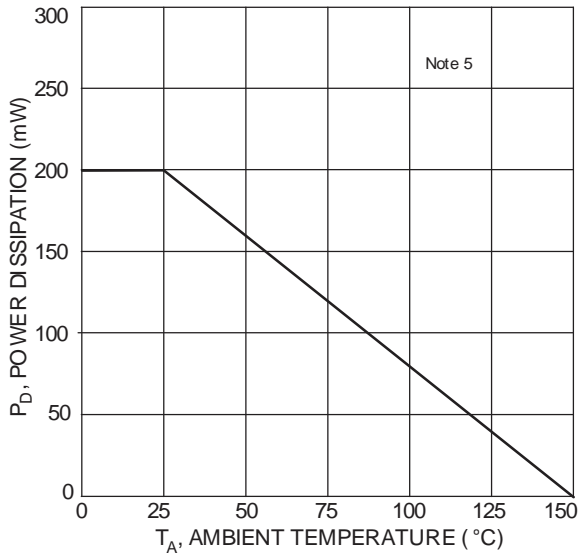


Fig. 1 Power Derating Curve, Total Package

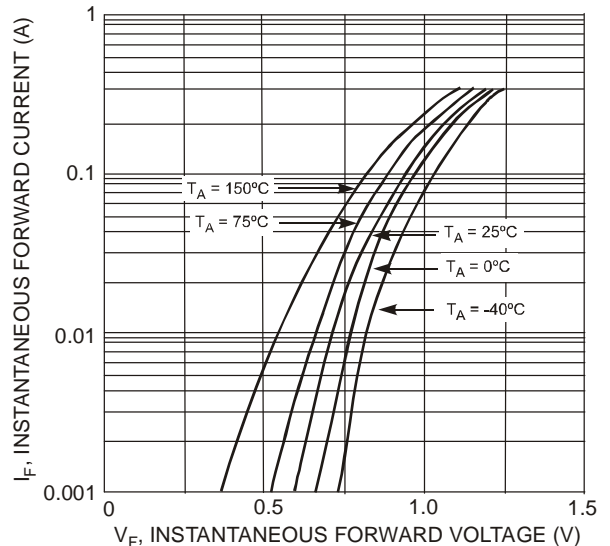


Fig. 2 Typical Forward Characteristics, Per Element

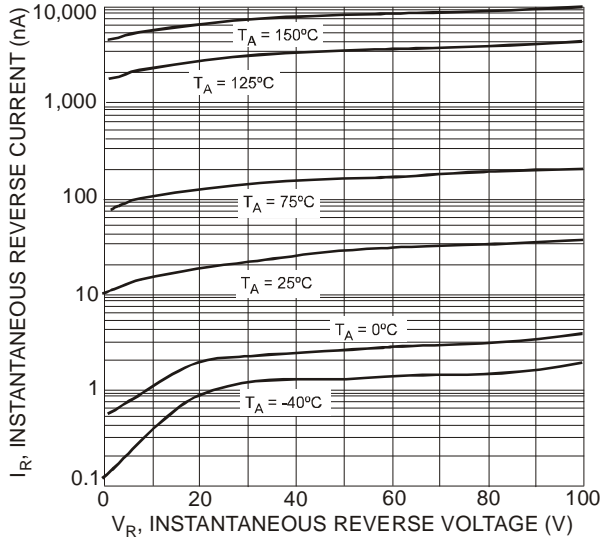


Fig. 3 Typical Reverse Characteristics, Per Element

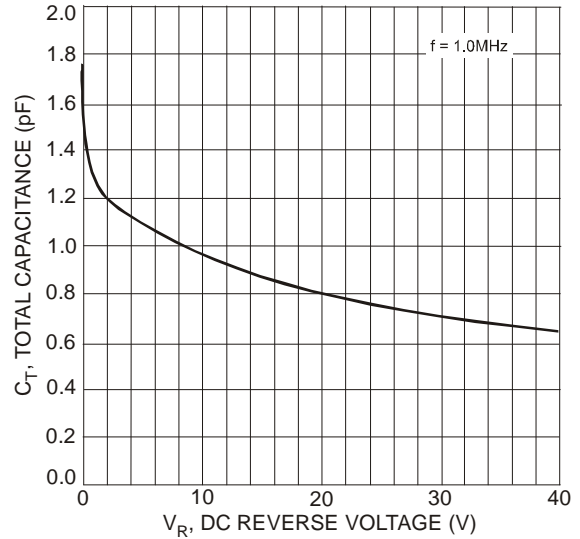
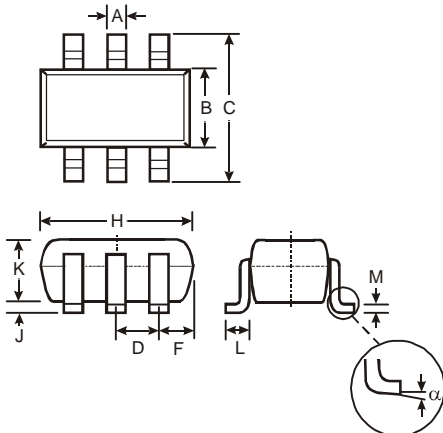


Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

## Package Outline Dimensions

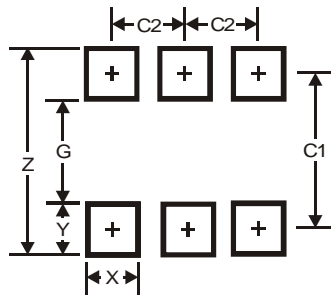
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT363			
Dim	Min	Max	Typ
A	0.10	0.30	0.25
B	1.15	1.35	1.30
C	2.00	2.20	2.10
D	0.65 Typ		
F	0.40	0.45	0.425
H	1.80	2.20	2.15
J	0	0.10	0.05
K	0.90	1.00	1.00
L	0.25	0.40	0.30
M	0.10	0.22	0.11
$\alpha$	0°	8°	-
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
<b>Z</b>	2.5
<b>G</b>	1.3
<b>X</b>	0.42
<b>Y</b>	0.6
<b>C1</b>	1.9
<b>C2</b>	0.65

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