



**THE DATASHEET OF  
DMP2035UFDF-7**



## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$ Max	$I_D$ Max $T_A = +25^\circ C$
-20V	29m $\Omega$ @ $V_{GS} = -4.5V$	-6.9A
	39m $\Omega$ @ $V_{GS} = -2.5V$	-5.9A

## Features and Benefits

- 0.6mm Profile – Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low Gate Threshold Voltage
- Low On-Resistance
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>**

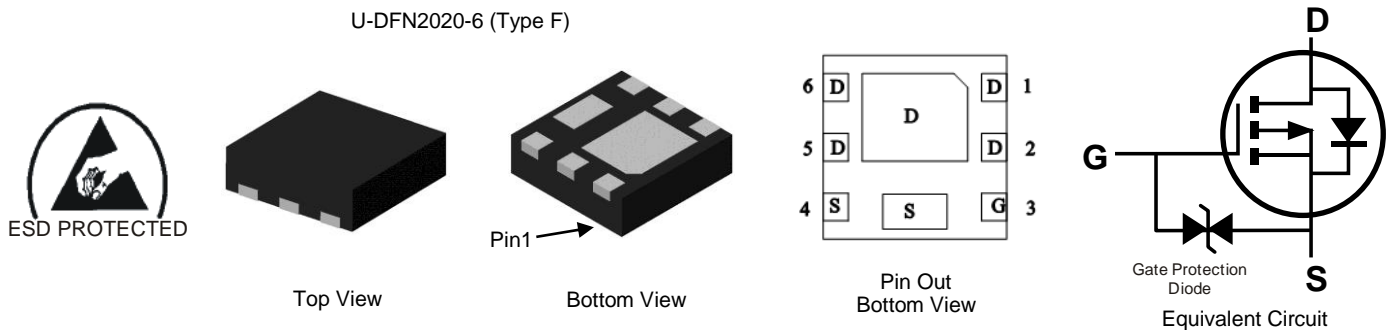
## Description and Applications

This MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Management Application
- Power Management Functions
- DC-DC Converters

## Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0065 grams (Approximate)



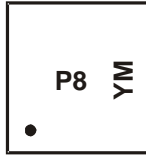
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2035UFDf-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMP2035UFDf-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information

Site 1



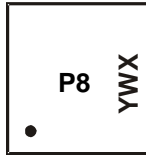
P8 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: H = 2020)  
 M = Month (ex: 9 = September)

Date Code Key

<b>Year</b>	<b>2016</b>	...	<b>2020</b>	<b>2021</b>	<b>2022</b>	<b>2023</b>	<b>2024</b>	<b>2025</b>	<b>2026</b>	<b>2027</b>	<b>2028</b>	<b>2029</b>
<b>Code</b>	D	...	H	I	J	K	L	M	N	O	P	R

<b>Month</b>	<b>Jan</b>	<b>Feb</b>	<b>Mar</b>	<b>Apr</b>	<b>May</b>	<b>Jun</b>	<b>Jul</b>	<b>Aug</b>	<b>Sep</b>	<b>Oct</b>	<b>Nov</b>	<b>Dec</b>
<b>Code</b>	1	2	3	4	5	6	7	8	9	O	N	D

Site 2



P8 = Product Type Marking Code  
 YWX = Date Code Marking  
 Y = Year (ex: 0 = 2020)  
 W = Week (ex: a = Week 27; z Represents Week 52 and 53)  
 X = Internal Code (ex: U = Monday)

Date Code Key

<b>Year</b>	<b>2016</b>	...	<b>2020</b>	<b>2021</b>	<b>2022</b>	<b>2023</b>	<b>2024</b>	<b>2025</b>	<b>2026</b>	<b>2027</b>	<b>2028</b>	<b>2029</b>
<b>Code</b>	6	...	0	1	2	3	4	5	6	7	8	9

<b>Week</b>	<b>1-26</b>		<b>27-52</b>				<b>53</b>	
<b>Code</b>	A-Z		a-z				z	

<b>Internal Code</b>	<b>Sun</b>	<b>Mon</b>	<b>Tue</b>	<b>Wed</b>	<b>Thu</b>	<b>Fri</b>	<b>Sat</b>
<b>Code</b>	T	U	V	W	X	Y	Z

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	-20	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -4.5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-6.9 -5.5	A
	t < 10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-8.1 -6.5	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	-40	A
Continuous Source-Drain Diode Current (Note 6)			I <sub>S</sub>	-2.5	A
Avalanche Current (Note 7) L = 0.1mH			I <sub>AS</sub>	-21	A
Avalanche Energy (Note 7) L = 0.1mH			E <sub>AS</sub>	23	mJ

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C		P <sub>D</sub>	0.66	W
	T <sub>A</sub> = +70°C			0.42	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R <sub>θJA</sub>	180	°C/W
	t < 10s			135	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C		P <sub>D</sub>	2.03	W
	T <sub>A</sub> = +70°C			1.31	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R <sub>θJA</sub>	63	°C/W
	t < 10s			43	
Thermal Resistance, Junction to Case (Note 6)			R <sub>θJC</sub>	17.5	
Operating and Storage Temperature Range			T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	-1	µA	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS</b> (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.4	—	-1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	20	29	mΩ	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -6.4A
			24	39		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -4.8A
			31	60		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.5A
			40	120		V <sub>GS</sub> = -1.5V, I <sub>D</sub> = -1.5A
			V <sub>GS</sub> = -1.5V, I <sub>D</sub> = -1.5A			
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1.0A
<b>DYNAMIC CHARACTERISTICS</b> (Note 9)						
Input Capacitance	C <sub>iss</sub>	—	1,808	—	pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	155	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	117	—		
Gate Resistance	R <sub>G</sub>	—	32	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>G</sub>	—	20.5	—	nC	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.0A
Gate-Source Charge	Q <sub>GS</sub>	—	2.8	—		
Gate-Drain Charge	Q <sub>GD</sub>	—	4.1	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	9.1	—	ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V, R <sub>G</sub> = 6Ω, I <sub>D</sub> = -1.0A
Turn-On Rise Time	t <sub>R</sub>	—	12.3	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	120	—		
Turn-Off Fall Time	t <sub>F</sub>	—	54	—		
Reverse Recovery Time	t <sub>RR</sub>	—	23.1	—	ns	I <sub>F</sub> = -1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	—	8.3	—	nC	I <sub>F</sub> = -1.0A, di/dt = 100A/µs

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
  - I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

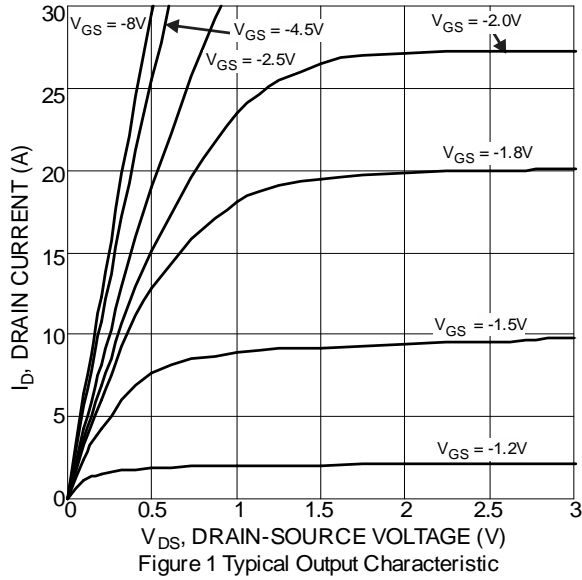


Figure 1 Typical Output Characteristic

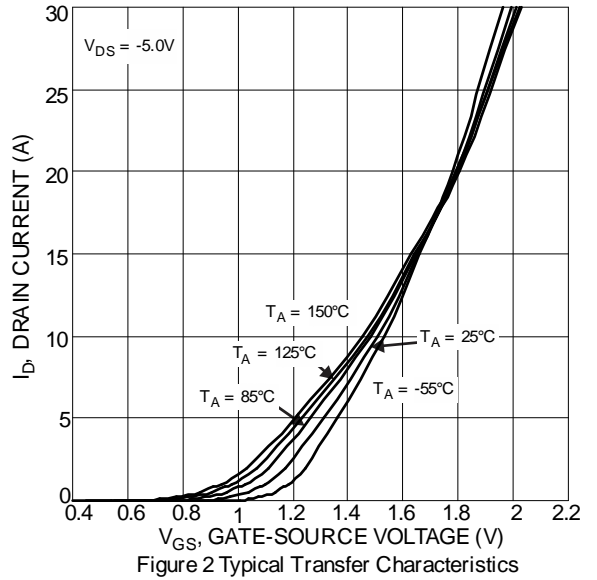


Figure 2 Typical Transfer Characteristics

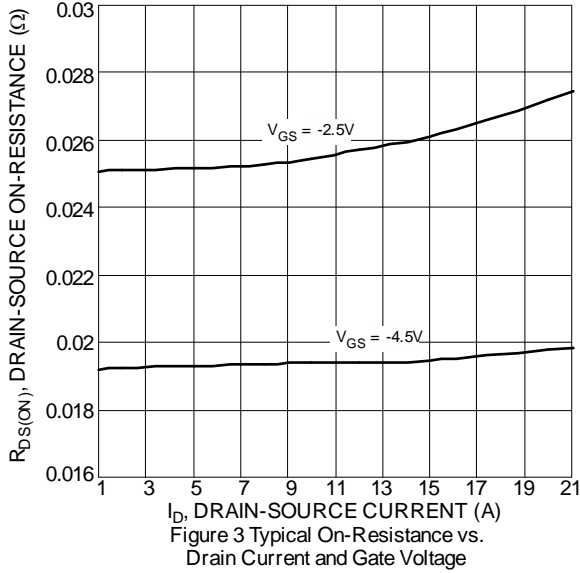


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

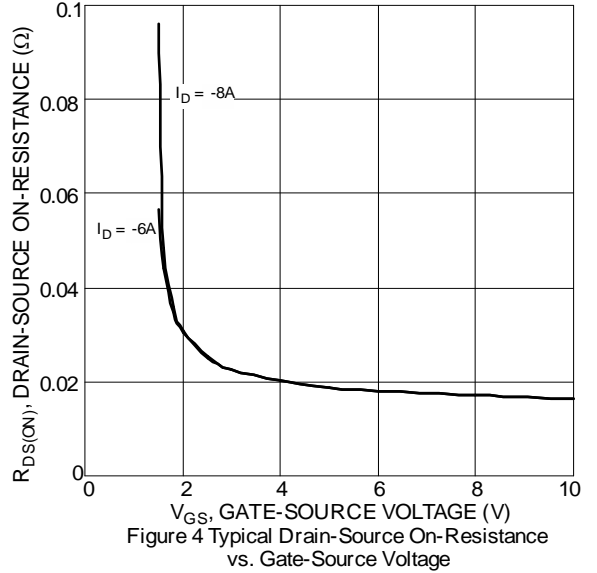


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

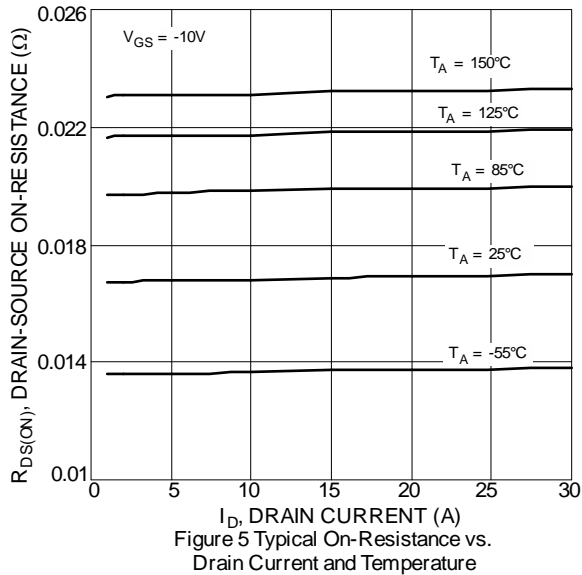


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

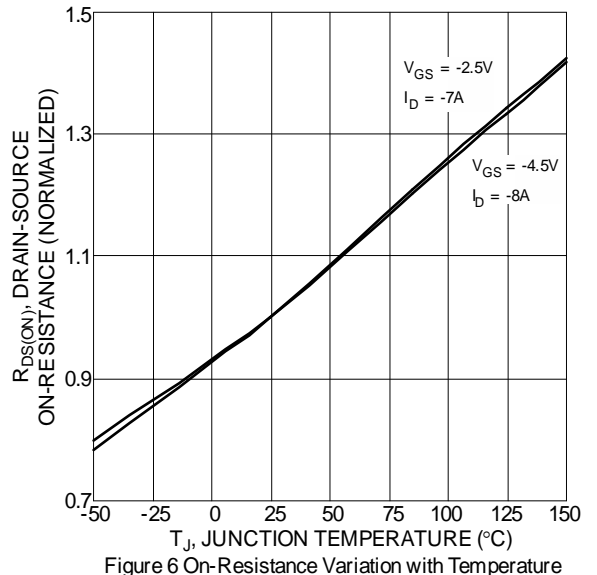


Figure 6 On-Resistance Variation with Temperature

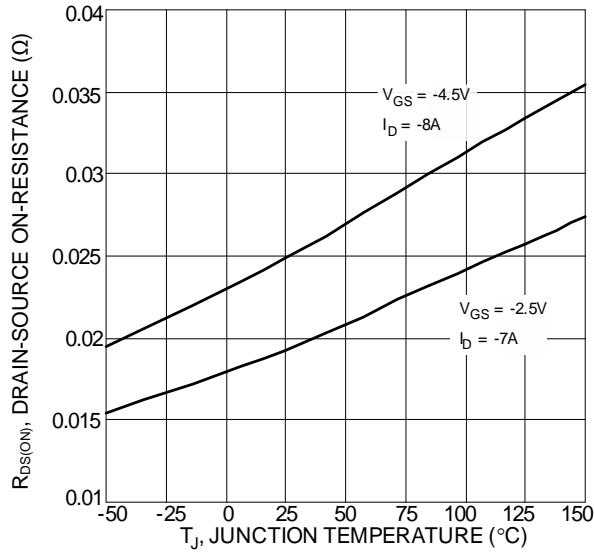


Figure 7 On-Resistance Variation with Temperature

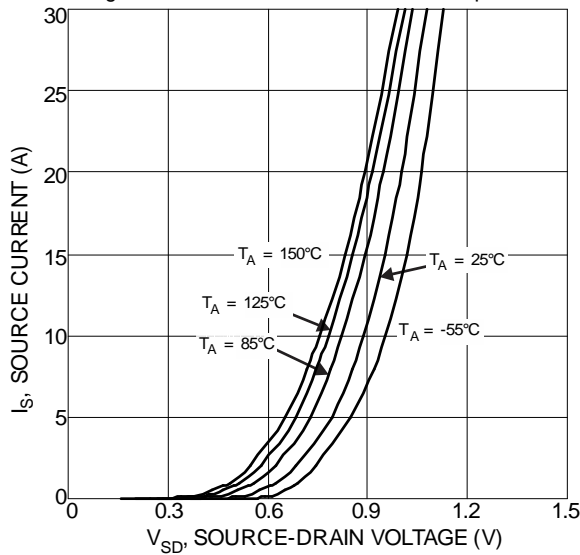


Figure 9 Diode Forward Voltage vs. Current

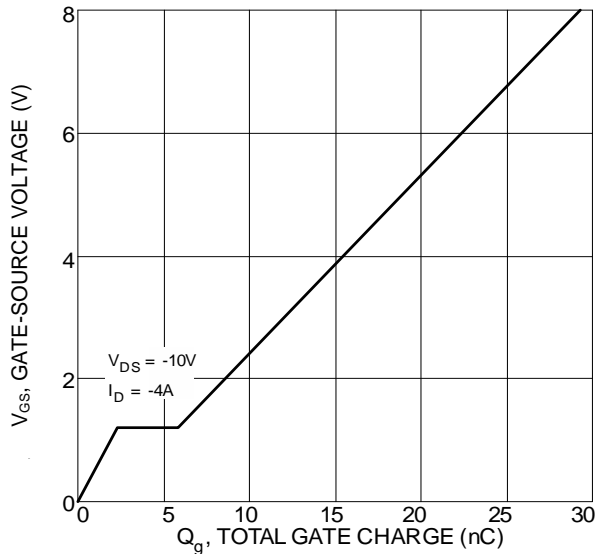


Figure 11 Gate Charge

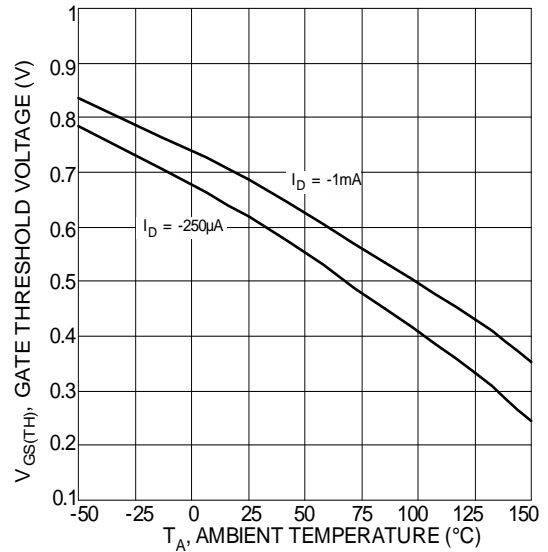


Figure 8 Gate Threshold Variation vs. Ambient Temperature

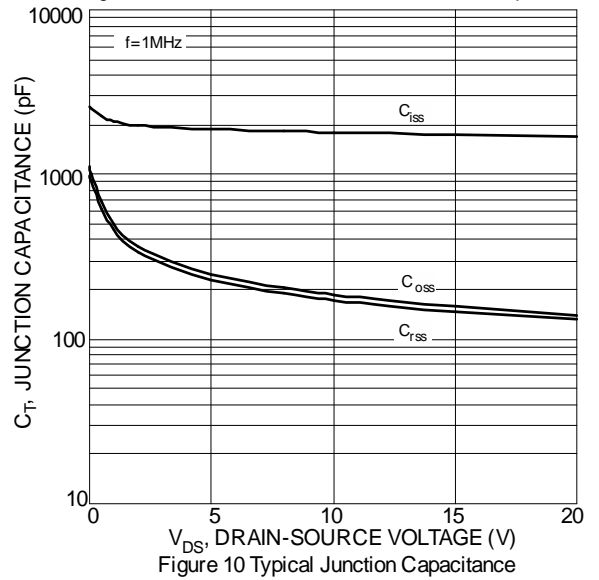


Figure 10 Typical Junction Capacitance

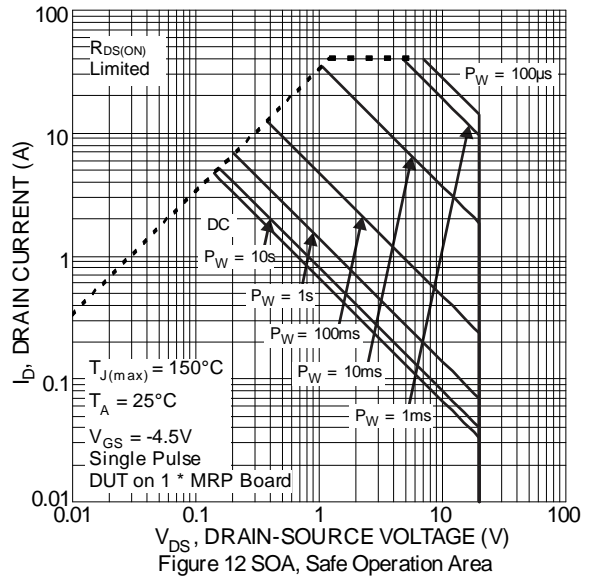
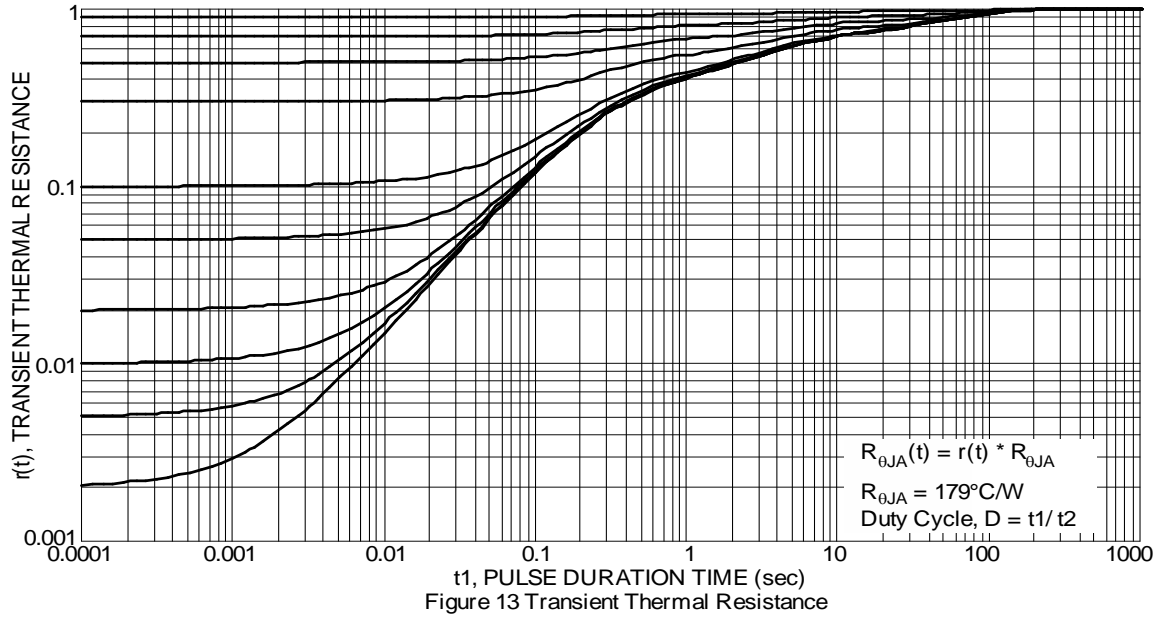


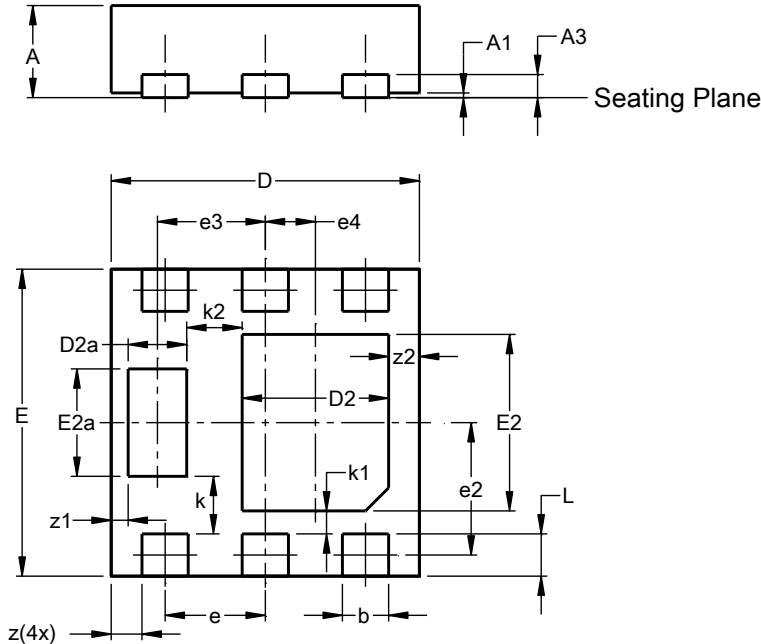
Figure 12 SOA, Safe Operation Area



## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type F)

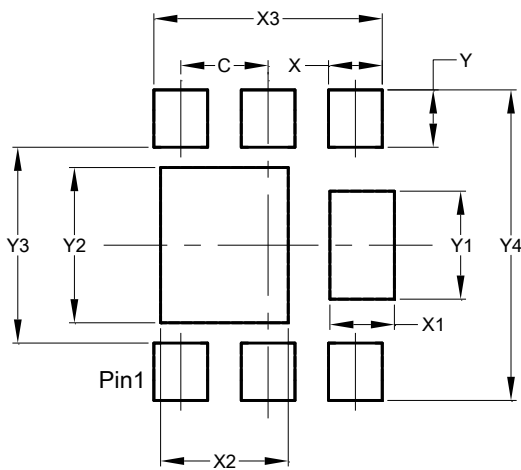


U-DFN2020-6 (Type F)			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0.00	0.05	0.03
A3	-	-	0.15
b	0.25	0.35	0.30
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
D2a	0.33	0.43	0.38
E	1.95	2.05	2.00
E2	1.05	1.25	1.15
E2a	0.65	0.75	0.70
e	0.65 BSC		
e2	0.863 BSC		
e3	0.70 BSC		
e4	0.325 BSC		
k	0.37 BSC		
k1	0.15 BSC		
k2	0.36 BSC		
L	0.225	0.325	0.275
z	0.20 BSC		
z1	0.110 BSC		
z2	0.20 BSC		
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300

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