

THE DATASHEET OF EXB-V4V331JV

win-source.net



0086-755-83957316

Issue No.	:	151EXB008056
		November 26.2008
		■ New □ Changed

PRODUCT SPECIFICATION FOR APPROVAL

Product Description	:	Chip Resistor Array (RoHS Compliance)
Product Part Number	:	$EXBV4V * * * \Box V$

Country of Origin	:	JAPAN, CHINA, MALAYSIA
Applications	:	Standard electronic equipment

*If you approve this specification, please fill in and sign the below and return 1 copy to us.

Approval No :
Approval Date :
Executed by :
(signature)
Title :
Dept. :

Circuit Components	Business Unit
Panasonic Electronic	Devices Co., Ltd.

401 Sadamasa-cho, Fukui City 910-8502 Japan

Phone : +81-776-56-8034 Fax : +81-776-56-3114

Prepared by	:	Engineering Section
Contact Person	:	2101010
Signature		7d. Jabukashi
Name(Print)		H.Yabukoshi
Title	:	
Authorized by	:	F. Watanake
Signature		- Walanake
Name(Print)		T.Watanabe
Title :		Manager of Engineering
Title :		Manager of Engineering



Subject					Spec. No.
Chip Resistor Arra	151-EXB-V4V01LH				
Part No.]	EXBV4V			9-1
1. Dimension $A \xrightarrow{a} 4 \cdot \phi D$ $\square \uparrow$ $\square \uparrow$ $\square \uparrow$ $\square \uparrow$ $\square \uparrow$ $\square \uparrow$ $\square \uparrow$ $\square \uparrow$ $\square \uparrow$		Alum (4)Te (I	bstrate nina ermination nner) • Ag/Pd	 (2)Protective coating Resin (5)Termination (Between) Ni Plating 	 (3)Resistive element Ruthenium oxide (6) Termination (Outer) Sn Plating
	a-a' sectio	nal plan) (3) (4) (5) (6	3)		
Dimension(mm)	L 1.60 ^{+0.20} -0.10	W 1.60 ^{+0.20} / _{-0.10}	T 0.60±0.10	A 0.60±0.10	B 0.30±0.15
Dimension(mm)	D (0.30)	P (0.80)	E 0.45±0.10	G 0.45±0.15	():Reference
2. Power derating of 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 100 1	C 70°	60 80 100 120 14 emperature(°C)	-55 C	egory temperature °C~+125°C	e range
3. Ratings	F	ig. 1			
Item	Rated Valu		olanation		
Rated Dissipation	0.063 W / e	lement the sho	rated diss wn in Fig.1	ipation should	ture over 70 °C, be reduced as
				stance is less that should be calc	an 50 mΩ) ulated from the

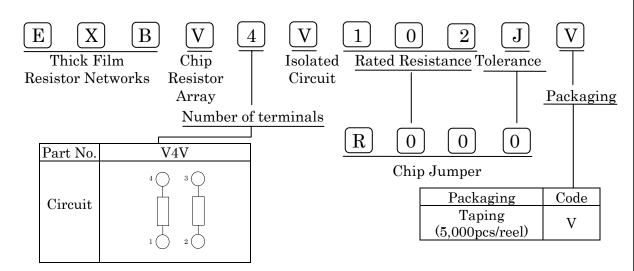
Rated voltage
&
Rated Continuous
Working Voltage
(RCWV)The rated voltage of each resistor should be calculated from the
equation below, and when the rated voltage exceeds the limiting
element voltage, the limiting element voltage should the maximum
working element voltage.
 $E = \sqrt{P \times R}$
Limiting element voltage : 50 V
 $E = \sqrt{P \times R}$
Limiting element voltage : 50 V
(W)

Subject		Spec. No.
Chip Resistor Array	PRODUCT SPECIFICATION FOR INFORMATION	151-EXB-V4V01LE
Part No.		

EXBV4V

Item	Rated Value	Explanation		
Maximum overload voltage	Voltage should be 2.5 × E. When the voltage exceeds the maximum overload voltage, the value shown below should be the maximum overload voltage. Maximum overload voltage: 100V Chip jumper: Max. overload current 2A			
Resistance tolerance	sign J 0	Tolerance for resista ±5% Chip Jumper	nce	
Range of rated resistance for manufacture	ToleranceJ0	Resistance range 1.0Ω to $1.0 M\Omega$ Less than 50 m Ω	Series E-24	

4. Explanation of part number



5. Appearance & Construction

Item	Specifications	Explanation
Appearance & Construction	 that do not fa unevenness, fla 2. The electrode dimensions. The unevenness, fla 3. The electrode s resistive element 4. Substrate should 	lement should be covered with protective coating ade easily. The surface of coating should avoid w, pinhole and discoloration. should be printed uniformly, as shown in the e plating should not fade easily, and should avoid w, pinhole, projection and discoloration. should be connected electrically, mechanically to nt. d not have chipping, flaw, flash and crack. Details priteria shall be as described in attached sheet

Subject				Spec. No.
Chip Resistor	Array PRODUCT S	PECIFICATIO	ON FOR INFORMATION	151-EXB-V4V01LI
Part No.	EX	BV4V		9-3
As far as there	shall be not designat	tion especially	, the following test and mea	asurement shall be
operated und	er normal temperatu	are(5 °C to 35	°C), normal humidity(45	%RH to 85 %RH),
normal atmos	spheric pressure(86 kl	Pa to 106 kPa)		
6. Performance	Specification			
	Specification		$\mathbf{T}_{\mathbf{r}}$	
Item	Resistor	Jumper	Test methods	
DC resistance	DC resistance value shall be within the specified tolerance	Less than 50 mΩ	Measuring voltage: refer t At 20 °C, 65 %RH	o JIS-C5201-1
Temperature coefficient	10Ω to $1M\Omega$ ±2 Chip jumper :	TCR +600 ×10 ⁻⁶ / °C 200×10 ⁻⁶ / °C	Natural resistance changed degree centigrade. $TCR = \frac{R_2 - R_1}{R_1 \times (t_2 - t_1)}$ $R_1 : Resistance value a temperature(t_1)$ $R_2 : Resistance value a temperature(t_2)$	t reference
	Less than	50 mΩ	$t_2 - t_1 = 100 \text{ °C}, t_1 = 25 \text{ °C}$	
Overload	±(2 %+0.1 Ω)	Less than 50 mΩ	Resistors shall be applied voltage for 5 seconds. Maximum over load voltage	
Intermittent Overload	±(5 %+0.1 Ω)	Less than 50 mΩ	Resistors shall be subjected 2.5 times the rated vol second with pause of 2 tests. Maximum over load voltage	tage applied for 5 5 seconds between
Dielectric Withstanding	No evidence of flashover, mechanical damage, arcing or insulation breakdown.		AC 100V between substra for 1 minute.	-
Insulation Resistance	Min. 1,000 MΩ		Insulation resistance bettermination shall be meas	

7. Mechanical characteristic

Item	Specification		Test methods	
Item	Resistor	Jumper	rest methods	
Bend strength of the face platingNo mechanical damage $+(1 \%+0.05 \text{ O})$ Less th		nagu	Substrate: Glass epoxy(t = 1.6 mm) ——Span: 90 mm	
			Bending distance: 3 mm (10 seconds)	
Solderability	(min 95 % coverage)		Resistors shall be dipped in the melted solder bath at 230 °C \pm 5 °C for 3 s \pm 0.5 s. Flux shall be removed from the surface of termination with clean organic solvent.	

Subject

Part No.

Chip Resistor Array PRODUCT SPECIFICATION FOR INFORMATION

EXBV4V

9-4

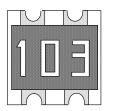
Item	Specification		Test methods	
Item	Resistor	Jumper	Test methods	
Resistance to	$\pm (1 \% + 0.05 \Omega)$	Less than	Resistors shall be dipped in the melted solder	
soldering heat	±(1 %)+0.03 22)	$50~\mathrm{m}\Omega$	bath at 270 °C \pm 5 °C for 10s \pm 1s.	
Vibration	±(1 %+0.05 Ω)	Less than 50 mΩ	Resistors shall be subjected to a single vibration having as double amplitude of 1.5 mm for 2 hours in each three mutually perpendicular directions for total 6 hours. The vibration frequency shall be varied uniformly 10 Hz to 55 Hz and return to 10 Hz traversing for 1 minute.	
	Without distinct of	leformation in	Solvent solution: Isopropyl alcohol	
	appearance	•	(1) Dipping 10 hours \pm 1 hour, dry in room	
Solvent resistance	±(0.5 %+0.05 Ω)	Less than 50 mΩ	 condition for 30 min ± 10 min. (2) Ultrasonic wave washing: 5 min ± 1 min (0.3 W/cm²,28 kHz) Dry in room condition for 30 min ± 10 min. 	

8. Environmental Test

Thomas	Specification		Test methods	
Item	Resistor	Jumper		
Low temperature exposure	±(1 %+0.05 Ω)	Less than 50 mΩ	Resistors shall be exposed at -55 °C \pm 3 °C for 1000 hours $^{+48}_{0}$ hours	
Endurance at upper category temperature	±(1 %+0.05 Ω)	Less than 50 mΩ	Resistors shall be exposed at +125 °C±3 °C 1000 hours $^{+48}_{0}$ hours.	
Temperature cycling	±(1 %+0.05 Ω)	Less than 50 mΩ	-55 °C ± 3 °C, 30 minutes $\uparrow\downarrow$ Nominal temp., 30minutes $\uparrow\downarrow$ +125 °C ± 3 °C, 30minutes	
Humidity (Steady state)	±(1 %+0.05 Ω)	Less than 50 mΩ	Resistors shall be exposed at 60 °C \pm 2 °C 90 % to 95 % relative humidity in a humi test chamber for 1000 hours $^{+48}_{-6}$ hours.	
Endurance at 70 °C	±(3 %+0.1 Ω)	Less than 50 mΩ	Resistors shall be exposed at 70 °C \pm 2 °C 1000 hours $^{+48}_{0}$ hours. During this time, rated voltage shall be applied intermitte for 1.5 hours ON, 0.5 hour OFF.	
Load life in Humidity	±(3 %+0.1 Ω)	Less than 50 mΩ	Resistor shall be exposed at 60 °C \pm 2 °C at 90 % to 95 % relative humidity for 1000 hour $^{+48}_{0}$ hours. During this time, the rated volta shall be applied intermittently for 1.5 hour ON, 0.5 hour OFF.	

Subject		Spec. No.
Chip Resistor Array	PRODUCT SPECIFICATION FOR INFORMATION	151-EXB-V4V01LE
Part No.		
	EXBV4V	9-5

Express resistance value on resin side with three digits.



(Example)

 $103 \rightarrow 10 \ k\Omega$. The first two digits should be significant figures of resistance for E-24 series and the third one denotes number of zeros in ohms.

 $000 \rightarrow \text{Chip jumper}$

Subject

Part No.

Chip Resistor Array PRODUCT SPECIFICATION FOR INFORMATION

EXBV4V

10. Notice for use

Notice for use
Notice for use
 This specification shows the quality and performance of the product in a unit component. Before adoption, be sure to evaluate and verify the product mounting it in your product. We take no responsibility for troubles caused by the product usage that is not specified in the product usage that the product usage the product usage
 specification. (3)In traffic transportation equipment (trains, cars, traffic signal equipment, etc.), medical equipment, aerospace equipment, electric heating appliances, combustion and gas equipment rotating equipment, disaster and crime preventive equipment, etc. in cases where it is forecat that the failure of this product gives serious damage to human life and others, use fail-safe designed ensure safety by studying the following items to Ensure safety as the system by setting protective circuits and protective equipment.
 Ensure safety as the system by setting protective circuits and protective equipment. Ensure safety as the system by setting such redundant circuits as do not cause danger by single failure.
4)When a dogma shall be occurred about safety for this product, be sure to inform us rapid operate your technical examination.
5) The product is designed to use in general standard applications of general electric equipment (AV products, household electric appliances, office equipment, information and communication equipment, etc.); hence, it do not take the use under the following special environments into consideration.
Accordingly, the use in the following special environments, and such environmental conditions may affect the performance of the product; prior to use, verify the performance, reliability, etc. thoroughly.
 Use in liquids such as water, oil, chemical, and organic solvent. Where the product is close to a heating component, or where an inflammable such as a polyvinyl chloride wire is arranged close to the product. Where the product is sealed or coated with resin, etc.
4) Where water or a water-soluble detergent is used in cleaning free soldering (Pay particula attention to soluble flux.)
 5) Use in such a place where the product is wetted due to dew condensation. 6) Use in places full of corrosive gases such as sea breeze, Cl₂, H₂S, NH₃, SO₂, and NO_x. 7) Use under direct sunlight, in outdoor or in dusty atmospheres.
 8) Use in environment with large static electricity or strong electromagnetic waves. (6) If transient load (heavy load in a short time) like pulse is expected to be applied, carry o evaluation and confirmation test with resistors actually mounted on your own board. When t load of more than rated power is applied under the load condition at steady state, it may impaperformance and/or reliability of resistor. Never exceed the rated power.
When the product shall be used under special condition, be sure to ask us in advance.(7)Halogen type (chlorine type, bromine type, etc.) or other high-activity flux is not recommended the residue may affect performance or reliability of resistors.
(8)When soldering with soldering iron, never touch the body of the chip resistor with a tip of t soldering iron. When using a soldering iron with a tip at high temperature, solder for a time short as possible. (Three seconds or less up to 350 °C)
(9)Avoid physical shock to the resistor and nipping of the resistor with hard tool (a pair of pliers tweezers) as it may damage protective firm or the body of resistor and may affect resistor performance.
(10)Reflow soldering method shall apply to this product in principle.

Subject		Spec. No.
Chip Resistor Array	PRODUCT SPECIFICATION FOR INFORMATION	151-EXB-V4V01LE
Part No		

EXBV4V

11. Storage method

If the product is stored in the following environments and conditions, the performance and solderability may be badly affected. Avoid the storage in the following environments.

- (1) Storage in places full of corrosive gases such as sea breeze, Cl₂, H₂S, NH₃, SO₂, and NO_X.
- (2) Storage in places exposed to direct sunlight.
- (3) Storage in places outside the temperature range of 5 °C to 35 °C and humidity range of 45 %RH to 85 %RH.
- (4) Storage over a year after our delivery (This item also applies to the case where the storage method specified in item (1) to (3) has been followed.).

12. Laws and Regulations

- (1) No ODCs or other ozone-depleting substances that are subject to regulation under the Montreal Protocol are used in our manufacturing processes, including in the manufacture of this product.
- (2) This product complies with the RoHS Directive (Restriction of the use of certain Hazardous Substances in electrical and electronic equipment (DIRECTIVE 2002/95/EC)).
- (3) All materials used in this product are existing chemical substances recognized under "lows on examination of chemical substances and regulations of manufacturing and others."
- (4) None of the materials used in this product contain the designated incombustible bromic substances, PBBOs and PBBs.
- (5) Please contact us to obtain a notice as to whether this product has passed inspection under review criteria primarily based on Foreign Exchange and Foreign Trade Control Laws, and appended table in the Export Control Laws.

13. Production Place

Production Country : Japan

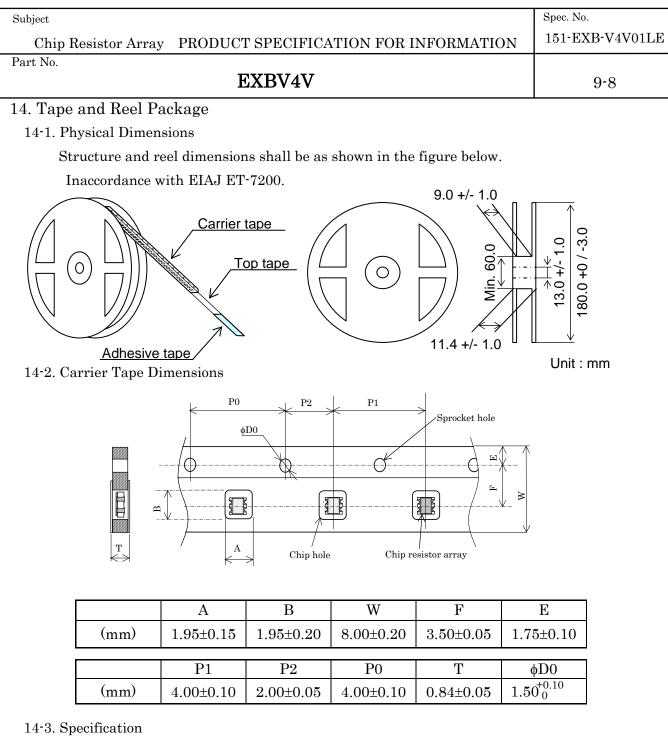
Production Plant : Panasonic Electronic Devices Japan Co., Ltd.

Production Country : China

Production Plant : Panasonic Electronic Devices (Tianjin) Co., Ltd. (PEDTJ)

Production Country : Malaysia

Production Plant : Panasonic Electronic Devices Malaysia Sdn. Bhd. (PEDMA)

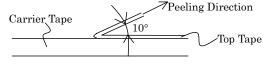


14-3-1. Taping

(1) When the test shall be operated with the below conditions, peel strength should be $0.049\mathrm{N}$

to 0.49N, should not have flash and tear after peeling.

<Test Method>



(2) Minimum Bending Radius

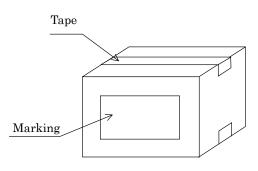
When carrier tape shall be bent by minimum bending radius (15 mm), no defection of chip and no break of carrier tape. However minimum bending radius shall be tested for 1 times.

Subject	Spec. No.
Chip Resistor Array PRODUCT SPECIFICATION FOR INFORMATION	151-EXB-V4V01LE
Part No. EXBV4V	9-9
(3) Resistance to climate	
When resistors shall be exposed at 60 °C \pm 2 °C, 90 %RH to 95 %RH fo	r 120 hours, no
defection of chip and no break off carrier tape.	
When the top tape shall be peeled, tape should not have flash and tear	
14-3-2. Quantity in Taping: 5,000 pcs. / reel	

- (1) Resistor side shall be facing upward.
- (2) Chip resistor shall not be sticking to top tape and bottom tape.
- (3) Chip resistors shall be easy to take out from carrier tape and chip hole or sprocket hole shall not have flash and break.

14-4. Outer Packaging

Quantity: 20 reels(Max.100,000 pcs.)



- (1) When packaging quantity does not reach max quantity, the remaining empty space shall be buried with buffer material.
- (2) When quantity shall be few, alternative packaging methods may used. No problem must occur during the exportation of the product..

14-5. Marking

At last, production country is displayed in English.

• Side of reel (Marking shall be on one side.)

(1)Part name (2)Part number (3)Quantity (4)Lot number (5)Maker name (6)Production country

•Packaging box

(1)Customer name(2)Part name(3)Part number(4)Customer part number(5)Quantity(6)Maker name(7) Production country



Chip Resistor Array PRODUCT SPECIFICATION FOR INFORMATION

Attached Sheet

APPEARANCE QUALITY CRITERIA

1-1

			•
Item	Defect Criteria	Appearance Criteria	Remark
Resin Chipping		$\begin{array}{l} A \leq W/8 \\ B \leq C/2 \end{array}$	Both side chipping shall be judged defect
Terminal Chipping	Through hole terminal chipping WOblique line show chipping	A ≤ 1/2 of radius B ≤ Top terminal width D ≥ 1/2 of radius Through hole's chipping area is within 1/4 of through hole's area.	
Pin Hole		One pin hole / chip resistor φ ≤ 0.2 mm	This item is applied to pin holes which reach to the resistive materials
Flash	$\rightarrow + \overset{A}{\leftarrow}$	A ≤ 100 μm	
		<u> </u>	