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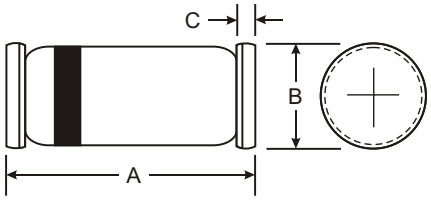
LLSD103C-7

LLSD103A - 103C

SURFACE MOUNT SCHOTTKY BARRIER DIODE

Features

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Fast Reverse Recovery Time
- Low Reverse Capacitance



MiniMELF		
Dim	**Min**	**Max**
A	3.30	3.70
B	1.30	1.60
C	0.28	0.50
All Dimensions in mm		

Mechanical Data

- Case: MiniMELF, Glass
- Terminals: Solderable per MIL-STD-202, Method 208
- Marking: Cathode Band Only
- Polarity: Cathode Band
- Weight: 0.05 grams (approx.)

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	LLSD103A	LLSD103B	LLSD103C	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	40	30	20	V
RMS Reverse Voltage	$V_{R(RMS)}$	28	21	14	V
Forward Continuous Current (Note 1)	I_{FM}	350			mA
Repetitive Peak Forward Current @ t ≤ 1.0s	I_{FRM}	1.0			A
Non-Repetitive Peak Forward Surge Current @ t ≤ 1.0s @ t = 10ms	I_{FSM}	1.5 7.5			A
Power Dissipation (Note 1)	P_d	400			mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	250			°C/W
Operating Temperature Range	T_j	-55 to +125			°C
Storage Temperature Range	T_{STG}	-55 to +150			°C

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop (Note 2)	V_F	—	—	0.37 0.60	V	I_F = 20mA I_F = 200mA
Peak Reverse Current (Note 2) LLSD103A LLSD103B LLSD103C	I_R	—	—	5.0	μA	V_R = 30V V_R = 20V V_R = 10V
Total Capacitance	C_T	—	50	—	pF	V_R = 0V, f = 1.0MHz
Reverse Recovery Time	t_{rr}	—	—	10	ns	$I_F = I_R$ = 50mA to 200mA, I_{rr} = 0.1 x I_R, R_L = 100Ω

Note: 1. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
2. Short duration test pulse used to minimize self-heating effect.

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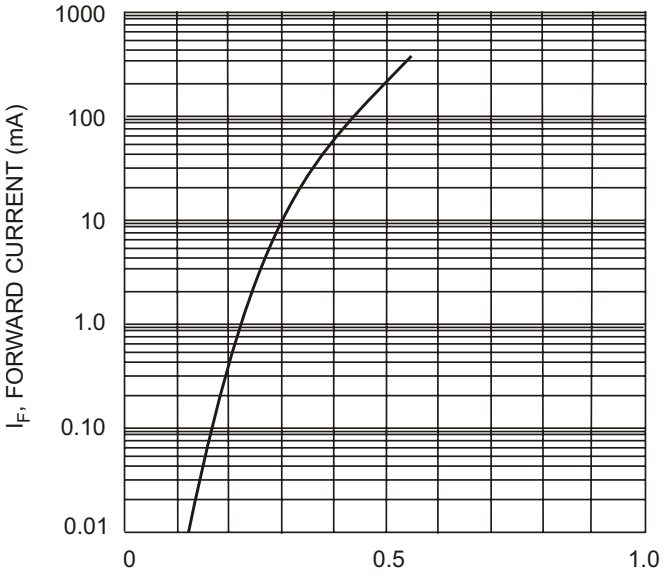


Fig. 1 Typical Forward Characteristics

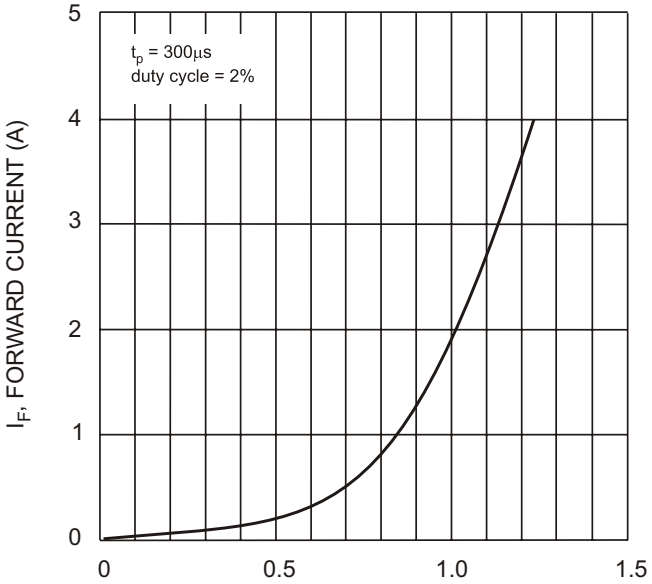


Fig. 2 Typical High Current Fwd Characteristics

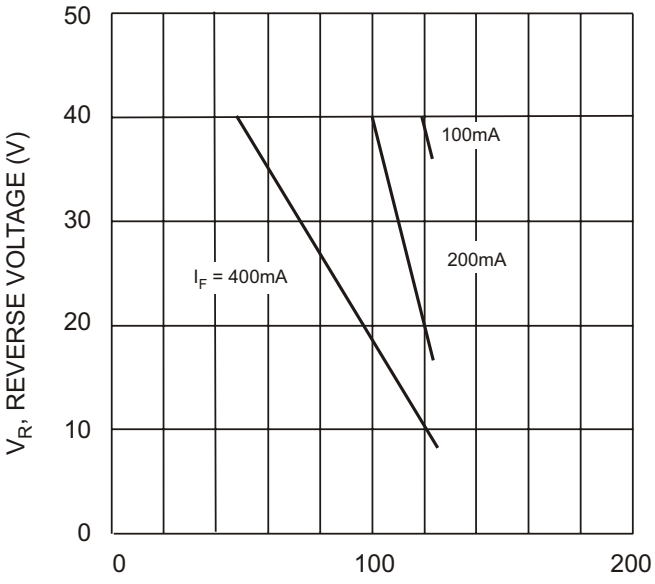


Fig. 3 Blocking Voltage Derating Curves

Ordering Information (Note 3)

Device	Packaging	Shipping
LLSD103A-7	MiniMELF	3000/Tape & Reel
LLSD103A-13		10000/Tape & Reel
LLSD103B-7		3000/Tape & Reel
LLSD103B-13		10000/Tape & Reel
LLSD103C-7		3000/Tape & Reel
LLSD103C-13		10000/Tape & Reel

Notes: 3. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

- View LLSD103C-7 on WIN SOURCE
- Diodes Incorporated Information

Optimize Your Supply Chain with WIN SOURCE Solutions

- Global Sourcing Solution
- Obsolete Management
- Cost Control Management
- Shortage Management
- Alternative Solution
- Excess Inventory Management